TOSHIBA Photocoupler GaAs IRED + Photo-Triac

TLP261J

Triac Drivers Programmable Controllers AC-Output Modules Solid-State Relays

The TOSHIBA mini-flat coupler TLP261J is housed in a small-outline package and suitable for surface-mount assembly.

The TLP261J consists of a gallium arsenide infrared emitting diode optically coupled to a triac-output photocoupler.

Peak off-state voltage: 600 V (min)

• Trigger LED current: 10 mA (max)

• On-state current: 70 mA (max)

Isolation voltage: 3000 Vrms (min)

• Zero-crossing function

• UL-recognized: UL1577, file no. E67349

• Option (V4) type

VDE-approved: EN60747-5-2 satisfied

Maximum operating insulation voltage: 565 VpK

Maximum permissible overvoltage: 6000 Vpk

Note: When an EN60747-5-2 approved type is needed, be sure to specify "Option (V4)".

Construction Mechanical Rating Creepage distance: 4.0 mm (min) Clearance: 4.0 mm (min)

Insulation thickness: 0.4 mm (min)

Unit: mm 7.0 ± 0.4 0.5 MIN. 11-4C **TOSHIBA** 11-4C1

Weight: 0.09 g (typ.)

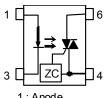
Trigger LED Current

	Trigger LED	Product	
Classification*	V _T =3 V,	Classification	
	Min	Max	Marking
(IFT7)	_	7	T7
Standard	_	10	T7, blank

*E.g. (IFT7): TLP261J (IFT7)

Note: Be sure to use standard product type names when submitting type names for safety certification testing, i.e., TLP261J (IFT7): TLP261J.

Pin Configuration



1: Anode

3: Cathode

4 : Terminal 1

6: Terminal 2

Absolute Maximum Ratings (Ta = 25°C)

	Characteristic		Symbol	Rating	Unit	
	Forward current	lF	50	mA		
	Forward current derating (Ta ≥ 53°C)	ΔI _F / °C	-0.7	mA / °C		
Peak forward current (100 µs pulse, 100 pps)		s)	I _{FP}	1	Α	
	Reverse voltage		V _R	5	V	
Junction temperature			Tj	125	°C	
	Off-state output terminal voltage	V _{DRM}	600	V		
	0 11 5110	Ta = 25°C	I	70	mA	
Detector	On–state RMS current	Ta = 70°C	T(RMS)	40		
	On–state current derating (Ta ≥ 25°C)	ΔI _T / °C	-0.67	mA / °C		
	Peak on-state current (100 µs pulse, 120 p	I _{TP}	2	Α		
	Peak nonrepetitive surge current (PW = 10 ms)	I _{TSM}	1.2	Α		
	Junction temperature		Tj	100	°C	
Storage temperature range			T _{stg}	-55 to 125	°C	
Operating temperature range			T _{opr}	-40 to 100	°C	
Lead soldering temperature (10 s)		T _{sol}	260	°C		
Isola	ation voltage (AC, 1 min., R.H ≤ 60%)	BVS	3000	Vrms		

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: Device considered as a two-terminal device: Pins 1 and 3 shorted together and pins 4 and 6 shorted together.

Recommended Operating Conditions

Characteristic	Symbol	Min	Тур.	Max	Unit
Supply voltage	V _{AC}	_	_	240	V _{ac}
Forward current	lF	15	20	25	mA
Peak on-state current	I _{TP}	_	_	1	Α
Operating temperature	T _{opr}	-25	_	85	°C

Note: Recommended operating conditions are given as a design guideline to obtain expected performance of the devices. Each item also has its own independent guideline document. In developing designs using these products, please confirm the specified characteristics shown in these documents.

Individual Electrical Characteristics (Ta = 25°C)

	Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
	Forward voltage	V _F	I _F = 10 mA	1.0	1.15	1.3	V
LED	Reverse current	I _R	V _R = 5 V	_	_	10	μA
	Capacitance	C _T	V = 0, f = 1 MHz	_	30	_	pF
	Peak off-state current	I _{DRM}	V _{DRM} = 600 V	_	10	1000	nA
	Peak on-state voltage	V _{TM}	I _{TM} = 70 mA	_	1.7	2.8	V
ctor	Holding current	lΗ	_	_	0.6	_	mA
Detector	Critical rate of rise of off–state voltage	dv / dt	V _{in} = 240 Vrms, Ta = 85°C (Fig. 1)	200	500	_	V/µs
	Critical rate of rise of commutating voltage	dv / dt(c)	V _{in} = 60 Vrms, I _T = 15 mA (Fig. 1)	_	0.2	_	V/µs

Coupled Electrical Characteristics (Ta = 25°C)

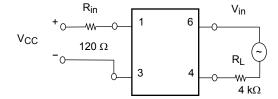
Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Trigger LED current	I _{FT}	V _T = 3 V	_	_	10	mA
Inhibit voltage	V _{IH}	I _F = Rated I _{FT}	_	_	20	V
Leakage in inhibited state	lін	I _F = Rated I _{FT} V _T = Rated V _{DRM}	_	200	600	μΑ
Turn-on time	ton	V_D = 3 \rightarrow 1.5 V, R_L = 20 Ω I_F = rated I_{FT} × 1.5	_	30	100	μs

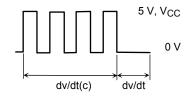
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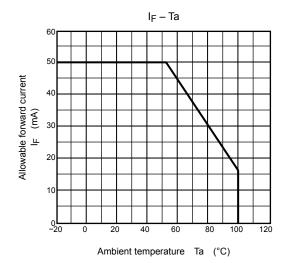
Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Capacitance (input to output)	Cs	V _S = 0, f = 1 MH _Z	_	0.8	_	pF
Isolation resistance	R _S	V _S = 500 V, R.H. ≤ 60%	5×10 ¹⁰	10 ¹⁴	_	Ω
Isolation voltage	BVS	AC, 1 minute	3000	_	_	- V _{rms}
		AC, 1 second, in oil	_	5000	_	
		AC, 1 minute, in oil	_	5000	1	Vdc

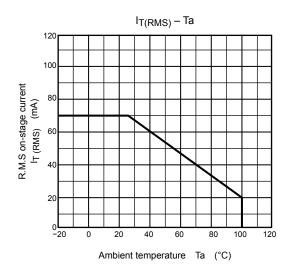
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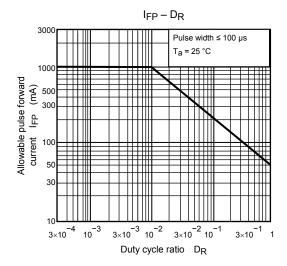
Fig. 1: dv / dt test circuit

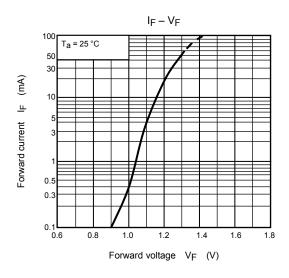


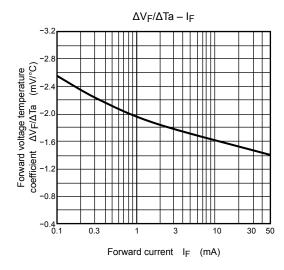


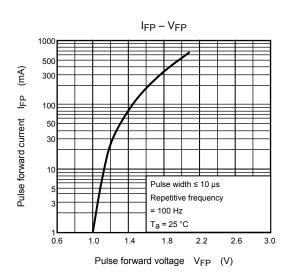


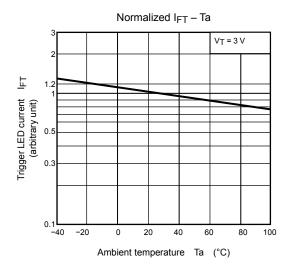


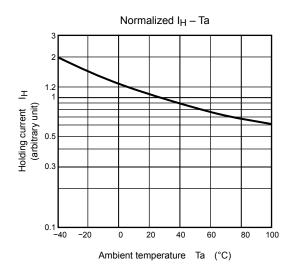


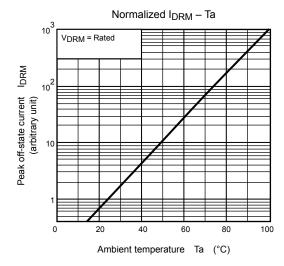


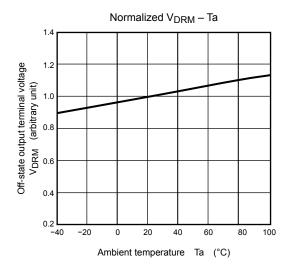


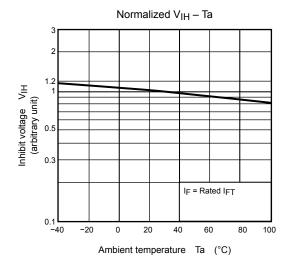


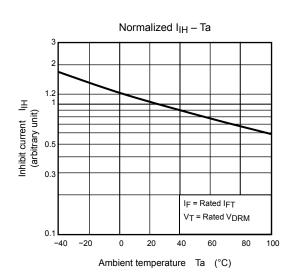












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