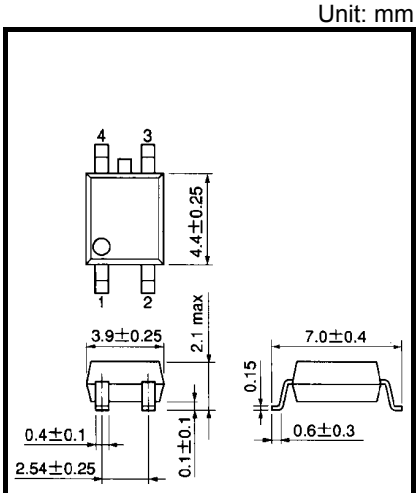
TOSHIBA PHOTOCOUPLER PHOTO RELAY

TLP179D

Measurement Instruments
Logic IC Testers / Memory Testers
Board Testers / Scanners

Unit: mm



JEDEC	—
JEITA	—
TOSHIBA	11−5H1

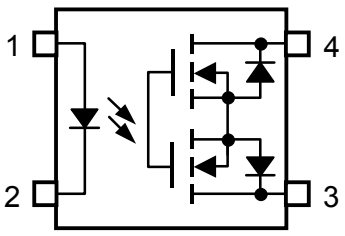
Weight: 0.1 g (typ.)

The TOSHIBA TLP179D Mini-flat photorelay is a small-outline photorelay, suitable for surface-mount assembly. The TLP179D consists of a GaAs infrared-emitting diode optically coupled to a photo-MOS FET and housed in a 4-pin package.
Its characteristics include low OFF-state current and low output pin capacitance, enabling it to be used in high-frequency measurement instruments.

Features

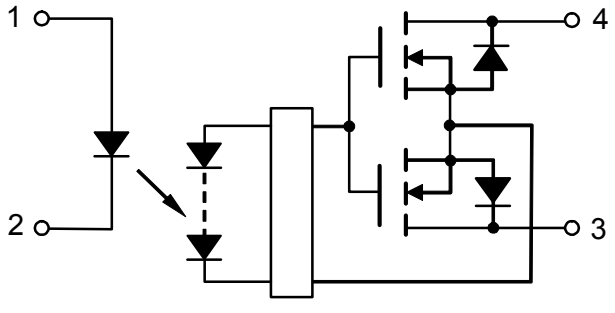
- 4 pin SOP (2.54SOP4) : 2.1 mm high, 2.54 mm pitch
- 1-Form-A
- Peak OFF-State Voltage : 200 V (min)
- Trigger LED Current : 3 mA (max)
- ON-State Current : 50 mA (max)
- ON-State Resistance : 50 Ω (max)
- Output Capacitance : 20 pF (max)
- Isolation Voltage : 1500 Vrms (min)

Pin Configuration (top view)



1 : ANODE
2 : CATHODE
3 : DRAIN
4 : DRAIN

Schematic



Absolute Maximum Ratings (Ta = 25°C)

CHARACTERISTIC		SYMBOL	RATING	UNIT
LED	Forward Current	I_F	50	mA
	Forward Current Derating (Ta ≥ 25°C)	ΔI_F/°C	−0.5	mA/°C
	Reverse Voltage	V_R	5	V
	Junction Temperature	T_j	125	°C
DETECTOR	OFF-State Output Terminal Voltage	V_{OFF}	200	V
	ON-State Current	I_{ON}	50	mA
	ON-State Current Derating (Ta ≥ 25°C)	ΔI_{ON}/°C	−0.5	mA/°C
	Junction Temperature	T_j	125	°C
Storage Temperature Range		T_{stg}	−55 to 125	°C
Operating Temperature Range		T_{opr}	−40 to 85	°C
Lead Soldering Temperature (10 s)		T_{sol}	260	°C
Isolation Voltage (AC, 1 minute, R.H.≤ 60%) (Note1)		BV_S	1500	Vrms

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.
Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook (“Handling Precautions”/“Derating Concept and Methods”) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note1: Device considered a two-terminal device : LED side pins shorted together, and DETECTOR side pins shorted together.

Recommended Operating Conditions

CHARACTERISTIC	SYMBOL	Min	Typ.	Max	UNIT
Supply Voltage	V_{DD}	—	—	160	V
Forward Current	I_F	5	7.5	15	mA
ON-State Current	I_{ON}	—	—	50	mA
Operating Temperature	T_{opr}	−20	—	60	°C

Note: Recommended operating conditions are given as a design guideline to obtain expected performance of the device. Additionally, each item is an independent guideline respectively. In developing designs using this product, please confirm specified characteristics shown in this document.

Individual Electrical Characteristics (Ta = 25°C)

CHARACTERISTIC		SYMBOL	TEST CONDITION	Min	Typ.	Max	UNIT
LED	Forward Voltage	V_F	I_F = 10 mA	1.0	1.15	1.3	V
	Reverse Current	I_R	V_R = 5 V	—	—	10	μA
	Capacitance	C_T	V = 0, f = 1 MHz	—	30	—	pF
DETECTOR	OFF-State Current	I_{OFF}	V_{OFF} = 160 V	—	—	1	nA
	Capacitance	C_{OFF}	V = 0, f = 1 MHz	—	15	20	pF

Coupled Electrical Characteristics (Ta = 25°C)

CHARACTERISTIC	SYMBOL	TEST CONDITION	Min	Typ.	Max	UNIT
Trigger LED Current	I_{FT}	I_{ON} = 50 mA	—	1	3	mA
Return LED Current	I_{FC}	I_{OFF} = 100 μA	0.1	—	—	mA
ON-State Resistance	R_{ON}	I_{ON} = 50 mA, I_F = 5 mA	—	40	50	Ω

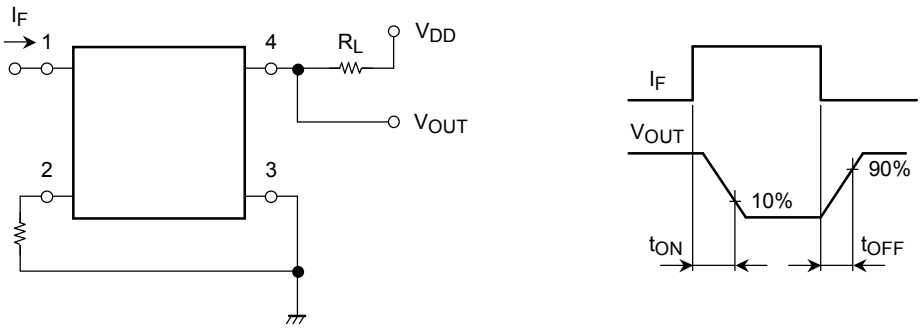
Isolation Characteristics (Ta = 25°C)

CHARACTERISTIC	SYMBOL	TEST CONDITION	Min	Typ.	Max	UNIT
Capacitance Input to Output	C_S	V_S = 0 V, f = 1 MHz	—	0.8	—	pF
Isolation Resistance	R_S	V_S = 500 V, R.H. ≦ 60%	5×10^{10}	10^{14}	—	Ω
Isolation Voltage	BV_S	AC, 1 minute	1500	—	—	Vrms
		AC, 1 second (in oil)	—	3000	—	
		DC, 1 minute (in oil)	—	3000	—	Vdc

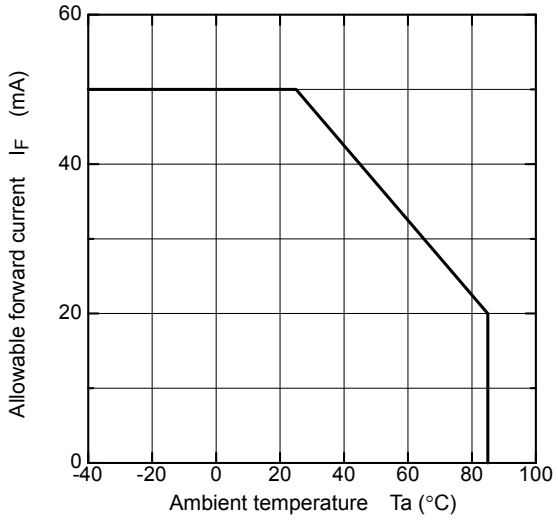
Switching Characteristics (Ta = 25°C)

CHARACTERISTIC	SYMBOL	TEST CONDITION	Min	Typ.	Max	UNIT
Turn-on Time	t_{ON}	R_L = 200 Ω (Note2) V_{DD} = 10 V, I_F = 5 mA	—	0.03	0.5	ms
Turn-off Time	t_{OFF}		—	0.07	0.2	

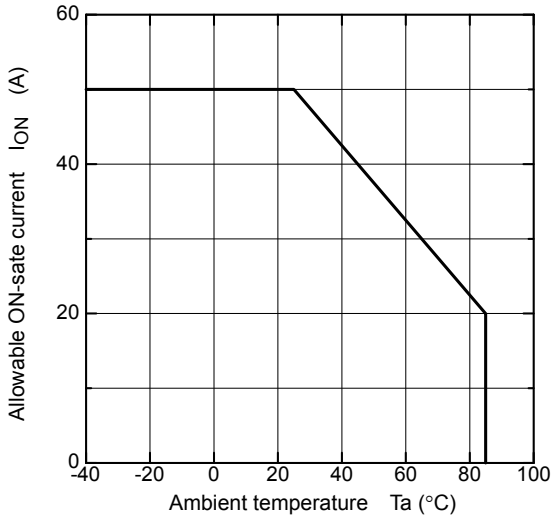
Note2 : Switching Time Test Circuit



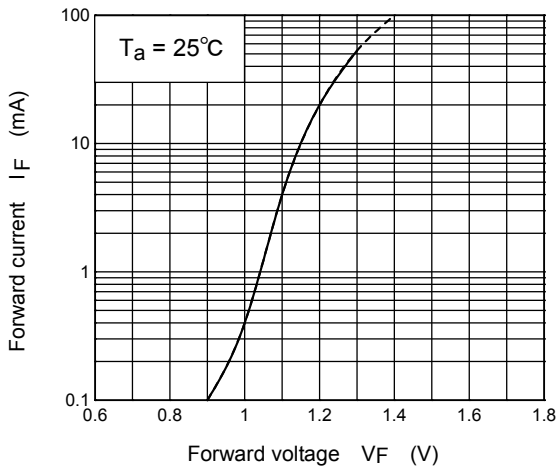
$I_F - Ta$



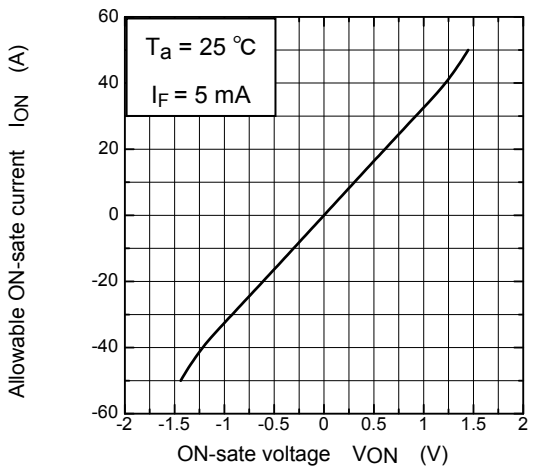
$I_{ON} - Ta$



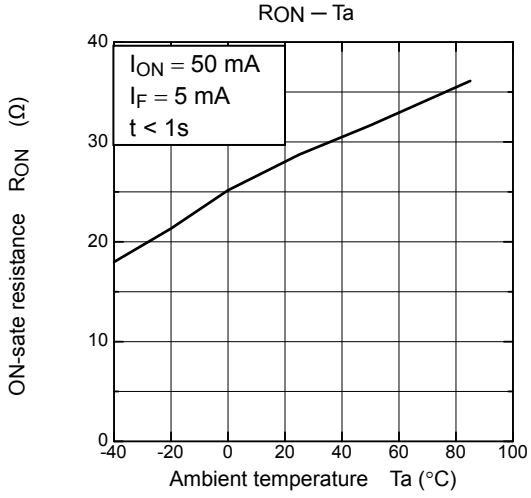
$I_F - V_F$



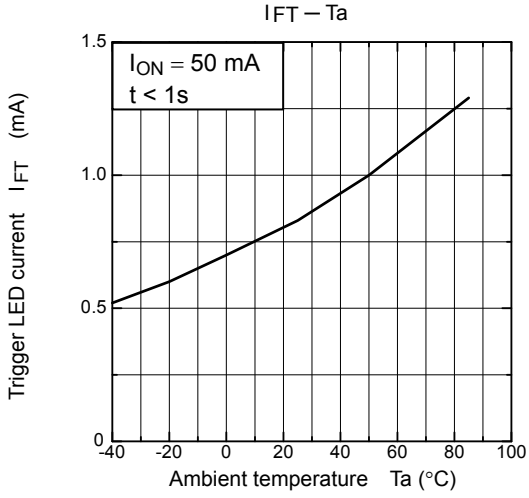
$I_{ON} - V_{ON}$



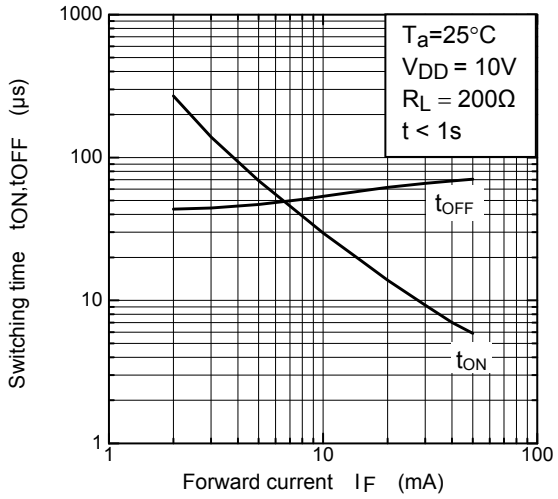
$R_{ON} - Ta$



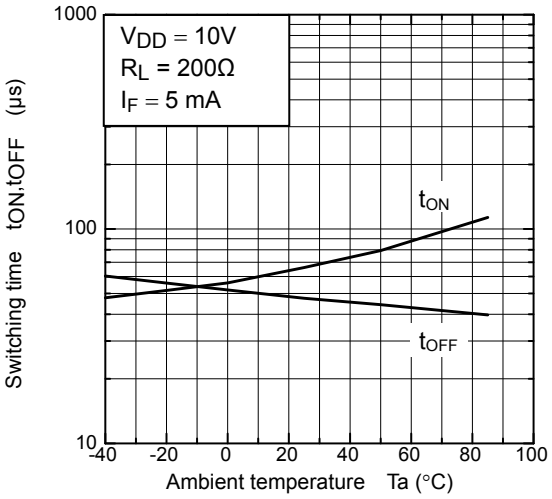
$I_{FT} - Ta$



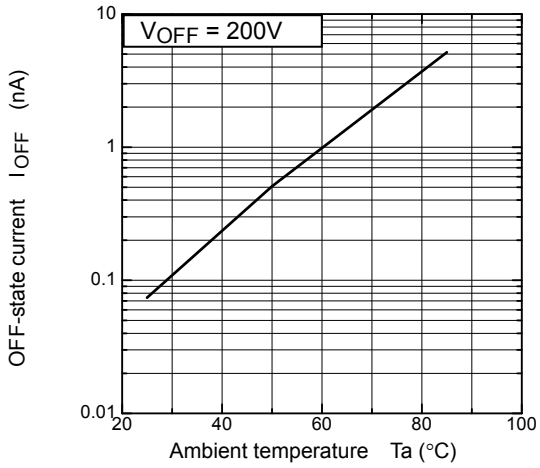
t_{ON},t_{OFF} – I_F



t_{ON},t_{OFF} – Ta



I_{OFF} – Ta



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