TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC74LCX244F,TC74LCX244FT,TC74LCX244FK

Low-Voltage Octal Bus Buffer with 5-V Tolerant Inputs and Outputs

The TC74LCX244 is a high-performance CMOS octal bus buffer. Designed for use in 3.3-V systems, it achieves high-speed operation while maintaining the CMOS low power dissipation.

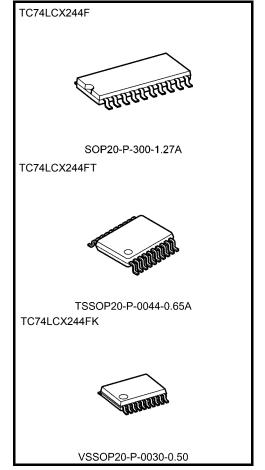
The device is designed for low-voltage (3.3 V) VCC applications, but it could be used to interface to 5 V supply environment for both inputs and outputs.

The 74LCX244F/FT is a non-inverting 3-state buffer having two active-low output enables. This device is designed to be used with 3-state memory address drivers, etc.

All inputs are equipped with protection circuits against static discharge.

Features

- Low-voltage operation: V_{CC} = 1.65 to 3.6 V
- High-speed operation: $t_{pd} = 6.5 \text{ ns (max) (V}_{CC} = 3.0 \text{ to } 3.6 \text{ V)}$
- Ouput current: $|I_{OH}|/I_{OL} = 24 \text{ mA (min) (V}_{CC} = 3.0 \text{ V)}$
- Latch-up performance: ≥ ±500 mA
- Available in JEITA SOP, TSSOP and VSSOP (US)
- Power-down protection provided on all inputs and outputs
- Pin and function compatible with the 74 series (74AC/VHC/HC/F/ALS/LS etc.) 244 type

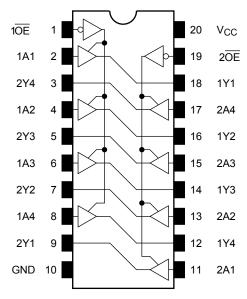


Weight

SOP20-P-300-1.27A : 0.22 g (typ.) TSSOP20-P-0044-0.65A : 0.08 g (typ.) VSSOP20-P-0030-0.50 : 0.03 g (typ.)

Note: The Electrical Characteristics of V_{CC}=1.8±0.15V is only applicable for products which manufactured from January 2009 onward.

Pin Assignment (top view)



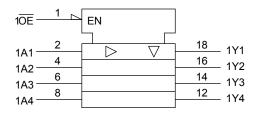
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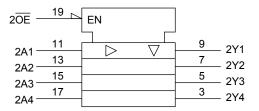
| Inp | uts | Outputs | | |
|-----|-----|---------|--|--|
| ŌĒ | An | Outputs | | |
| L | L | L | | |
| L | Н | Н | | |
| Н | Х | Z | | |

X: Don't care

Z: High impedance

IEC Logic Symbol







Absolute Maximum Ratings (Note 1)

| Characteristics | Symbol | Rating | Unit |
|------------------------------------|-----------------------------------|-------------------------------|------|
| Power supply voltage | Vcc | -0.5 to 7.0 | V |
| DC input voltage | V _{IN} | -0.5 to 7.0 | V |
| | | -0.5 to 7.0 (Note 2) | |
| DC output voltage | V _{OUT} | -0.5 to V _{CC} + 0.5 | V |
| | | (Note 3) | |
| Input diode current | I _{IK} | -50 | mA |
| Output diode current | lok | ±50 (Note 4) | mA |
| DC output current | lout | ±50 | mA |
| Power dissipation | PD | 180 | mW |
| DC V _{CC} /ground current | I _{CC} /I _{GND} | ±100 | mA |
| Storage temperature | T _{stg} | -65 to 150 | °C |

Note 1: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 2: Output in OFF state

Note 3: High or low state. IOUT absolute maximum rating must be observed.

Note 4: $V_{OUT} < GND, V_{OUT} > V_{CC}$

Operating Ranges (Note 1)

| Characteristics | Symbol | Rating | Unit | |
|--------------------------|----------------------------------|-------------------------------|------|--|
| Power supply voltage | V _{CC} | 1.65 to 3.6 | V | |
| Power supply voltage | vCC | 1.5 to 3.6 (Note 2) | V | |
| Input voltage | V _{IN} | 0 to 5.5 | V | |
| Output voltage | \/ | 0 to 5.5 (Note 3) | V | |
| Output voltage | V _{OUT} | 0 to V _{CC} (Note 4) | V | |
| Output current | I _{OH} /I _{OI} | ±24 (Note 5) | mA | |
| Output current | IOH/IOL | ±12 (Note 6) | IIIA | |
| Operating temperature | T _{opr} | -40 to 85 | °C | |
| Input rise and fall time | dt/dv | 0 to 10 (Note 7) | ns/V | |

Note 1: The operating ranges must be maintained to ensure the normal operation of the device. Unused inputs must be tied to either V_{CC} or GND.

Note 2: Data retention only

Note 3: Output in OFF state

Note 4: High or low state

Note 5: $V_{CC} = 3.0 \text{ to } 3.6 \text{ V}$

Note 6: $V_{CC} = 2.7 \text{ to } 3.0 \text{ V}$

Note 7: $V_{IN} = 0.8$ to 2.0 V, $V_{CC} = 3.0$ V



Electrical Characteristics

DC Characteristics (Ta = -40 to 85°C)

| Characterist | ics | Symbol | Test Co | Test Condition | | | Max | Unit | | | | |
|-------------------------------------|---|--|---|---------------------------|------------------|-------------------------|----------------------------------|------------------------|-------------------------|-----|---|-----|
| Ondracterior | | Cymbol | 1001 001 | V _{CC} (V) | | Min | Wax | Offic | | | | |
| | | | | | 1.65 to 2.3 | V _{CC} ×0.9 | _ | | | | | |
| | H-level | V_{IH} | _ | | 2.3 to 2.7 | 1.7 | _ | | | | | |
| Input voltage | | | | | 2.7 to 3.6 | 2.0 | _ | V | | | | |
| input voitage | | | | | 1.65 to 2.3 | | V _{CC} ×0.1 | V | | | | |
| | L-level | V_{IL} | _ | - | 2.3 to 2.7 | | 0.7 | | | | | |
| | | | | | 2.7 to 3.6 | | 0.8 | | | | | |
| | | | | I _{OH} = -100 μA | 1.65 to 3.6 | V _{CC} -0.2 | _ | | | | | |
| | | | | I _{OH} = -4 mA | 1.65 | 1.05 | _ | | | | | |
| | H-level | V | \\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\ | $I_{OH} = -8 \text{ mA}$ | 2.3 | 1.7 | _ | | | | | |
| | n-ievei | V _{OH} | $V_{IN} = V_{IH}$ or V_{IL} | I _{OH} = -12 mA | 2.7 | 2.2 | _ | | | | | |
| | | | | I _{OH} = -18 mA | 3.0 | 2.4 | _ | | | | | |
| | | | | I _{OH} = -24 mA | 3.0 | 2.2 | _ | | | | | |
| Output voltage | | | | I _{OL} = 100 μA | 1.65 to 3.6 | _ | 0.2 | V | | | | |
| | | | $V_{IN} = V_{IH}$ or V_{IL} | I _{OL} = 4 mA | 1.65 | _ | 0.45 | | | | | |
| | Linnel | | | $I_{OL} = 8 \text{ mA}$ | 2.3 | | 0.7 | | | | | |
| | L-level | V _{OL} | | AIN = AIH OL AIL | AIV = AIH OL AIF | VIN = VIH OL VIL | $AIM = AIH \text{ or } AI\Gamma$ | VIN = VIH OI VIL | I _{OL} = 12 mA | 2.7 | _ | 0.4 |
| | | | | ļ | | I _{OL} = 16 mA | 3.0 | _ | 0.4 | | | |
| | | | | I _{OL} = 24 mA | 3.0 | _ | 0.55 | | | | | |
| Input leakage current | | I _{IN} | $V_{IN} = 0$ to 5.5 V | | 1.65 to 3.6 | | ±5.0 | μΑ | | | | |
| 3-state output off-state current | | $V_{IN} = V_{IH}$ or V_{IL} | | 1.65 to 3.6 | | ±5.0 | μА | | | | | |
| | | 02 | V _{OUT} = 0 to 5.5 V | | | | | | | | | |
| Power off leakage curi | ower off leakage current I _{OFF} | | V _{IN} /V _{OUT} = 5.5 V | | 0 | _ | 10.0 | μА | | | | |
| Quiescent supply current | | Icc | V _{IN} = V _{CC} or GND | | 1.65 to 3.6 | _ | 10.0 | | | | | |
| | | V _{IN} /V _{OUT} = 3.6 to 5.5 V | | 1.65 to 3.6 | _ | ±10.0 | μΑ | | | | | |
| Increase in I _{CC} per inp | out | ΔI_{CC} | $V_{IH} = V_{CC} - 0.6V$ | | 2.7 to 3.6 | _ | 500 | | | | | |



AC Characteristics ($Ta = -40 \text{ to } 85^{\circ}\text{C}$)

| Characteristics | Symbol | Test Condition | Test Condition V _{CC} (V) | | Max | Unit |
|------------------------|--------------------------------------|--------------------|------------------------------------|-----|------|------|
| | | | 1.8 ± 0.15 | _ | 25.0 | ns . |
| Propagation delay time | t _{pLH} | Figure 1 Figure 2 | 2.5 ± 0.2 | _ | 8.5 | |
| Propagation delay time | t _{pHL} | Figure 1, Figure 2 | 2.7 | _ | 7.5 | |
| | | | 3.3 ± 0.3 | 1.5 | 6.5 | |
| | | | 1.8 ± 0.15 | _ | 32.0 | - ns |
| Output enable time | t _{pZL} t _{pZH} | Figure 1, Figure 3 | 2.5 ± 0.2 | | 16.0 | |
| Output enable time | | | 2.7 | | 9.0 | |
| | | | 3.3 ± 0.3 | 1.5 | 8.0 | |
| | | | 1.8 ± 0.15 | _ | 30.0 | |
| Outrout disable times | t _{pLZ} t _{pHZ} | Figure 1, Figure 3 | 2.5 ± 0.2 | | 15.0 | |
| Output disable time | | | 2.7 | _ | 8.0 | ns |
| | | | 3.3 ± 0.3 | 1.5 | 7.0 | |
| Output to output skew | t _{osLH} | (Note) | 2.7 | _ | | ns |
| Output to output skew | t _{osHL} | (NOIE) | 3.3 ± 0.3 | _ | 1.0 | 110 |

Note: Parameter guaranteed by design.

 $(t_{OSLH} = |t_{PLHm} - t_{PLHn}|, t_{OSHL} = |t_{PHLm} - t_{PHLn}|)$

Dynamic Switching Characteristics (Ta = 25°C, input: $t_r = t_f = 2.5$ ns, $C_L = 50$ pF, $R_L = 500$ Ω)

| Characteristics | Symbol | Test Condition | V _{CC} (V) | Тур. | Unit |
|--|------------------|--|---------------------|------|------|
| Quiet output maximum dynamic V _{OL} | V _{OLP} | $V_{IH} = 3.3 \text{ V}, V_{IL} = 0 \text{ V}$ | 3.3 | 8.0 | V |
| Quiet output minimum dynamic V _{OL} | V _{OLV} | V _{IH} = 3.3 V, V _{IL} = 0 V | 3.3 | 0.8 | V |

Capacitive Characteristics (Ta = 25°C)

| Characteristics | Symbol | Test Condition | V _{CC} (V) | Тур. | Unit |
|-------------------------------|------------------|--------------------------------|---------------------|------|------|
| Input capacitance | C _{IN} | _ | 3.3 | 7 | pF |
| Output capacitance | C _{OUT} | _ | 3.3 | 8 | pF |
| Power dissipation capacitance | C _{PD} | $f_{IN} = 10 \text{ MHz}$ (Not | 9) 3.3 | 25 | pF |

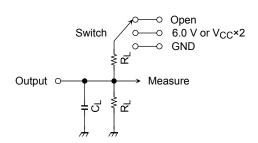
Note: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption.

Average operating current can be obtained by the equation:

 $I_{CC (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/8 \text{ (per bit)}$



AC Test Circuit



| Parameter | Switch | | |
|-------------------------------------|--------------------|------------------------------|--|
| t _{pLH} , t _{pHL} | | Open | |
| | 6.0 V | @ V _{CC} =3.3±0.3V | |
| t t | | @ V _{CC} =2.7V | |
| t _{pLZ} , t _{pZL} | V _{CC} ×2 | @ V _{CC} =2.5±0.2V | |
| | | @ V _{CC} =1.8±0.15V | |
| t _{pHZ} , t _{pZH} | GND | | |

Figure 1

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AC Waveform

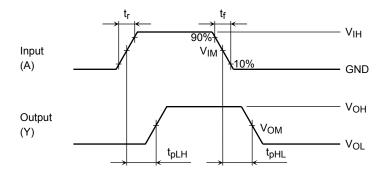


Figure 2 t_{pLH}, t_{pHL}

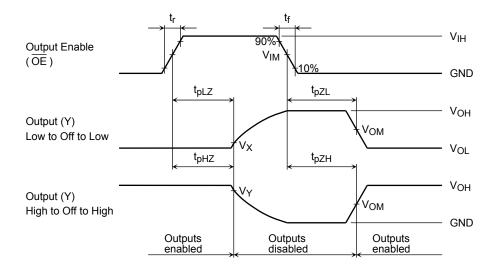


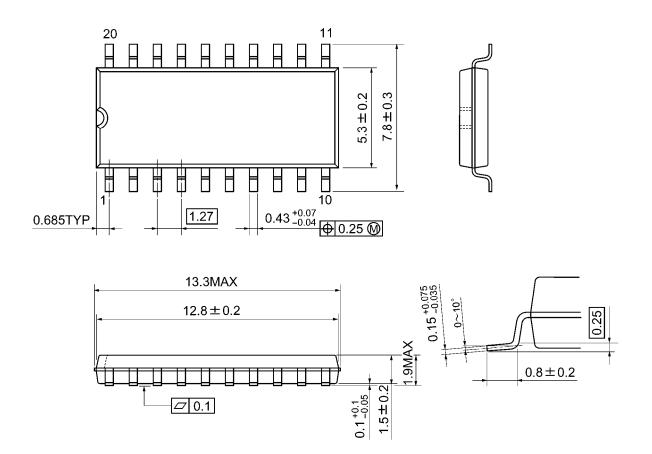
Figure 3 t_{pLZ} , t_{pHZ} , t_{pZL} , t_{pZH}

| | | Vcc | | | | |
|--------|-----------------|------------------------|------------------------|------------------------|--|--|
| | Symbol | $3.3\pm0.3~\textrm{V}$ | 25 + 027 | 4.0 + 0.45 \/ | | |
| | | 2.7V | 2.5 ± 0.2 V | 1.8 ± 0.15 V | | |
| Input | V _{IH} | 2.7V | V _{CC} | V _{CC} | | |
| | V _{IM} | 1.5V | V _{CC} /2 | V _{CC} /2 | | |
| | tr,tf | 2.5ns | 2.0ns | 2.0ns | | |
| Output | V _{OM} | 1.5V | V _{OH} /2 | V _{OH} /2 | | |
| | VX | V _{OL} +0.3V | V _{OL} +0.15V | V _{OL} +0.15V | | |
| | V _Y | V _{OH} -0.3V | V _{OH} -0.15V | V _{OH} -0.15V | | |
| Load | CL | 50pF | 30pF | 30pF | | |
| | RL | 500Ω | 500Ω | 1kΩ | | |



Package Dimensions

SOP20-P-300-1.27A Unit: mm

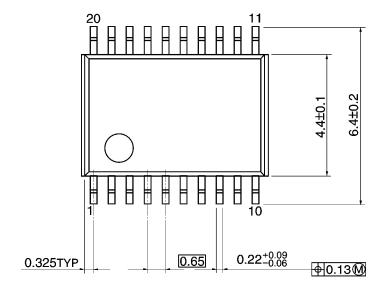


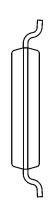
Weight: 0.22 g (typ.)

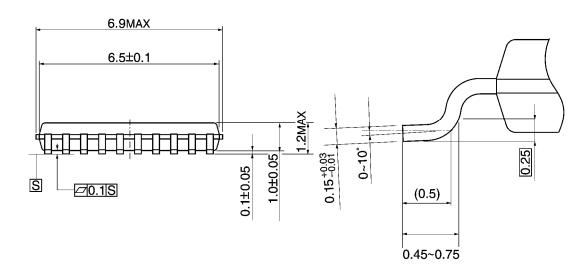
Package Dimensions

TSSOP20-P-0044-0.65A

Unit: mm



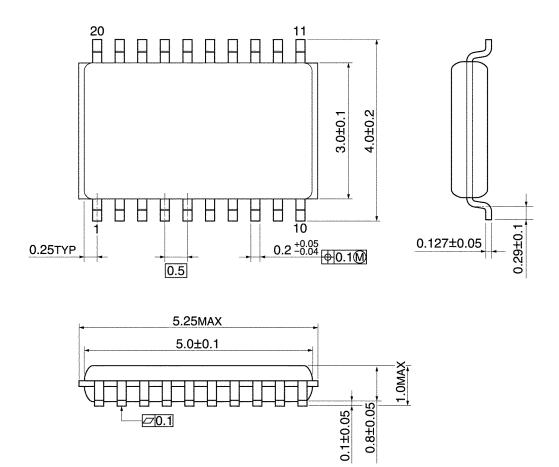




Weight: 0.08 g (typ.)

Package Dimensions

VSSOP20-P-0030-0.50 Unit: mm



Weight: 0.03 g (typ.)

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