TOSHIBA Field Effect Transistor Silicon N Channel MOS Type  $(\pi - MOSVII)$ 

# **TK11A55D**

### **Switching Regulator Applications**

Unit: mm

Low drain-source ON-resistance: R<sub>DS</sub> (ON) = 0.52 Ω (typ.)

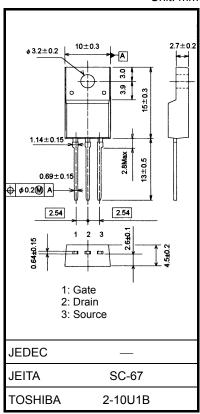
• High forward transfer admittance:  $|Y_{fS}| = 6.0 \text{ S (typ.)}$ 

• Low leakage current:  $I_{DSS} = 10 \mu A \text{ (max) (V}_{DS} = 550 \text{ V)}$ 

• Enhancement mode:  $V_{th}$  = 2.0 to 4.0 V ( $V_{DS}$  = 10 V,  $I_D$  = 1 mA)

### **Absolute Maximum Ratings (Ta = 25°C)**

Characteristics		Symbol	Rating	Unit
Drain-source voltage		$V_{DSS}$	550	V
Gate-source voltage		$V_{GSS}$	±30	V
Drain current	DC (Note 1)	I <sub>D</sub>	11	Α
	Pulse (Note 1)	I <sub>DP</sub>	44	A
Drain power dissipation (Tc = 25°C)		P <sub>D</sub>	45	W
Single pulse avalanche energy (Note 2)		E <sub>AS</sub>	276	mJ
Avalanche current		I <sub>AR</sub>	11	Α
Repetitive avalanche energy (Note 3)		E <sub>AR</sub>	4.5	mJ
Channel temperature		T <sub>ch</sub>	150	°C
Storage temperature range		T <sub>stg</sub>	-55 to 150	°C



Weight: 1.7 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

### **Thermal Characteristics**

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R <sub>th (ch-c)</sub>	2.78	°C/W
Thermal resistance, channel to ambient	R <sub>th (ch-a)</sub>	62.5	°C/W

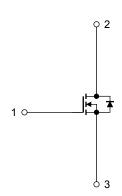
Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2:  $V_{DD}$  = 90 V,  $T_{ch}$  = 25°C (initial), L = 3.94 mH,  $R_G$  = 25  $\Omega$ ,  $I_{AR}$  = 11 A

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device. Handle with care.

Internal Connection



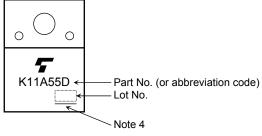
# Electrical Characteristics (Ta = 25°C)

Chara	acteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cur	rent	I <sub>GSS</sub>	$V_{GS} = \pm 30 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±1	μΑ
Drain cut-off current		I <sub>DSS</sub>	V <sub>DS</sub> = 550 V, V <sub>GS</sub> = 0 V	_	_	10	μА
Drain-source bre	akdown voltage	V (BR) DSS	I <sub>D</sub> = 10 mA, V <sub>GS</sub> = 0 V	550	_	_	V
Gate threshold vo	oltage	V <sub>th</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 mA	2.0	_	4.0	V
Drain-source ON	-resistance	R <sub>DS</sub> (ON)	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 5.5 A	_	0.52	0.63	Ω
Forward transfer	admittance	Y <sub>fs</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 5.5 A	1.5	6.0	_	S
Input capacitance		C <sub>iss</sub>		_	1350	_	
Reverse transfer capacitance		C <sub>rss</sub>	$V_{DS} = 25 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$	_	6	_	pF
Output capacitance		Coss		_	135	_	
Switching time	Rise time	t <sub>r</sub>	$I_D = 5.5 \text{ A } V_{OUT}$		22	_	. ns
	Turn-on time	t <sub>on</sub>		_	55	_	
	Fall time	t <sub>f</sub>		_	15	_	
	Turn-off time	t <sub>off</sub>	Duty $\leq$ 1%, $t_W = 10 \mu s$	_	100	_	
Total gate charge		Qg		_	25	_	
Gate-source charge		Q <sub>gs</sub>	$V_{DD} \approx 400 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 11 \text{ A}$	_	16	_	nC
Gate-drain charge		Q <sub>gd</sub>		_	9		

### Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I <sub>DR</sub>	_	_	_	11	Α
Pulse drain reverse current (Note 1)	I <sub>DRP</sub>	_	_	_	44	Α
Forward voltage (diode)	$V_{DSF}$	I <sub>DR</sub> = 11 A, V <sub>GS</sub> = 0 V	_	_	-1.7	V
Reverse recovery time	t <sub>rr</sub>	I <sub>DR</sub> = 11 A, V <sub>GS</sub> = 0 V,	_	1300	_	ns
Reverse recovery charge	Q <sub>rr</sub>	dl <sub>DR</sub> /dt = 100 A/μs		12	_	μС

## Marking

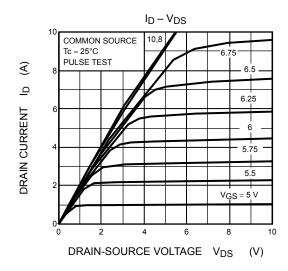


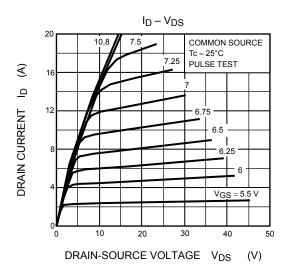
Note 4: A line under a Lot No. identifies the indication of product Labels.

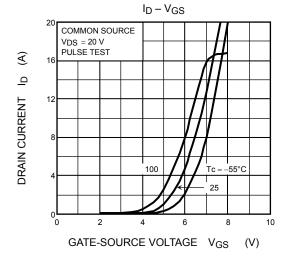
Not underlined: [[Pb]]/INCLUDES > MCV

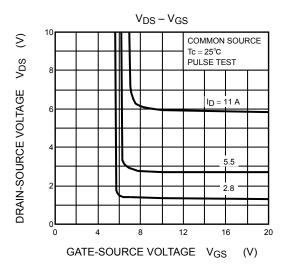
Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

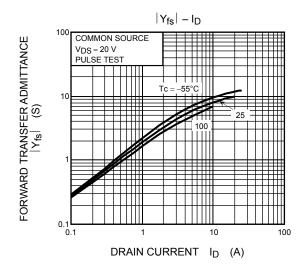
Part No. (or abbreviation code)
Lot No.
Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS compatibility of Product.
The RoHS is the Directive 2002/95/EC of the European Parliament and of the Council of 27 January 2003 on the restriction of the use of certain hazardous substances in electrical and electronic equipment.

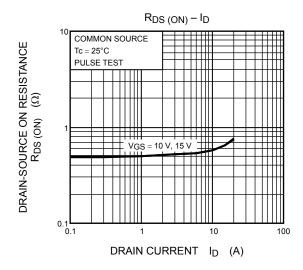




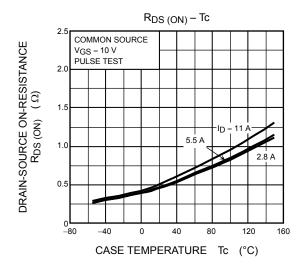


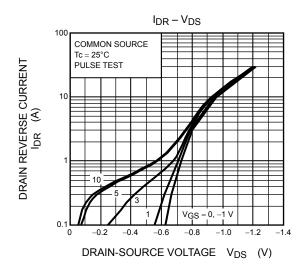


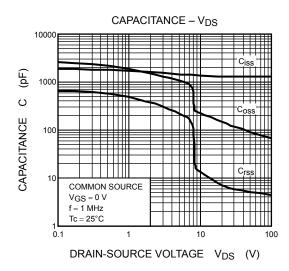


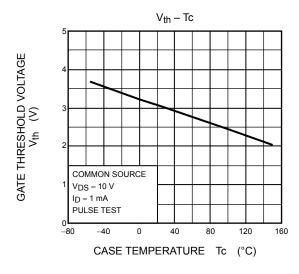


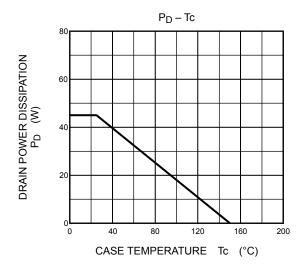
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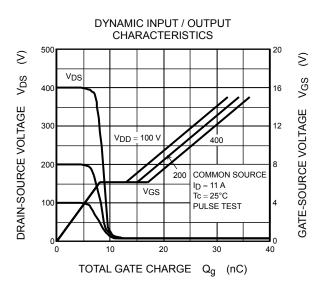


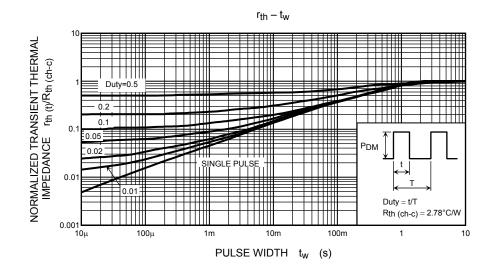


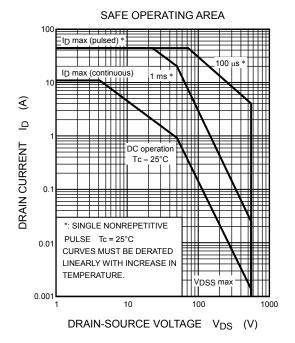


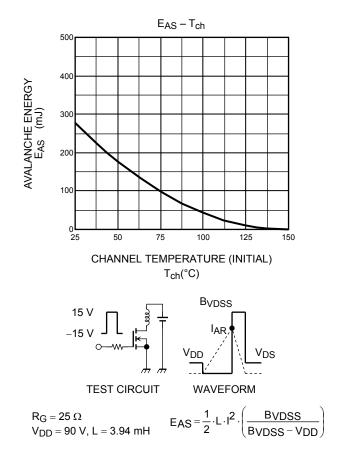












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