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TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (π -MOSVII)

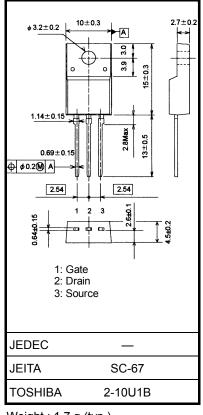
TK6A60D

Switching Regulator Applications

- Low drain-source ON-resistance: $RDS(ON) = 1.0 \Omega$ (typ.)
- High forward transfer admittance: $|Y_{fs}| = 3.0 \text{ S}$ (typ.)
- Low leakage current: $I_{DSS} = 10 \ \mu A \ (max) \ (V_{DS} = 600 \ V)$
- Enhancement mode: $V_{th} = 2.0$ to 4.0 V ($V_{DS} = 10$ V, $I_D = 1$ mA)

320 (1.2 20)								
Characteristics			Symbol	Rating	Unit			
Drain-source voltage			V _{DSS}	600	V			
Gate-source voltage			V _{GSS}	±30	V			
Drain current	DC (Note 1)	۱ _D	6	А			
	Pulse (Note 1)	I _{DP}	24	~			
Drain power dissipation (Tc = 25° C)			PD	40	W			
Single pulse avalanche energy (Note 2)			E _{AS}	173	mJ			
Avalanche current			I _{AR}	6	А			
Repetitive avalanche energy (Note 3)			E _{AR}	4.0	mJ			
Channel temperature			T _{ch}	150	°C			
Storage temperature range			T _{stg}	-55 to 150	°C			

Absolute Maximum Ratings (Ta = 25°C)



Weight : 1.7 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc.).

Thermal Characteristics

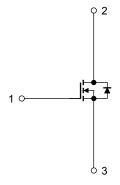
Characteristics	Symbol	Max	Unit	
Thermal resistance, channel to case	R _{th (ch-c)}	3.125	°C/W	
Thermal resistance, channel to ambient	R _{th (ch-a)}	62.5	°C/W	

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: $V_{DD} = 90 \text{ V}, \text{ T}_{ch} = 25^{\circ}\text{C}$ (initial), L = 8.4 mH, R_G = 25 Ω , I_{AR} = 6 A

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device. Handle with care.



Unit: mm

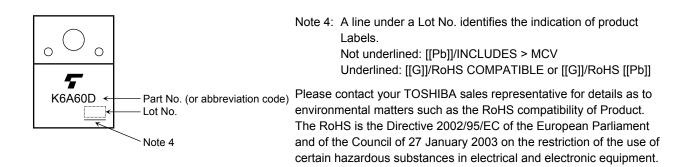
Electrical Characteristics (Ta = 25°C)

Char	acteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current		I _{GSS}	$V_{GS}=\pm 30~V,~V_{DS}=0~V$	_		±1	μA
Drain cut-off curr	ent	I _{DSS}	$V_{DS} = 600 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$			10	μA
Drain-source bre	akdown voltage	V (BR) DSS	$I_D = 10 \text{ mA}, V_{GS} = 0 \text{ V}$	600			V
Gate threshold v	oltage	V _{th}	$V_{DS} = 10 \text{ V}, \text{ I}_{D} = 1 \text{ mA}$	2.0		4.0	V
Drain-source ON	-resistance	R _{DS (ON)}	$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 3 \text{ A}$		1.0	1.25	Ω
Forward transfer admittance		Y _{fs}	$V_{DS} = 10 \text{ V}, \text{ I}_{D} = 3 \text{ A}$	0.8	3.0	_	S
Input capacitance		C _{iss}			800	_	
Reverse transfer capacitance		C _{rss}	$V_{DS} = 25 \text{ V}, \text{ V}_{GS} = 0 \text{ V}, \text{ f} = 1 \text{ MHz}$		4	_	pF
Output capacitance		C _{oss}			100		
Switching time	Rise time	tr	V_{GS} $I_D = 3 A V_{OUT}$		20	_	- ns
	Turn-on time	t _{on}	$\begin{bmatrix} 0 \\ 0 \\ 0 \\ 0 \\ 0 \\ 0 \\ 0 \\ 0 \\ 0 \\ 0 $		40		
	Fall time	t _f			12		
	Turn-off time	t _{off}	$v_{DD} \approx 200 v$ Duty $\leq 1\%$, t _w = 10 µs	_	60	—	
Total gate charge		Qg		_	16		
Gate-source charge		Q _{gs}	$V_{DD} \approx 400 \text{ V}, \text{ V}_{GS} = 10 \text{ V}, \text{ I}_{D} = 6\text{A}$	_	10	—	nC
Gate-drain charge		Q _{gd}]	_	6	—	

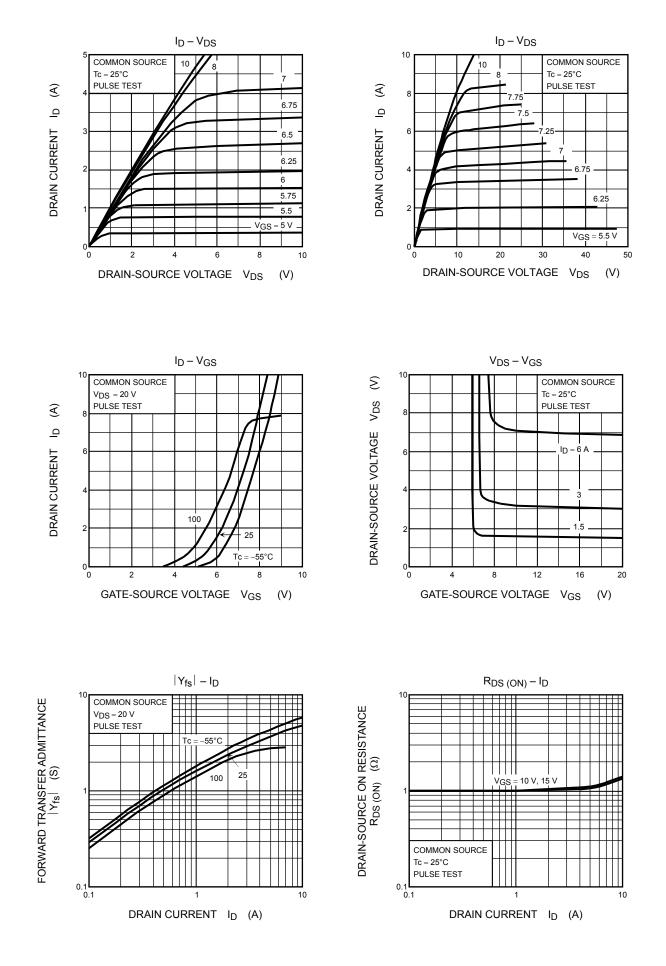
Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I _{DR}	—	_	_	6	А
Pulse drain reverse current (Note 1)	I _{DRP}	—	_	_	24	А
Forward voltage (diode)	V _{DSF}	$I_{DR} = 6 \text{ A}, V_{GS} = 0 \text{ V}$	_	_	-1.7	V
Reverse recovery time	t _{rr}	$I_{DR} = 6 \text{ A}, V_{GS} = 0 \text{ V},$	_	1200	_	ns
Reverse recovery charge	Qrr	dl _{DR} /dt = 100 A/μs		10	_	μC

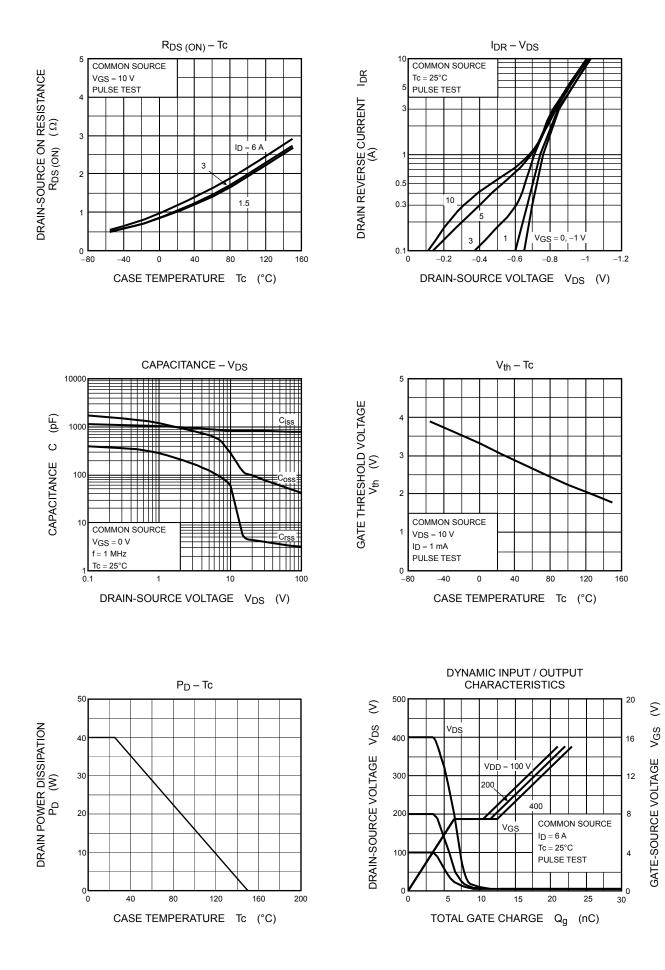
Marking

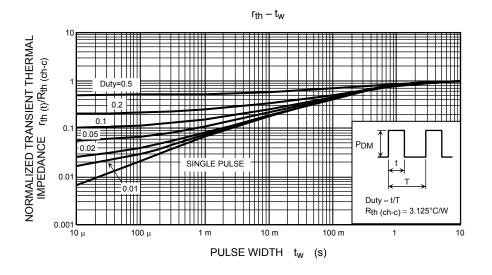


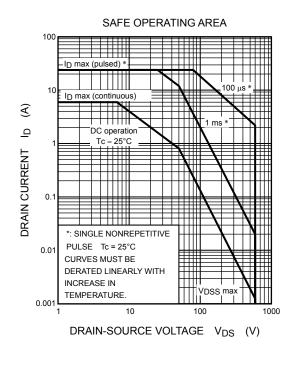
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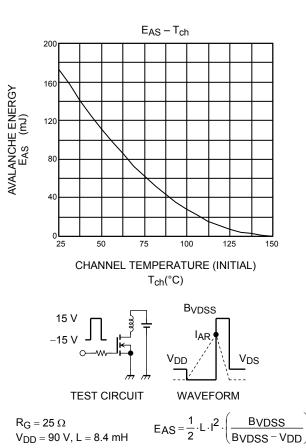


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