TOSHIBA Bipolar Linear Integrated Circuit Silicon Monolithic

# TA58M05S,TA58M06S,TA58M08S,TA58M09S TA58M10S,TA58M12S,TA58M15S

## 500 mA Low Dropout Voltage Regulator

The TA58M\*\*S Series consists of fixed-positive-output, low-dropout regulators with an output current of 500 mA (max) that utilize PNP transistors for the output stage. Low dropout voltage and standby current make the TA58M\*\*S Series suitable for applications requiring low power consumption.

# HSIP3-P-2.54A

Weight: 1.7 g (typ.)

#### **Features**

Maximum output current : 500 mA

Output voltage : 5 / 6 / 8 / 9 / 10 / 12 / 15 V
 Output voltage accuracy : VOUT ± 3% (@T<sub>i</sub> = 25°C)

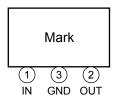
• Low-dropout voltage : 0.65 V (max) (@I<sub>OUT</sub> = 500 mA)

• Protection function : Overcurrent protection / overheating protection /

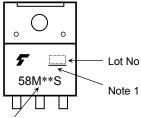
Reverse connection of power supply / 60 V load dump

• Package type : TO-220NIS

# **Pin Assignment**



### Marking



Part No. (or abbreviation code)

Note 1: A line under a Lot No. identifies the indication of product Labels.

Not underlined: [[Pb]]/INCLUDES > MCV

Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS compatibility of Product. The RoHS is the Directive 2002/95/EC of the European Parliament and of the Council of 27 January 2003 on the restriction of the use of certain hazardous substances in electrical and electronic equipment.

Note 2: The "\*\*" in each product name is replaced with the output voltage of each product.

The product(s) in this document ("Product") contain functions intended to protect the Product from temporary small overloads such as minor short-term overcurrent, overvoltage, or overheating. The protective functions do not necessarily protect Product under all circumstances. When incorporating Product into your system, please design the system (1) to avoid such overloads upon the Product, and (2) to shut down or otherwise relieve the Product of such overload conditions immediately upon occurrence. For details, please refer to the notes appearing below in this document and other documents referenced in this document.

## **Pin Description**

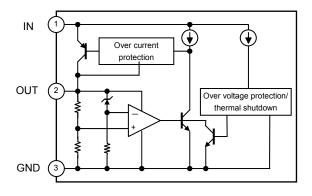
Pin No.	Symbol	Description
1	IN	Input terminal. Connected by capacitor (C <sub>IN</sub> ) to GND.
3	GND	Ground terminal
2	OUT	Output terminal. Connected by capacitor (C <sub>OUT</sub> ) to GND.

## **How to Order**

Product No.	Package	Package Type and Capacity
TA58M** S(Q) (Note 3)	TO-220NIS	Sack (50 pcs./sack)

Note 3: The "\*\*" in each product number is replaced with the output voltage of each product.

#### **Block Diagram**



2



# Absolute Maximum Rating (Ta = 25°C)

Chara	acteristic	Symbol	Rating	Unit
Input voltage	DC	V <sub>IN (DC)</sub>	29	V
input voltage	Pulse	V <sub>IN (Pulse)</sub>	60 ( <i>τ</i> =200 ms)	٧
Output current		l <sub>OUT</sub> 500		mA
Operating tempera	ture	T <sub>opr</sub>	T <sub>opr</sub> –40 to 105	
Junction temperate	ıre	T <sub>j</sub> 150		°C
Storage temperatu	re	T <sub>stg</sub>	–55 to 150	°C
Power dissipation	Ta = 25°C		2	W
	Tc = 25°C	P <sub>D</sub> 20		VV

Note 4: Do not apply current and voltage (including reverse polarity) to any pin that is not specified.

Note 5: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

#### **Thermal Characteristics**

Characteristic	Symbol	Max	Unit
Thermal resistance, junction to ambient	R <sub>th (j−a)</sub>	62.5	°C/W
Thermal resistance, junction to case	R <sub>th (j-c)</sub>	6.25	°C/W

# **Protection Function (Reference)**

Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Thermal shutdown	T <sub>SD</sub>	V <sub>IN</sub> = 14 V (05 to 06S)/ 16 V (08 to 10S)/ 18 V (12S)/ 20 V (15S)	_	170	_	°C
Thermal shutdown hysteresis width	T <sub>SD(hys)</sub>		_	20	_	°C
Peak circuit current	I <sub>PEAK</sub>	$V_{IN} =$ 14 V (05 to 06S)/ 16 V (08 to 10S)/ 18 V (12S)/ 20 V (15S), $T_j =$ 25°C	_	1	_	Α
Short circuit current	I <sub>SC</sub>	$V_{IN} =$ 14 V (05 to 06S)/ 16 V (08 to 10S)/ 18 V (12S)/ 20 V (15S), $T_j =$ 25°C	_	600	_	mA
Overvoltage protection	V <sub>IN</sub>	$T_j = 25^{\circ}C$	29	33	_	V

Note 6: Ensure that the devices operate within the limits of the maximum rating when in actual use.

Note 7: When the input voltage exceeds 29 V, the overvoltage protection circuit is activated to turn off the output voltage.



# TA58M05S Electrical Characteristics (unless otherwise specified, $T_j$ = 25°C)

Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
		V <sub>IN</sub> = 14 V, I <sub>OUT</sub> = 250 mA	4.85	5.00	5.15	
Output voltage	V <sub>OUT</sub>		4.8	5.0	5.2	V
Line regulation	Reg·line	9 V $\leq$ V <sub>IN</sub> $\leq$ 16 V, I <sub>OUT</sub> = 250 mA	_	2	20	mV
Line regulation		$6~V \leq V_{IN} \leq 26~V,~I_{OUT} = 250~mA$	_	5	30	IIIV
Load regulation	Reg·load	$V_{IN} = 14 \text{ V}, \text{ 5 mA} \leq I_{OUT} \leq 500 \text{ mA}$	_	20	60	mV
Quiescent current	1_	$6~V \leq V_{IN} \leq 26~V,~I_{OUT} = 0~A$	_	0.5	1.0	mA
Quiescent current	lΒ	$6~V \leq V_{IN} \leq 26~V,~I_{OUT} = 500~mA$	_	35	80	IIIA
Downstand	\/-	I <sub>OUT</sub> = 250 mA	_	0.22	0.35	V
Dropout voltage	$V_D$	I <sub>OUT</sub> = 500 mA	_	0.42	0.65	v

# TA58M06S Electrical Characteristics (unless otherwise specified, $T_j = 25$ °C)

Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
		V <sub>IN</sub> = 14 V, I <sub>OUT</sub> = 250 mA	5.82	6.00	6.18	
Output voltage	Vout	$ 7 \text{ V} \leq \text{V}_{IN} \leq 26 \text{ V}, \text{ I}_{OUT} = 250 \text{ mA}, \\ -40^{\circ}\text{C} \leq \text{T}_{j} \leq 105^{\circ}\text{C} $	5.76	6.00	6.24	V
Line regulation	Reg·line	$10 \text{ V} \le V_{IN} \le 17 \text{ V}, I_{OUT} = 250 \text{ mA}$	_	2	20	mV
Line regulation	Regime	$7 \text{ V} \le V_{IN} \le 26 \text{ V}, I_{OUT} = 250 \text{ mA}$	_	5	30	IIIV
Load regulation	Reg·load	$V_{IN} = 14 \text{ V}, 5 \text{ mA} \le I_{OUT} \le 500 \text{ mA}$	_	20	60	mV
Quiescent current	1_	$7 \text{ V} \le V_{IN} \le 26 \text{ V}, I_{OUT} = 0 \text{ A}$	_	0.5	1.0	mA
Quiescent current	lΒ	$7 \text{ V} \le V_{IN} \le 26 \text{ V}, I_{OUT} = 500 \text{ mA}$	_	35	80	IIIA
Dropout voltage	\/-	I <sub>OUT</sub> = 250 mA	_	0.22	0.35	V
Dropout voltage	V <sub>D</sub>	I <sub>OUT</sub> = 500 mA	_	0.42	0.65	٧

4



# TA58M08S Electrical Characteristics (unless otherwise specified, $T_j = 25$ °C)

Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
		V <sub>IN</sub> = 16 V, I <sub>OUT</sub> = 250 mA	7.76	8.00	8.24	
Output voltage	V <sub>OUT</sub>	$\begin{array}{c} 9 \ V \leq V_{IN} \leq 26 \ V, \ I_{OUT} = 250 \ mA, \\ -40 ^{\circ}C \leq T_{j} \leq 105 ^{\circ}C \end{array}$	7.68	8.00	8.32	V
Line regulation	Reg·line	$12 \text{ V} \le V_{IN} \le 19 \text{ V}, I_{OUT} = 250 \text{ mA}$	_	2	20	mV
Line regulation		$9 \text{ V} \le V_{IN} \le 26 \text{ V}, I_{OUT} = 250 \text{ mA}$	_	5	30	1110
Load regulation	Reg·load	$V_{IN} = 16 \text{ V}, 5 \text{ mA} \le I_{OUT} \le 500 \text{ mA}$	_	20	70	mV
Quiescent current	1_	$9 \text{ V} \leq V_{IN} \leq 26 \text{ V}, I_{OUT} = 0 \text{ A}$	_	0.5	1.0	mA
Quiescent current	I <sub>B</sub>	$9 \text{ V} \le V_{IN} \le 26 \text{ V}, I_{OUT} = 500 \text{ mA}$	_	35	80	ША
Dropout voltage	Vo	I <sub>OUT</sub> = 250 mA	_	0.22	0.35	V
	V <sub>D</sub>	I <sub>OUT</sub> = 500 mA	_	0.42	0.65	٧

# TA58M09S Electrical Characteristics (unless otherwise specified, $T_j = 25$ °C)

Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
		V <sub>IN</sub> = 16 V, I <sub>OUT</sub> = 250 mA	8.73	9.00	9.27	
Output voltage	V <sub>OUT</sub>		8.64	9.00	9.36	V
	Pog. lino	$13~V \leq V_{IN} \leq 20~V,~I_{OUT} = 250~mA$	_	2	20	mV
Line regulation	Reg·line	$10 \text{ V} \le \text{V}_{IN} \le 26 \text{ V}, \text{ I}_{OUT} = 250 \text{ mA}$	_	5	30	IIIV
Load regulation	Reg·load	$V_{IN} = 16 \text{ V}, 5 \text{ mA} \le I_{OUT} \le 500 \text{ mA}$	_	20	70	mV
Quippont ourrent	1-	$10 \text{ V} \le V_{IN} \le 26 \text{ V}, I_{OUT} = 0 \text{ A}$	_	0.6	1.0	m 1
Quiescent current	lΒ	$10 \text{ V} \le V_{IN} \le 26 \text{ V}, I_{OUT} = 500 \text{ mA}$	_	35	80	mA
Doorsellens	V-	I <sub>OUT</sub> = 250 mA	_	0.22	0.35	.,
Dropout voltage	$V_D$	I <sub>OUT</sub> = 500 mA	_	0.42	0.65	V



# TA58M10S Electrical Characteristics (unless otherwise specified, $T_j = 25^{\circ}C$ )

Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
		V <sub>IN</sub> = 16 V, I <sub>OUT</sub> = 250 mA	9.7	10.0	10.3	
Output voltage	V <sub>OUT</sub>		9.6	10.0	10.4	V
Line regulation	Reg·line	$14 \text{ V} \le V_{IN} \le 21 \text{ V}, I_{OUT} = 250 \text{ mA}$	_	2	30	mV
Line regulation		$11 \text{ V} \le V_{IN} \le 26 \text{ V}, I_{OUT} = 250 \text{ mA}$	_	5	40	
Load regulation	Reg·load	$V_{IN} = 16 \text{ V}, 5 \text{ mA} \le I_{OUT} \le 500 \text{ mA}$	_	20	80	mV
Quiescent current	1-	$11 \text{ V} \le \text{V}_{IN} \le 26 \text{ V}, \text{ I}_{OUT} = 0 \text{ A}$	_	0.6	1.2	mA
Quiescent current	lΒ	$11 \text{ V} \le \text{V}_{IN} \le 26 \text{ V}, \text{ I}_{OUT} = 500 \text{ mA}$	_	35	80	IIIA
Daniel	\/-	I <sub>OUT</sub> = 250 mA	_	0.22	0.35	.,
Dropout voltage	V <sub>D</sub>	I <sub>OUT</sub> = 500 mA		0.42	0.65	V

TA58M12S Electrical Characteristics (unless otherwise specified,  $T_j = 25$ °C)

Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
		V <sub>IN</sub> = 18 V, I <sub>OUT</sub> = 250 mA	11.64	12.00	12.36	
Output voltage	V <sub>OUT</sub>	$ \begin{array}{c c} 13 \ V \leq V_{IN} \leq 26 \ V, \ I_{OUT} = 250 \ mA, \\ -40 ^{\circ}C \leq T_{j} \leq 105 ^{\circ}C \end{array} $	11.52	12.00	12.48	V
Line regulation	Regiline	$16 \text{ V} \le V_{IN} \le 23 \text{ V}, I_{OUT} = 250 \text{ mA}$	_	2	30	mV
Line regulation	Regiline	$13 \text{ V} \le \text{V}_{IN} \le 26 \text{ V}, \text{ I}_{OUT} = 250 \text{ mA}$	_	5	40	IIIV
Load regulation	Reg·load	$V_{IN} = 18 \text{ V}, 5 \text{ mA} \le I_{OUT} \le 500 \text{ mA}$	_	20	80	mV
Quiescent current	1_	$13~V \leq V_{IN} \leq 26~V,~I_{OUT} = 0~A$	_	0.7	1.2	mA
Quiescent current	I <sub>B</sub>	$13 \text{ V} \le V_{IN} \le 26 \text{ V}, I_{OUT} = 500 \text{ mA}$	_	35	80	IIIA
Dropout voltage	$V_{D}$	I <sub>OUT</sub> = 250 mA	_	0.22	0.35	V
		I <sub>OUT</sub> = 500 mA	_	0.42	0.65	V



# TA58M15S Electrical Characteristics (unless otherwise specified, $T_j = 25$ °C)

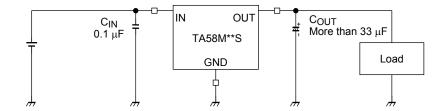
Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
		V <sub>IN</sub> = 20 V, I <sub>OUT</sub> = 250 mA	14.55	15.00	15.45	
Output voltage	V <sub>OUT</sub>	$ \begin{array}{c} 16 \ V \leq V_{IN} \leq 26 \ V, \ I_{OUT} = 250 \ mA, \\ -40 ^{\circ}C \leq T_{j} \leq 105 ^{\circ}C \\ \end{array} $	14.4	15.0	15.6	V
	Reg·line	$19 \text{ V} \le \text{V}_{IN} \le 26 \text{ V}, \text{ I}_{OUT} = 250 \text{ mA}$	_	2	30	m) /
Line regulation		$16 \text{ V} \le \text{V}_{IN} \le 26 \text{ V}, \text{ I}_{OUT} = 250 \text{ mA}$	_	5	40	mV
Load regulation	Reg·load	$V_{IN} = 20 \text{ V}, \text{ 5 mA} \le I_{OUT} \le 500 \text{ mA}$	_	30	100	mV
Quiescent surrent	1-	$16 \text{ V} \le \text{V}_{IN} \le 26 \text{ V}, \text{ I}_{OUT} = 0 \text{ A}$	_	0.7	1.4	mΛ
Quiescent current	lΒ	$16 \text{ V} \le \text{V}_{IN} \le 26 \text{ V}, \text{ I}_{OUT} = 500 \text{ mA}$	_	35	80	mA
<u> </u>	V-	I <sub>OUT</sub> = 250 mA	_	0.22	0.35	.,
Dropout voltage	$V_D$	I <sub>OUT</sub> = 500 mA	_	0.42	0.65	V



#### **Electrical Characteristics Common to All Products**

• Tj = 25°C in the measurement conditions of each item is a regulation for where the standard condition when a pulse test is carried out, and any drift in the electrical characteristic due to a rise in the junction temperature of the chip may be disregarded.

# **Standard Application Circuit**



• Place C<sub>IN</sub> as close as possible to the input terminal and GND. Place C<sub>OUT</sub> as close as possible to the output terminal and GND. Although capacitor C<sub>OUT</sub> acts to smooth the dc output voltage during suspension of output oscillation or load change, it might cause output oscillation in a cold environment due to increased capacitor ESR. It is therefore recommended to use a capacitor with small variations temperature sensitivity. The IC may oscillate due to external conditions (output current, temperature, or the type of the capacitor used). The type of capacitor required must be determined by the actual application circuit in which the IC is used.

# **Usage Precautions**

#### · Low voltage

Do not apply voltage to the Product that is lower than the minimum operating voltage, or the Product's protective functions will not operate properly and the Product may be permanently damaged.

#### • Overcurrent Protection

The overcurrent protection circuits in the Product are designed to temporarily protect Product from minor overcurrent of brief duration. When the overcurrent protective function in the Product activates, immediately cease application of overcurrent to Product. Improper usage of Product, such as application of current to Product exceeding the absolute maximum ratings, could cause the overcurrent protection circuit not to operate properly and/or damage Product permanently even before the protection circuit starts to operate.

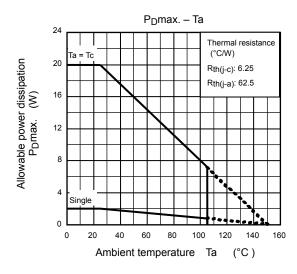
#### • Overheating Protection

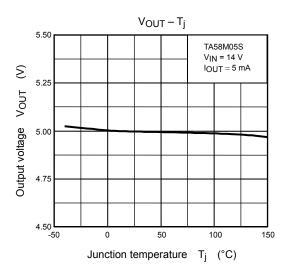
The thermal shutdown circuits in the Product are designed to temporarily protect Product from minor overheating of brief duration. When the overheating protective function in the Product activates, immediately correct the overheating situation. Improper usage of Product, such as the application of heat to Product exceeding the absolute maximum ratings, could cause the overheating protection circuit not to operate properly and/or damage Product permanently even before the protection circuit starts to operate.

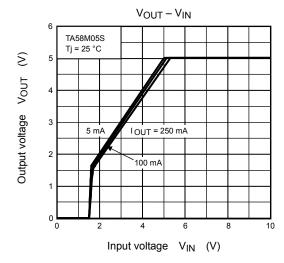
#### • Overvoltage Protection

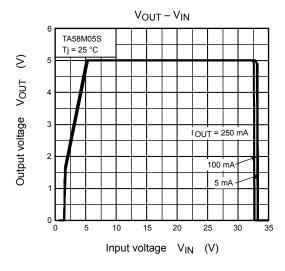
The overvoltage protection circuits in the Product are designed to temporarily protect Product from minor overvoltage of brief duration. When the overvoltage protective function in the Product activates, immediately cease application of overvoltage to Product. Improper usage of Product, such as application of voltage to Product exceeding the absolute maximum ratings, could cause the overvoltage protection circuit not to operate properly and/or damage Product permanently even before the protection circuit starts to operate.

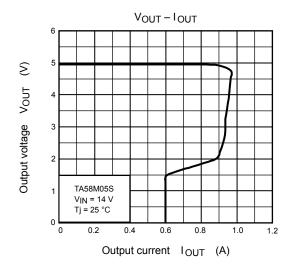
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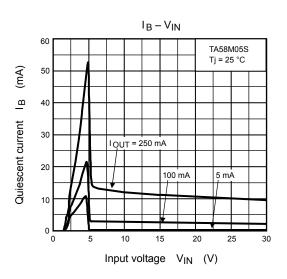


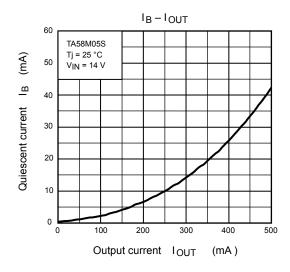


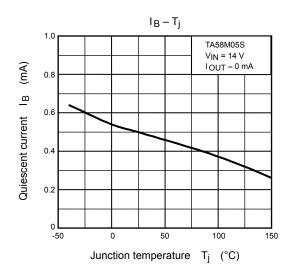


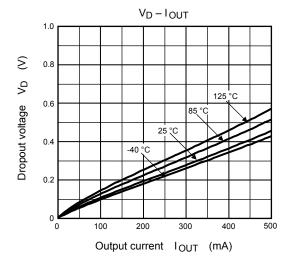


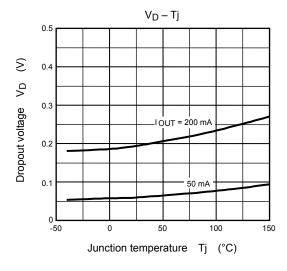


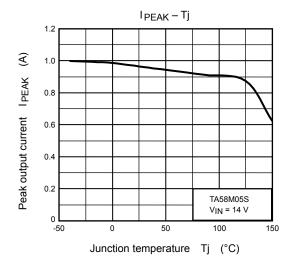


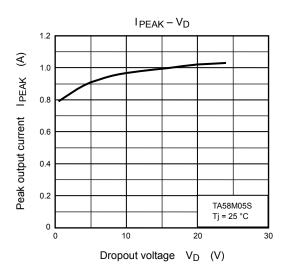








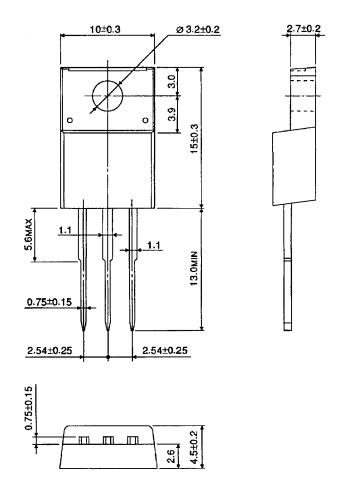






# **Package Dimensions**

HSIP3-P-2.54A Unit: mm



11

Weight: 1.7 g (typ.)



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