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TOSHIBA Transistor Silicon NPN Epitaxial Type (PCT Process) Silicon PNP Epitaxial Type (PCT Process)

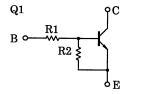
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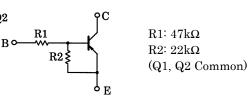
Switching, Inverter Circuit, Interface Circuit and Driver Circuit Applications

- Including two devices in US6 (ultra super mini type with 6 leads)
- With built-in bias resistors •
- Simplify circuit design •
- Reduce a quantity of parts and manufacturing process

Equivalent Circuit and Bias Resister Values

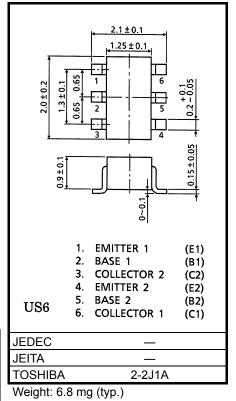
Q2





Q1 Absolute Maximum Ratings (Ta = 25°C)

Characteristic	Symbol	Rating	Unit
Collector-base voltage	V _{CBO}	50	V
Collector-emitter voltage	V _{CEO}	50	V
Emitter-base voltage	V _{EBO}	15	V
Collector current	Ι _C	100	mA



Q2 Absolute Maximum Ratings (Ta = 25°C)

Characteristic	Symbol	Rating	Unit
Collector-base voltage	V _{CBO}	-50	V
Collector-emitter voltage	V _{CEO}	-50	V
Emitter-base voltage	V _{EBO}	-15	V
Collector current	Ι _C	-100	mA

Unit: mm

Q1, Q2 Common Absolute Maximum Ratings (Ta = 25°C)

Characteristic	Symbol	Rating	Unit
Collector power dissipation	P _C *	200	mW
Junction temperature	Tj	150	°C
Storage temperature range	T _{stg}	-55~150	°C

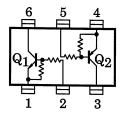
Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

* : Total rating

Marking

Equivalent Circuit (Top View)



Q1 Electrical Characteristics (Ta = 25°C)

Characteristic	Symbol	Test Circuit	Test Condition	Min	Тур.	Max	Unit	
Collector cut-off current	I _{CBO}	_	V _{CB} = 50V, I _E = 0	_	_	100	nA	
	ICEO	—	V _{CE} = 50V, I _B = 0	—	_	500		
Emitter cut-off current	I _{EBO}	—	V _{EB} = 15V, I _C = 0	0.167	_	0.311	mA	
DC current gain	h _{FE}	_	V _{CE} = 5V, I _C = 10mA	70	-	_	-	
Collector-emitter saturation voltage	V _{CE (sat)}	_	I _C = 5mA, I _B = 0.25mA	_	0.1	0.3	V	
Input voltage (ON)	V _{I (ON)}	_	V _{CE} = 0.2V, I _C = 5mA	2.2	_	5.8	V	
Input voltage (OFF)	VI (OFF)	_	V _{CE} = 5V, I _C = 0.1mA	1.5	_	2.6	V	
Transition frequency	f _T	_	V _{CE} = 10V, I _C = 5mA	_	250	_	MHz	
Collector output capacitance	C _{ob}	_	V _{CB} = 10V, I _E = 0, f = 1 MHz	_	3	6	pF	

Q2 Electrical Characteristics (Ta = 25°C)

Characteristic	Symbol	Test Circuit	Test Condition	Min	Тур.	Max	Unit
Collector cut-off current	I _{CBO}	_	$V_{CB} = -50V, I_E = 0$	-	_	-100	nA
	ICEO		$V_{CE} = -50V, I_B = 0$	_	—	-500	ПА
Emitter cut-off current	I _{EBO}		V _{EB} = −15V, I _C = 0	-0.167	—	-0.311	mA
DC current gain	h _{FE}		$V_{CE} = -5V, I_C = -10mA$	70	—	_	—
Collector-emitter saturation voltage	V _{CE (sat)}		I _C = −5mA, I _B = −0.25mA	_	-0.1	-0.3	V
Input voltage (ON)	V _{I (ON)}	-	$V_{CE} = -0.2V, I_{C} = -5mA$	-2.2	_	-5.8	V
Input voltage (OFF)	VI (OFF)		$V_{CE} = -5V, I_C = -0.1mA$	-1.5	—	-2.6	V
Transition frequency	f _T		V _{CE} = −10V, I _C = −5mA	_	200	_	MHz
Collector output capacitance	C _{ob}	_	V _{CB} = -10V, I _E = 0	_	3	6	pF

Q1, Q2 Common Electrical Characteristics (Ta = 25°C)

Characteristic	Symbol	Test Circuit	Test Condition	Min	Тур.	Max	Unit
Input resistor	R1	_	—	32.9	47	61.1	kΩ
Resistor ratio	R1/R2	—	_	1.92	2.14	2.35	—

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0.6

1.0

1.4

1.8

INPUT VOLTAGE VI(OFF) (V)

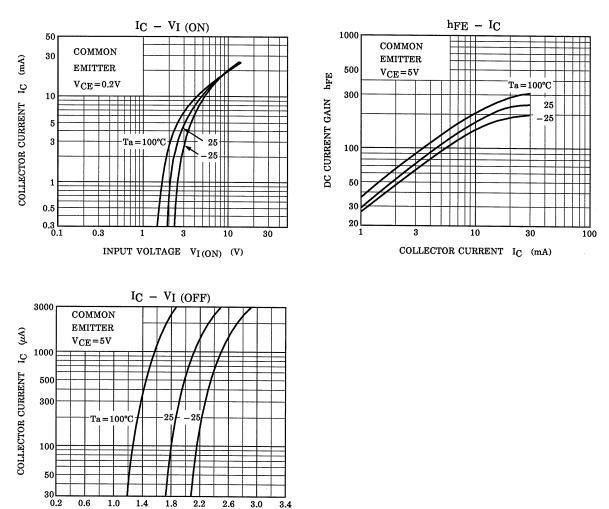
2.2

2.6

3.0

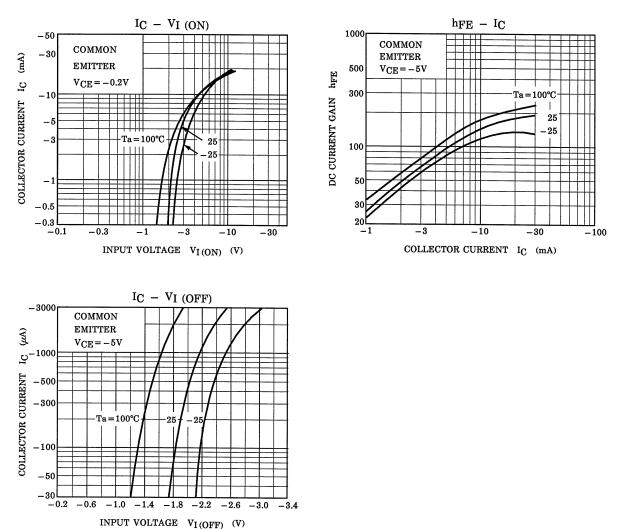
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Q1



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Q2



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