

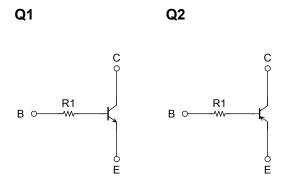
TOSHIBA Transistor Silicon NPN · PNP Epitaxial Type (PCT Process) (Bias Resistor Built-in Transistor)

# **RN4991FE**

Switching, Inverter Circuit, Interface Circuit and Driver Circuit Applications

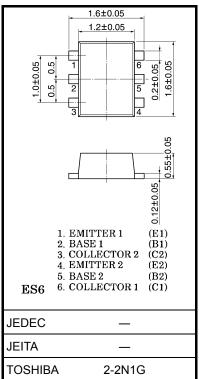
- Two devices are incorporated into an Extreme-Super-Mini (6-pin) package.
- Incorporating a bias resistor into a transistor reduces parts count. Reducing the parts count enables the manufacture of ever more compact equipment and lowers assembly cost.

#### **Equivalent Circuit and Bias Resistor Values**



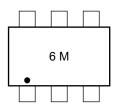
R1: 10 kΩ

(Q1, Q2 common)

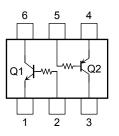


Weight: 0.003 g (typ.)

#### Marking



#### Equivalent Circuit (top view)



Unit: mm

#### Absolute Maximum Ratings (Ta = 25°C) (Q1)

Characteristics	Symbol	Rating	Unit
Collector-base voltage	V <sub>CBO</sub>	50	V
Collector-emitter voltage	V <sub>CEO</sub>	50	V
Emitter-base voltage	V <sub>EBO</sub>	5	V
Collector current	Ι <sub>C</sub>	100	mA

# Absolute Maximum Ratings (Ta = 25°C) (Q2)

Characteristics	Symbol	Rating	Unit
Collector-base voltage	V <sub>CBO</sub>	-50	V
Collector-emitter voltage	V <sub>CEO</sub>	-50	V
Emitter-base voltage	V <sub>EBO</sub>	-5	V
Collector current	Ι <sub>C</sub>	-100	mA

#### Absolute Maximum Ratings (Ta = 25°C) (Q1, Q2 common)

Characteristics	Symbol	Rating	Unit
Collector power dissipation	P <sub>C</sub> (Note 1)	100	mW
Junction temperature	Tj	150	°C
Storage temperature range	T <sub>stg</sub>	-55~150	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: Total rating

# Electrical Characteristics (Ta = 25°C) (Q1)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Collector cut-off current	I <sub>CBO</sub>	$V_{CB} = 50 \text{ V}, \text{ I}_{E} = 0$	_	_	100	nA
Emitter cut-off current	I <sub>EBO</sub>	$V_{EB} = 5 V, I_{C} = 0$	_	_	100	nA
DC current gain	h <sub>FE</sub>	$V_{CE} = 5 \text{ V}, \text{ I}_{C} = 1 \text{ mA}$	120	_	700	
Collector-emitter saturation voltage	V <sub>CE (sat)</sub>	$I_{C} = 5 \text{ mA}, I_{B} = 0.25 \text{ mA}$	_	0.1	0.3	V
Transition frequency	f <sub>T</sub>	$V_{CE} = 10 \text{ V}, \text{ I}_{C} = 5 \text{ mA}$	_	250	_	MHz
Collector output capacitance	C <sub>ob</sub>	$V_{CB} = 10 \text{ V}, I_E = 0, f = 1 \text{ MHz}$	_	3	6	pF

# Electrical Characteristics (Ta = 25°C) (Q2)

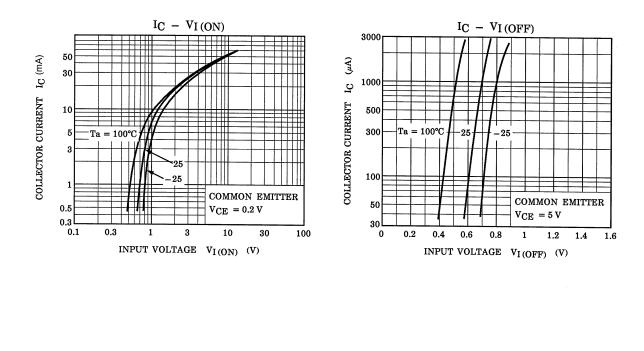
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Collector cut-off current	I <sub>CBO</sub>	$V_{CB}=-50~V,~I_{E}=0$	_	_	-100	nA
Emitter cut-off current	I <sub>EBO</sub>	$V_{EB}=-5~V,~I_C=0$	_	_	-100	nA
DC current gain	h <sub>FE</sub>	$V_{CE} = -5 \text{ V}, \text{ I}_{C} = -1 \text{ mA}$	120	_	400	
Collector-emitter saturation voltage	V <sub>CE (sat)</sub>	$I_C = -5 \text{ mA}, I_B = -0.25 \text{ mA}$	_	-0.1	-0.3	V
Transition frequency	f <sub>T</sub>	$V_{CE} = -10$ V, $I_C = -5$ mA	_	200	—	MHz
Collector output capacitance	C <sub>ob</sub>	$V_{CB} = -10 \text{ V}, I_E = 0, f = 1 \text{ MHz}$		3	6	pF

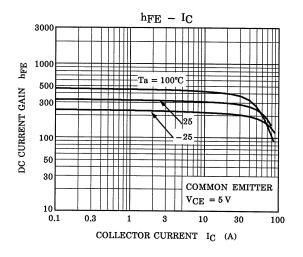
# Electrical Characteristics (Ta = 25°C) (Q1, Q2 common)

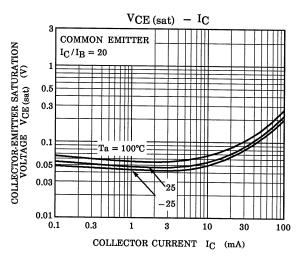
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Input resistor	R1	_	7	10	13	kΩ

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Q1



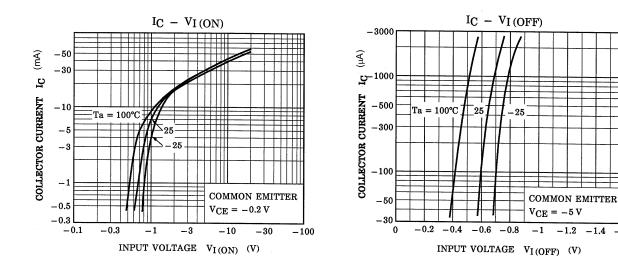


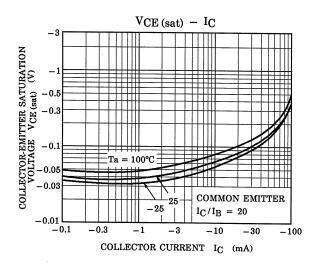


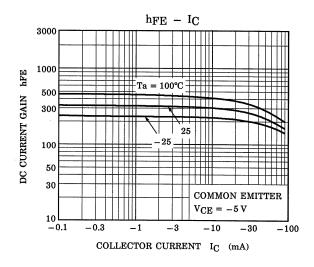
# TOSHIBA

-1.2 -1.4 -1.6

Q2







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