

DUAL 2-INPUT NAND GATE

The KIC7W00FK is a high speed C²MOS 2-INPUT NAND GATE fabricated with silicon gate C²MOS technology.

It achieves high speed operation similar to equivalent LSTTL while maintaining the C²MOS low power dissipation.

The internal circuit is composed of 3 stages including buffer output, which enables high noise immunity and stable output.

All inputs are equipped with protection circuits against static discharge or transient excess voltage.

FEATURES

*Not recommended
for new design*