

**TIMER**

The KIA555P/F monolithic circuit is a highly stable device as producing accurate time delay or timing pulse. Additional terminals are provided for triggering or resetting, if desired. In the time delay or monostable mode of operation, the time is precisely controlled by one external resistor and capacitor. In the astable mode of operation, the frequency and duty cycle are accurately and independently controlled with two external resistors and one capacitor.

The circuit of the KIA555P/F may be triggered and reset on falling waveforms, and the output structure and source and sink up to 200mA or drive TTL circuit. Operation is specified for supplies of 5 to 15 volts.

**FEATURES**

*Not recommended  
for new design*