

Midium Power Transistors (-50V / -3A)

2SAR533P

Structure

PNP Silicon epitaxial planar transistor

Features

- 1) Low saturation voltage, typically $V_{CE (sat)}$ = -0.4V (Max.) (I_C / I_B= -1A / -50mA)
- 2) High speed switching

Applications

Driver

Packaging specifications

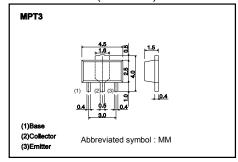
Туре	Package	Taping			
	Code	T100			
	Basic ordering unit (pieces)	1000			
2SAR533P		0			

◆ Absolute maximum ratings (Ta = 25°C)

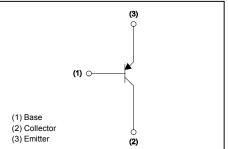
Para	Symbol	Limits	Unit	
Collector-base voltage		V_{CBO}	-50	V
Collector-emitter voltage		V_{CEO}	-50	V
Emitter-base voltage		V_{EBO}	-6	V
Collector current	DC	Ic	-3	Α
	Pulsed	I _{CP} *1	-6	Α
Power dissipation		P _D *2	0.5	W
		P _D *3	2	W
Junction temperature		Tj	150	°C
Range of storage temperature		T_{stg}	-55 to 150	°C

^{*1} Pw=10ms, Single Pulse

Dimensions (Unit : mm)



● Inner circuit (Unit : mm)



^{*2} Each terminal mounted on a recommended land.

^{*3} Mounted on a ceramic board. (40x40x0.7mm³)

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●Electrical characteristic (Ta = 25°C)

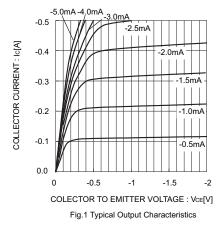
Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions	
Collector-emitter breakdown voltage	BV_{CEO}	-50	-	-	V	I _C = -1mA	
Collector-base breakdown voltage	BV _{CBO}	-50	-	-	V	I _C = -100μA	
Emitter-base breakdown voltage	BV_{EBO}	-6	-	-	V	I _E = -100μA	
Collector cut-off current	I _{CBO}	-	-	-1	μA	V _{CB} = -50V	
Emitter cut-off current	I _{EBO}	-	-	-1	μA	V _{EB} = -4V	
Collector-emitter staturation voltage	V _{CE(sat)} *1	-	-200	-400	mV	I _C = -1A, I _B = -50mA	
DC current gain	h _{FE}	180	-	450	-	V_{CE} = -3V, I_{C} = -50mA	
Transition frequency	f _T *1	-	300	-	MHz	V _{CE} = -10V I _E =500mA, f=100MHz	
Collector output capacitance	C _{ob}	-	24	-	pF	V _{CB} = -10V, I _E =0A f=1MHz	
Turn-on time	t _{on} *2	-	45	-	ns	L = 1.50 L = 150mA	
Storage time	t _{stg} *2	-	250	-	ns	I _C = -1.5A, I _{B1} = -150mA, I _{B2} =150mA, V _{CC} ~-10V	
Fall time	t _f *2	-	35	-	ns		

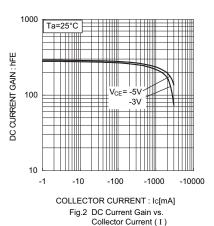
^{*1} Pulsed

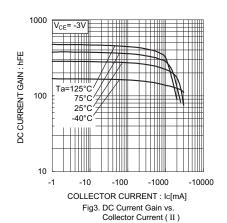
^{*2} See switching time test circuit

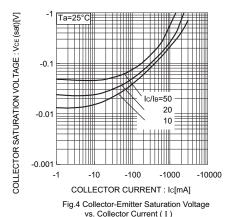
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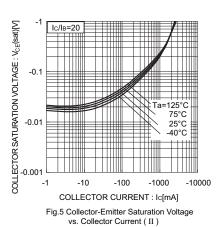
•Electrical characteristic curves

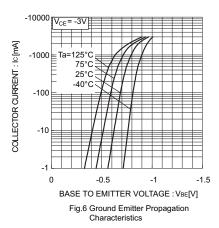


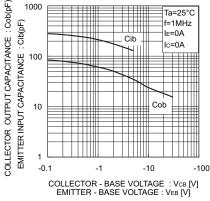


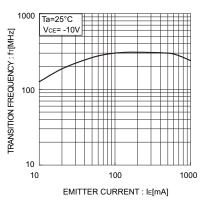












-10 Single pulse

-10 Single pulse

-10 Single pulse

-10 Single pulse

-10 Inms

-10 100ms

-100ms

-

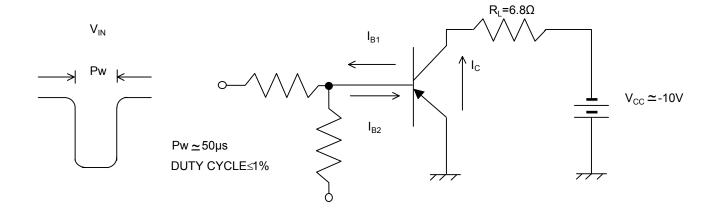
Fig.7 Emitter Input Capacitance vs.
Emitter-Base Voltage
Collector Output Capacitance vs.
Collector-Base Voltage

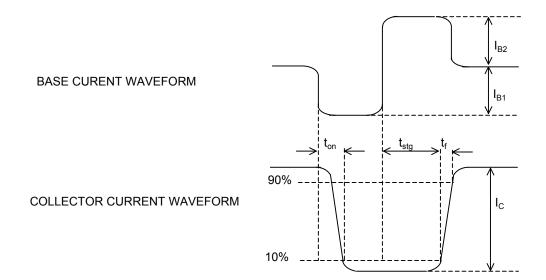
Fig.8 Gain Bandwidth Product vs. Emitter Current

Fig.9 Safe Operating Area

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•Switching time test circuit





Notes

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