

Chip tantalum capacitors

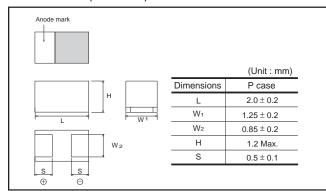
(New bottom surface electrode type : Large capacitance)

TCS Series P Case

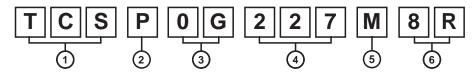
●Features (P)

- 1) New package construction enables a higher capacitance than conventional products (TCT Series)
- 2) Thin, compact, high capacitance design contributes to smaller, thinner, high performance sets
- 3) Ideal for noise removal on power supply lines with limited space
- 4) Eco-friendly halogen-free products

●Dimensions (Unit: mm)



●Part No. Explanation



- 1 Series name
- 2 Case style
- (3) Rated voltage

Rated voltage (V)							
CODE	0E	0G	0J	1A	1C	1D	1E

(4) Nominal capacitance

Nominal capacitance in pF in 3 digits: 2 significant figures followed by the figure representing the number of 0's.

5 Capacitance tolerance

M: ±20%

- **6** Taping
 - 8 : Reel width : 8mm
 - R : Positive electrode on the side opposite to sprocket hole

Rated table

(μF)			Rated	voltage	(V.DC)		
(μΓ)	2.5	4	6.3	10	16	20	25
10 (106)						Р	*P
15 (156)							
22 (226)					*P		
33 (336)				*P	*P		
47 (476)				Р			
68 (686)							
100 (107)			Р				
150 (157)			*P				
220 (227)		Р					
330 (337)	*P						

Remark) Case size codes (P) in the above show products line-up.

^{*} Under development

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Marking

The indications listed below should be given on the surface of a capacitor.

: The polarity should be shown by □ bar. (on the anode side)

(2) Rated DC voltage: A voltage code is shown as below table.
(3) Capacitance value: A capacitance code is shown as below table.

Voltage Code	Rated DC Voltage (V)
е	2.5
g	4
j	6.3
А	10
С	16
D	20
E	25

Capacitance Code	Capacitance Value (μF)					
а	10					
е	15					
j	22					
n	33					
S	47					
w	68					
ā	100					
ē	150					
j	220					
ī	330					

Visual typical example

(1) voltage code (2) capacitance code

[P case] note 1)



manufacture code note 2) voltage code and capacitance code are variable with parts number

Characteristics

Iter	n	Performance						nand	е		Test conditions (based on JIS C 5101–1 and JIS C 5101–3)			
Operating Temp	oerature	-5	5°C	to +	-125	°C					Voltage reduction when temperature exceeds +85°C			
Maximum operating temperature with no voltage derating			+85°C											
Rated voltage (V.DC)	2.5	4	6.3	10	16	20	25	35		at 85°C			
Category voltag	e (V.DC)	1.6	2.5	4	6.3	10	13	16	22		at 125°C			
Surge voltage (V.DC)	3.2	5	8	13	20	26	32	44		at 85°C			
DC Leakage cu	rrent	_			itisfic		ne va	alue	on		As per 4.9 JIS C 5101-1 As per 4.5.1 JIS C 5101-3 Voltage : Rated voltage for 5min			
Capacitance tol	erance	Shall be satisfied allowance range. ±20%									As per 4.7 JIS C 5101-1 As per 4.5.2 JIS C 5101-3 Measuring frequency: 120±12Hz Measuring voltage: 0.5Vrms +1.5V.DC Measuring circuit: DC Equivalent series circuit			
Tangent of loss (Df, tan δ)	angle	Shall be satisfied the value on "Standard list"							on		As per 4.8 JIS C 5101-1 As per 4.5.3 JIS C 5101-3 Measuring frequency: 120±12Hz Measuring voltage: 0.5Vrms +1.5V.DC Measuring circuit: DC Equivalent series circuit			
Impedance / ESI	R	Shall be satisfied the value on " Standard list "									As per 4.10 JIS C 5101-1 As per 4.5.4 JIS C 5101-3 Measuring frequency: 100±10kHz Measuring voltage : 0.5Vrms or less Measuring circuit : DC Equivalent series circuit			
Resistance to Soldering heat	Appearance	There should be no significant abnormality. The indications should be clear.								mality.	As per 4.14 JIS C 5101-1 As per 4.6 JIS C 5101-3			
	L.C.	Less than 200% of initial limit						al lir	nit		Dip in the solder bath Solder temp : 260±10°C			
	⊿C / C	W	ithin	±30)% o	f init	ial v	alue)		Duration : 5±0.5s			
	Df (tan δ)	Le	Within ±30% of initial value Less than 200% of initial limit					al lir	nit		Repetition : 1 After the specimens, leave it at room temperature for over 24h and then measure the sample.			

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Iten	n	Performance	Test conditions (based on JIS C 5101–1 and JIS C 5101–3							
Temperature cycle	Appearance	There should be no significant abnormality. The indications should be clear.	As per	4.	16 JIS C 5101- 10 JIS C 5101-					
	L.C.	Less than 200% of initial limit	Repetition : 5 cycles (1 cycle : steps 1 to 4) without discontinuation.							
	⊿c/c	Within ±30% of initial value								
	Df (tan δ)	Less than 200% of initial limit								
			<u> </u>	2	Room temp.	3min. or less				
			⊢	3	125±2°C	30±3min. 3min. or less				
			4 Room temp. 3min. or less After the specimens, leave it at room temperature for							
					•	sure the sample	•			
Moisture resistance	Appearance	There should be no significant abnormality. The indications should be clear.	As per 4.22 JIS C 5101-1 As per 4.12 JIS C 5101-3							
	L.C.	Less than 200% of initial limit				under such atrature and hum				
	⊿C / C	Within ±30% of initial value	60±2°0	Са	nd 90 to 95% F	RH, respectively				
	Df (tan δ)	Less than 200% of initial limit	leave it at room temperature for over 24h and then measure the sample.							
Temperature	Temp.	−55°C	As per	4.:	29 JIS C 5101-	1				
Stability	⊿C / C	Within 0/-30% of initial value			13 JIS C 5101-					
	Df (tan δ)	Shall be satisfied the value on " Standard list "	-							
	L.C.	-	-							
	Temp.	+85°C	-							
	⊿C / C	Within +15/–5% of initial value								
	Df (tan δ)	Shall be satisfied the value on " Standard list "								
	L.C.	Less than 1000% of initial limit	-							
	Temp.	+125°C	-							
	⊿C / C	Within +20/–5% of initial value	-							
	Df (tan δ)	Shall be satisfied the value on " Standard list "	-							
	L.C.	Less than 1250% of initial limit	-							
Surge voltage	Appearance	There should be no significant abnormality. The indications should be clear.	As per 4.26JIS C 5101-1 As per 4.14JIS C 5101-3 Apply the specified surge voltage via the serial resistance of $1k\Omega$ every 5±0.5 min. for 30±5 s. each time in the atmospheric condition of 85±2°C.							
	L.C.	Less than 200% of initial limit								
		Within ±30% of initial value								
	Df (tan δ)	Less than 200% of initial limit	Repeat this procedure 1,000 times. After the specimens, leave it at room temperature for over 24h and then measure the sample.							
Loading at High temperature	Appearance	There should be no significant abnormality. The indications should be clear.	As per	4.	23 JIS C 5101- 15 JIS C 5101-	3	2 20/01 :::			
	L.C.	Less than 200% of initial limit	discon	itini	uation via the s	erial resistance				
	⊿C / C	Within ±30% of initial value				2°C, leave the s or over 24h and	ample at room measure the value.			
	Df (tan δ)	Less than 200% of initial limit			,					
Terminal	Capacitance	The measured value should be stable.			35 JIS C 5101-					
strength	Appearance	There should be no significant abnormality.	As per 4.9 JIS C 5101-3 A force is applied to the terminal until it bends to 1mm and by a prescribed tool maintain the condition for 5s. (See the figure below) (Unit : mm) F (Apply force) Thickness=1.6mm							
					45	45				

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It	em	Performance	Test conditions (JIS C 5101–1 and JIS C 5101–3)				
Adhesiveness		The terminal should not come off.	As per 4.34 JIS C 5101-1 As per 4.8 JIS C 5101-3 Apply force of 5N in the two directions shown in the figure below for 10±1s after mounting the terminal on a circuit board.				
Dimension	S	Refer to "External dimensions"	Measure using a caliper of JIS B 7507 Class 2 or higher grade.				
Resistance	e to solvents	The indication should be clear	As per 4.32 JIS C 5101-1 As per 4.18 JIS C 5101-3 Dip in the isopropyl alcohol for 30±5s, at room temperature.				
Solderability		3/4 or more surface area of the solder coated terminal dipped in the soldering bath should be covered with the new solder.	As per 4.15.2 JIS C 5101-1 As per 4.7 JIS C 5101-3 Dip speed: 25±2.5mm / s Pre-treatment (accelerated aging): Leave the sample on the boiling distilled water for 1 h. Solder temp.: 245±5°C Duration: 3±0.5s Solder: M705 Flux: Rosin 25% IPA 75%				
Vibration	Capacitance	Measure value should not fluctuate during the measurement.	As per 4.17 JIS C 5101-1 Frequency : 10 to 55 to 10Hz/min. Amplitude : 1.5mm				
Appearance		There should be no significant abnormality.	Time: 2h each in X and Y directions Mounting: The terminal is soldered on a print circuit board.				

• Standard products list, TCS series P case

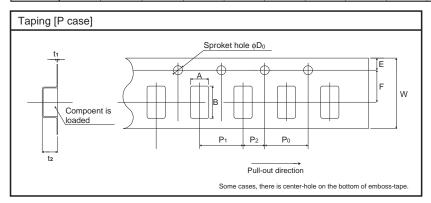
Part No.	Rated voltage 85°C	Category voltage 125°C	Surge voltage 85°C	Cap. 120Hz	Tolerance	Leakage current 25°C		Df 120Hz (%)		IMP 100kHz
	(V)	(V)	(V)	(μF)	(%)	1WV.5min (μA)	–55°C	25°C 85°C	125°C	(Ω)
* TCS P 0E 337 M8R	2.5	1.6	3.2	330	±20	83.0	80	40	60	3.0
TCS P 0G 227 M8R	4	2.5	5	220	±20	88.0	80	40	60	3.0
TCS P 0J 107 M8R	6.3	4	8	100	±20	63.0	80	40	60	3.0
* TCS P 0J 157 M8R	6.3	4	8	150	±20	95.0	80	40	60	3.0
* TCS P 1A 336 M8R	10	6.3	13	33	±20	17.0	60	30	40	4.0
TCS P 1A 476 M8R	10	6.3	13	47	±20	24.0	60	30	40	4.0
* TCS P 1C 226 M8R	16	10	20	22	±20	18.0	60	30	40	4.0
* TCS P 1C 336 M8R	16	10	20	33	±20	27.0	60	30	40	4.0
TCS P 1D 106 M8R	20	13	26	10	±20	10.0	30	20	30	6.0
* TCS P 1E 106 M8R	25	16	32	10	±20	13.0	30	20	30	6.0

^{*=}Under development

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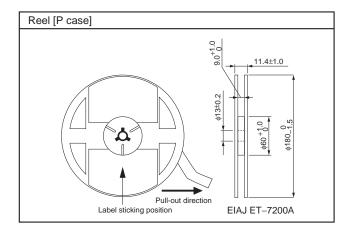
Packaging specifications

Case code	A+0.1	B±0.1	W±0.2	E±0.1	F±0.05	P1 <u>±</u> 0.1	P2±0.05	Po±0.1	D ₀ ±0.05	t1±0.05	t2±0.1
Р	1.55	2.30	8.00	1.75	3.50	4.00	2.00	4.00	φ1.55	0.25	1.32



Packaging style

Case code	Packaging	Packag	ging style	Symbol	Basic ordering units
P case	Taping	plastic taping	φ180mm Reel	R	3,000pcs



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