



AS225-313, AS225-313LF: PHEMT GaAs IC 1 W Low Loss 0.1–6 GHz SPDT Switch

Applications

• WLAN 802.11a, b, g

Features

- Positive low voltage control (0/3 V)
- Low insertion loss (0.6 dB, 0.1–6 GHz)
- High linearity (IIP3 = 53 dBm @ 3 V)
- Miniature QFN-6 pin plastic package (2 mm x 3 mm)
- PHEMT process
- Available lead (Pb)-free and RoHS-compliant MSL-1 @ 260 °C per JEDEC J-STD-020

Description

The AS225-313 is a 0.1-6 GHz PHEMT GaAs IC SPDT antenna switch. Designed for WLAN applications, this device is capable of switching 1 W microwave signals with 3 V control voltage while maintaining high-linearity performance. The switch covers the entire 802.11a, b and g frequency ranges. The low-loss, highisolation, high-inearity and low-cost features make this switch ideal for Wireless LAN systems.



Skyworks offers lead (Pb)-free, RoHS (Restriction of Hazardous Substances)-compliant packaging.

Electrical Specifications at 25 °C

$Z_0 = 50 \Omega$, $V_{CTBL} = 0/3 V$, unless otherwise noted

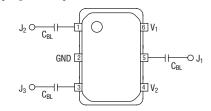
Parameter	Test Condition	Frequency	Min.	Тур.	Max.	Unit
Insertion loss	J ₁ –J ₂ , J ₁ –J ₃	0.10-6.00 GHz		0.60	0.75	dB
		2.40–2.50 GHz		0.50	0.65	dB
		5.15–5.85 GHz		0.60	0.70	dB
Isolation	J ₁ –J ₂ , J ₁ –J ₃	0.10-6.00 GHz	18	20		dB
		2.40–2.50 GHz	18	20		dB
		5.15–5.85 GHz	19	21		dB
Return loss	J ₁ –J ₂ , J ₁ –J ₃	0.10-6.00 GHz	18	20		dB
		2.40–2.50 GHz	23	25		dB
		5.15–5.85 GHz	21	23		dB



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Pin Out (Top View)



DC blocking capacitors (CBL) must be supplied externally. $C_{BI} = 15 \text{ pF}.$

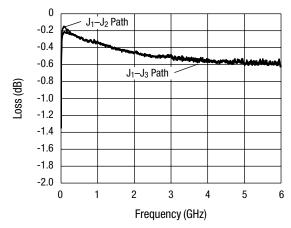
Operating Characteristics at 25 °C

 $Z_0 = 50 \ \Omega$, $V_{CTRL} = 0/3$ V, unless otherwise noted

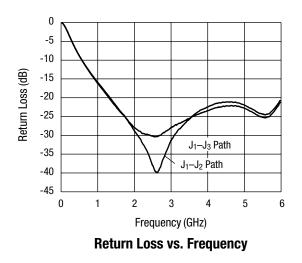
Parameter	Condition	Frequency	Min.	Тур.	Max.	Unit
P _{1 dB}	@ 3 V @ 5 V	5200 MHz 5200 MHz		30 34		dBm dBm
2nd harmonic	$P_{IN} = 22 \text{ dBm}, V_C = 3 \text{ V}$ $V_C = 5 \text{ V}$	2450 MHz 2450 MHz 2450 MHz		70 75		dBc dBc
3rd harmonic	$V_{\rm C} = 3 V$ $P_{\rm IN} = 22 \text{ dBm}, V_{\rm C} = 3 V$ $V_{\rm C} = 5 V$	2450 MHz 2450 MHz 2450 MHz		68 70		dBc dBc
Input IP3	Two-tone 15 dBm, 5 MHz spacing $V_{CTL} = 0/3 V$ $V_{CTL} = 0/5 V$	5200 MHz 5200 MHz		53 55		dBm dBm
Control voltage	V _{C HIGH} V _{C LOW}		2.5	3.00 -0.25	5.00 0.25	V V
Gate leakage				10 15	100 200	μΑ μΑ

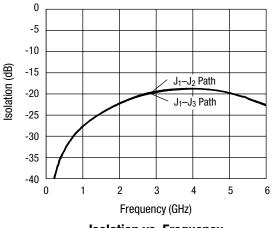
Typical Performance Data

 $Z_0 = 50 \Omega$, $V_{CTRL} = 0/3$ V, unless otherwise noted









Isolation vs. Frequency

Absolute Maximum Ratings

-				
Characteristic	Value			
Max input power @ 0/3 V	32 dBm			
Max input power @ 0/5 V	35 dBm			
Operating voltage	8 V			
Operating temperature	-40 °C to +85 °C			
Storage temperature	-65 °C to +150 °C			

Performance is guaranteed only under the conditions listed in the specifications table and is not guaranteed under the full range(s) described by the Absolute Maximum specifications. Exceeding any of the absolute maximum/minimum specifications may result in permanent damage to the device and will void the warranty.

CAUTION: Although this device is designed to be as robust as possible, ESD (Electrostatic Discharge) can damage this device. This device must be protected at all times from ESD. Static charges may easily produce potentials of several kilovolts on the human body or equipment, which can discharge without detection. Industry-standard ESD precautions must be employed at all times.

Recommended Solder Reflow Profiles

Refer to the "<u>Recommended Solder Reflow Profile</u>" Application Note.

Tape and Reel Information

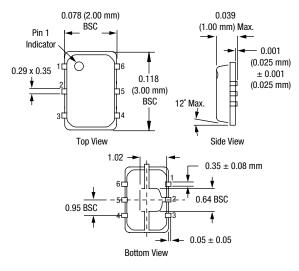
Refer to the "Discrete Devices and IC Switch/Attenuators Tape and Reel Package Orientation" Application Note.

Truth Table

V ₁	V2	J ₁ –J ₂	J ₁ –J ₃			
0	V _{HIGH}	Isolation	Insertion loss			
V _{HIGH}	0	Insertion loss	Isolation			
All other conditions not recommended						

All other conditions not recommended $V_{HIGH} = 2.5$ to 5 V.

QFN-6



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