

PWM Off-Line Switching Regulator ICs

Features and Benefits

- TO-220 fully-molded package with 6 pins
- Current mode PWM control
- PWM and frequency modulation functions: reduces EMI noise, simplifies EMI filters, and cuts cost by external part reduction
- Built-in Slope Compensation circuit: avoids subharmonic oscillation
- Automatic Standby Mode function (Input Power < 40 mW at no load)
 - Normal operation: PWM mode
 - Light load operation: Standby mode (burst oscillation)
- Built-in Audible Noise Suppression function during Standby mode

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Package: TO-220F-6L



Not to scale

Description

STR-W6200D series are power ICs for switching power supplies, incorporating a power MOSFET and a current mode PWM controller IC in one package.

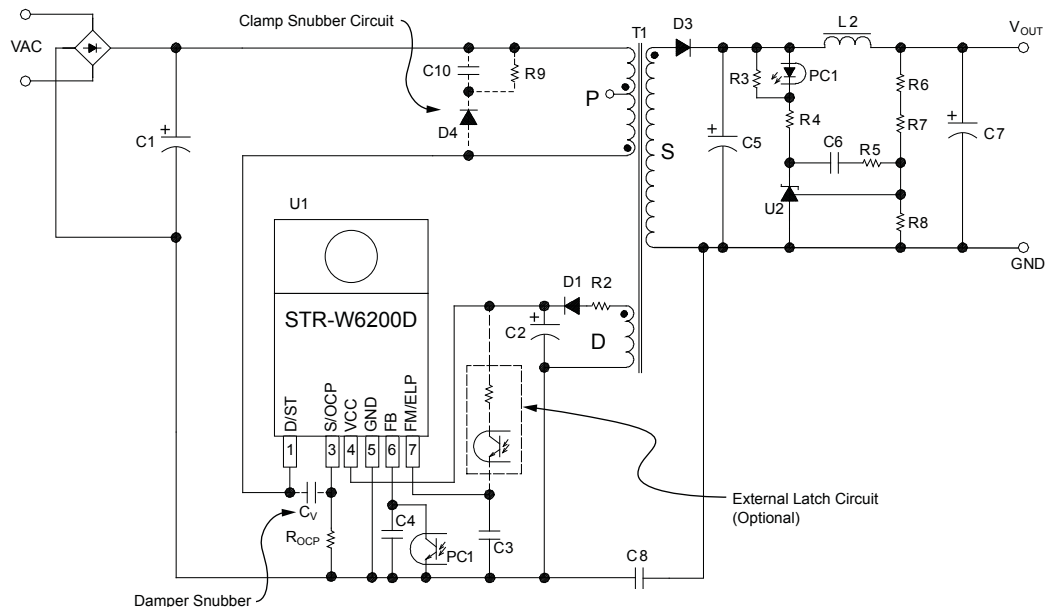
Including a startup circuit and a standby function in the controller, the product achieves low power consumption, low standby power, and high cost-effectiveness in power supply systems, while reducing external components.

Applications:

Switching power supplies for electronic devices such as:

- White goods
- Consumer electronics
- Office automation
- Industrial equipment
- Communication equipment

Typical Application



Features and Benefits (continued)

- Built-in startup circuit: reduces power consumption in standby operation, and eliminates external components
- Bias-Assist function: improves startup operation, suppresses VCC pin voltage drop in operation, and allows use of smaller V_{CC} capacitor
- Built-in Leading Edge Blanking function
- Protection Functions:
 - Overcurrent Protection function (OCP); pulse-by-pulse, built-in compensation circuit to minimize OCP point variation on AC input voltage
 - Overload Protection function (OLP); auto restart, built-in timer, reduces heat during overload condition, and few external components required
 - External Latch Protection function (ELP): latched shutdown by external signal
 - Overvoltage Protection function (OVP): latched shutdown
 - Thermal Shutdown function (TSD); latched shutdown

Selection Guide

Part Number	f _{osc} (kHz)	MOSFET V _{DSS(min)} (V)	R _{DS(on)} (max) (Ω)	P _{OUT} * (W)	
				230 VAC	85 to 265 VAC
STR-W6251D	67	650	3.95	45	30
STR-W6252D			2.8	60	40
STR-W6253D			1.9	90	60

*The listed output power is based on the thermal ratings, and the peak output power can be 120% to 140% of the value stated here. At low output voltage and short duty cycle, the output power may be less than the value stated here.

The polarity value for current specifies a sink as "+," and a source as "-", referencing the IC.

Absolute Maximum Ratings Unless specifically noted, valid at $T_A = 25^\circ\text{C}$

Characteristic	Symbol	Note	Pin	Rating	Unit
Drain Peak Current ¹	I _{DPEAK}	STR-W6251D	1-3	2.6	A
		STR-W6252D		3.2	A
		STR-W6253D		10	A
Maximum Switching Current ²	I _{DMAX}	STR-W6251D	1-3	2.6	A
		STR-W6252D		3.2	A
		STR-W6253D		10	A
Avalanche Energy ³	E _{AS}	STR-W6251D	1-3	47	mJ
		STR-W6252D		62	mJ
		STR-W6253D		86	mJ
S/OCP Pin Voltage	V _{OCP}		3-5	-6 to 6	V
FM/ELP Pin Voltage	V _{FM}		7-5	-0.3 to 12	V
FM/ELP Pin Sink Current	I _{FM}		7-5	3	mA
FB Pin Voltage	V _{FB}	FB pin is open	6-5	-0.3 to 9	V
Controller Part Input Voltage	V _{CC}		4-5	0 to 32	V
MOSFET Power Dissipation ⁴	P _{D1}	STR-W6251D	1-3	25	W
		STR-W6252D		26	W
		STR-W6253D		27.5	W
		Without heatsink		1.3	W
Controller Part Power Dissipation	P _{D2}		4-5	0.8	W
Internal Frame Temperature in Operation	T _F	Recommended operating temperature is T _F = 105°C (max)	-	-20 to 115	°C
Operating Ambient Temperature	T _{op}		-	-20 to 115	°C
Storage Temperature	T _{stg}		-	-40 to 125	°C
Channel Temperature	T _{ch}		-	150	°C

¹Refer to MOSFET Safe Operating Area Curve.

²The Maximum Switching Current is the drain current determined by the drive voltage of the IC and threshold voltage (V_{th}) of the MOSFET.

³Refer to MOSFET Avalanche Energy Derating Coefficient Curve.

⁴Refer to MOSFET Temperature versus Power Dissipation Curve.

Electrical Characteristics of Control Part Unless specifically noted, T_A is 25°C, $V_{CC} = 18\text{ V}$

Characteristic	Symbol	Pin	Min.	Typ.	Max	Unit
Power Supply Startup Operation						
Operation Start Voltage	$V_{CC(ON)}$	4-5	13.9	15.5	17.1	V
Operation Stop Voltage	$V_{CC(OFF)}$	4-5	8.0	8.9	9.8	V
Circuit Current in Operation	$I_{CC(ON)}$	4-5	–	1.4	2.8	mA
Circuit Current in Non-Oscillation	$I_{CC(STOP)}$	4-5	–	0.8	1.3	mA
Circuit Current in Non-Operation	$I_{CC(OFF)}$	4-5	–	5	20	μA
Startup Current	$I_{STARTUP}$	4-5	–0.9	–1.6	–2.3	mA
Bias Assist Voltage	$V_{CC(BIAS)}$	4-5	13.6	15.2	16.8	V
Normal Operation						
FM/ELP Pin High Threshold Voltage	$V_{FM(H)}$	7-5	4.0	4.5	5.0	V
FM/ELP Pin Low Threshold Voltage	$V_{FM(L)}$	7-5	2.4	2.8	3.2	V
FM/ELP Pin Voltage Difference	ΔV_{FM}	7-5	1.4	1.7	1.8	V
FM/ELP Pin Source Current	$I_{FM(SRC)}$	7-5	–17.4	–13	–8.6	μA
FM/ELP Pin Sink Current	$I_{FM(SNK)}$	7-5	8.6	13	17.4	μA
Average Switching Frequency	$f_{OSC(AVG)}$	1-5	60	67	74	kHz
Frequency Modulation Deviation	Δf	1-5	4.8	6.9	9	kHz
Maximum Duty Cycle (On-duty)	D_{MAX}	1-5	71	75	79	%
FB Pin Maximum Feedback Current	$I_{FB(MAX)}$	6-5	–220	–160	–100	μA
Standby Operation Startup Voltage	V_{STBY}	6-5	0.99	1.10	1.21	V
Slope Compensation Startup Duty Cycle	D_{SLP}	6-5	–	27	–	%
Slope Compensation Rate	SLP	6-5	–22	–17	–12	mV/ μs
Protection Operation						
OCP Threshold Voltage at Zero Duty Cycle (0% On-duty)	V_{OCP1}	3-5	0.71	0.78	0.86	V
Drain Peak Current Compensation Coefficient	D_{PC}	–	1.5	1.9	2.3	mV/D%
OCP Threshold Voltage After Compensation	V_{OCP2}	3-5	0.82	0.93	1.04	V
LEB Time	t_{BW}	1-5	280	400	520	ns
OLP Delay Time*	t_{DLY}	1-5	–	200	–	ms
Circuit Current in OLP-Operation	$I_{CC(OLP)}$	4-5	–	410	700	μA
OVP Threshold Voltage	$V_{CC(OVP)}$	4-5	27	28.5	30	V
Latch Circuit Holding Current	$I_{CC(La.H)}$	4-5	–	140	220	μA
Latch Circuit Release Voltage	$V_{CC(La.OFF)}$	4-5	6.4	7.1	7.8	V
ELP Threshold Voltage	V_{ELP}	7-5	6.4	7.1	7.8	V
Sink Current in ELP Operation	I_{ELP}	7-5	–	55	100	μA
Thermal Shutdown Activating Temperature	$T_{J(TSD)}$	–	135	–	–	°C

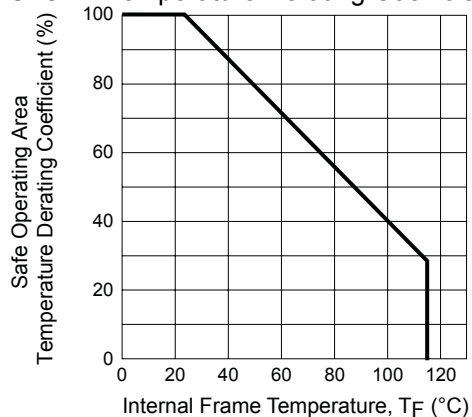
*Reference value of 47 nF capacitor between FM/ELP and GND Pins.

Electrical Characteristics of MOSFET Unless specifically noted, T_A is 25°C

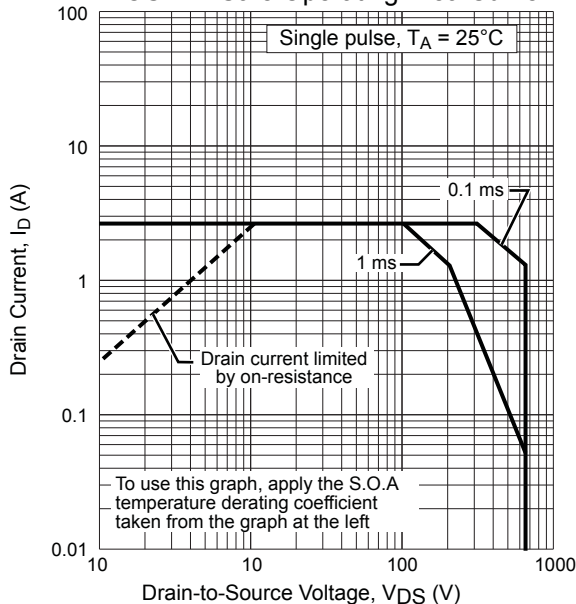
Characteristic	Symbol	Note	Pin	Min.	Typ.	Max.	Unit	
Drain-to-Source Breakdown Voltage	V_{DSS}		1-3	650	–	–	V	
Drain Leakage Current	I_{DSS}		1-3	–	–	300	μ A	
On-Resistance	$R_{DS(ON)}$	STR-W6251D	1-3	–	–	3.95	Ω	
		STR-W6252D		–	–	2.8	Ω	
		STR-W6253D		–	–	1.9	Ω	
Switching Time	t_r		1-3	–	–	400	ns	
Thermal Resistance	$R_{\theta ch-F}$	STR-W6251D	Between channel and internal frame	–	–	–	2.23	$^{\circ}$ C/W
		STR-W6252D			–	–	2.04	$^{\circ}$ C/W
		STR-W6253D			–	–	1.75	$^{\circ}$ C/W

**Typical Characteristic Performance
STR-W6251D**

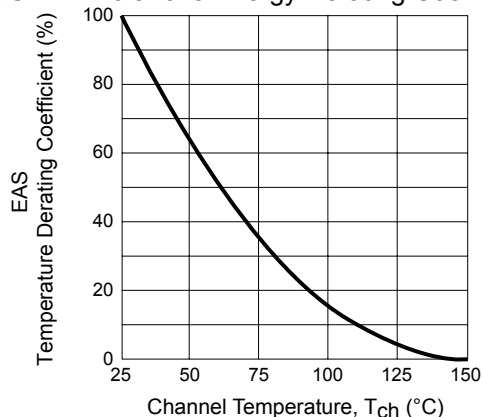
S. O. A. Temperature Derating Curve



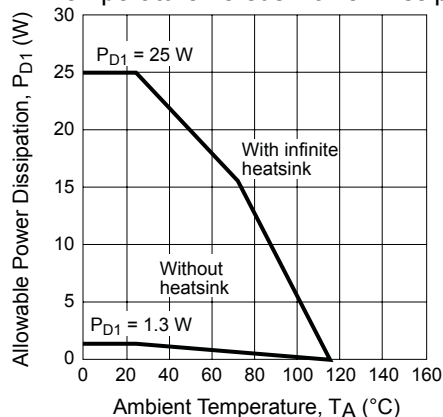
MOSFET Safe Operating Area Curve



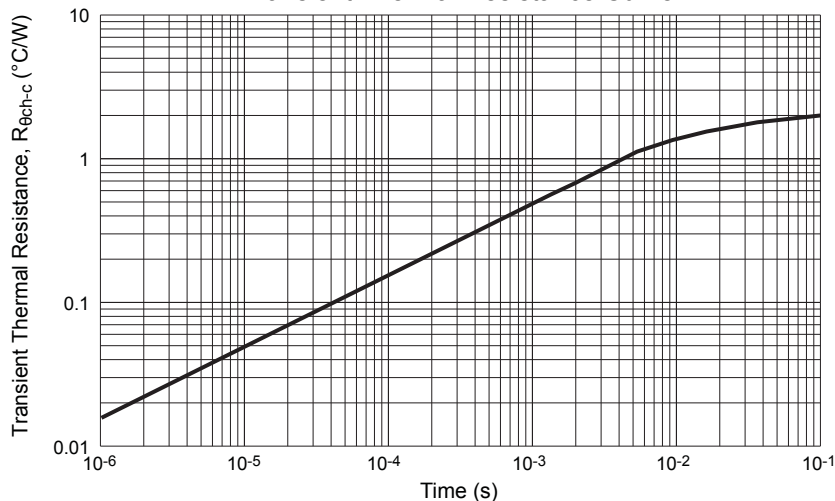
MOSFET Avalanche Energy Derating Curve



MOSFET Temperature versus Power Dissipation Curve

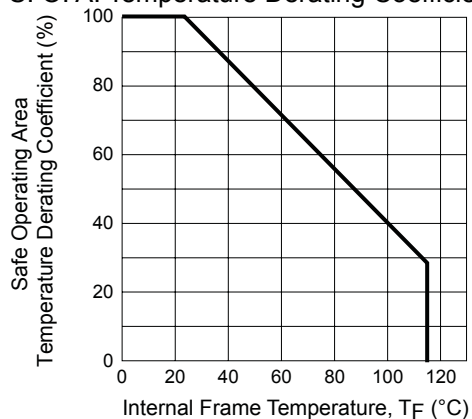


Transient Thermal Resistance Curve

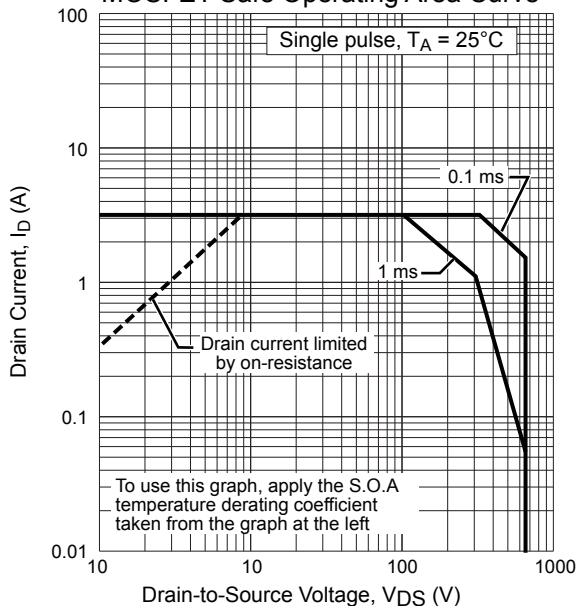


**Typical Characteristic Performance
STR-W6252D**

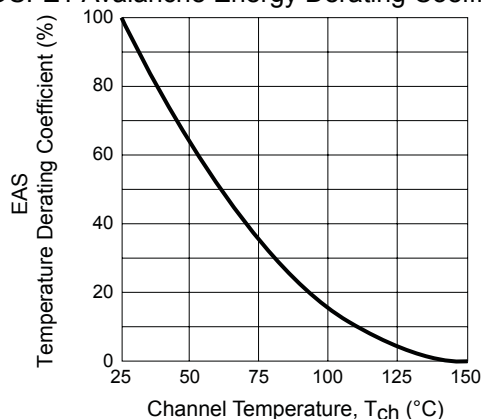
S. O. A. Temperature Derating Coefficient Curve



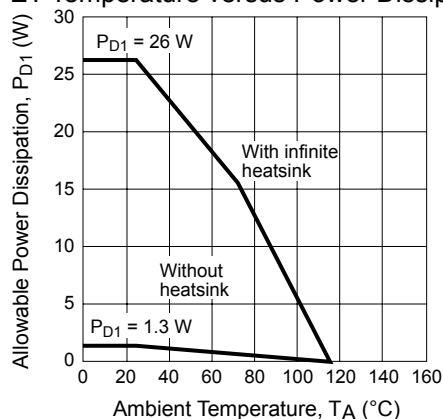
MOSFET Safe Operating Area Curve



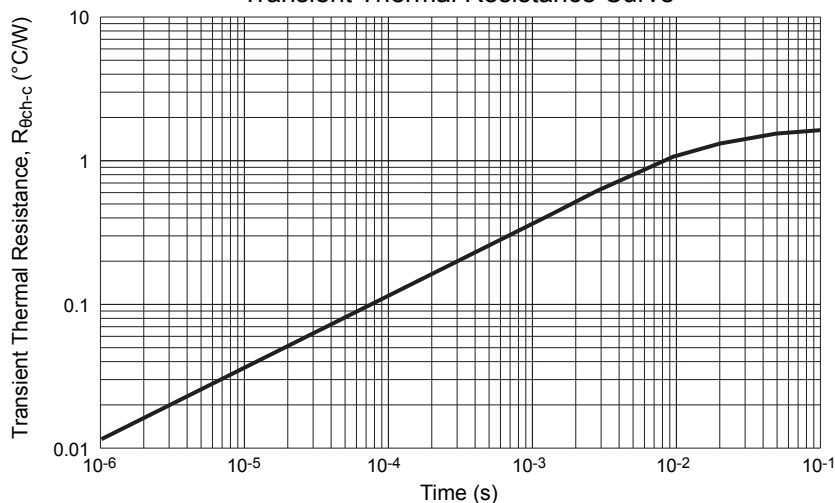
MOSFET Avalanche Energy Derating Coefficient Curve



MOSFET Temperature versus Power Dissipation Curve

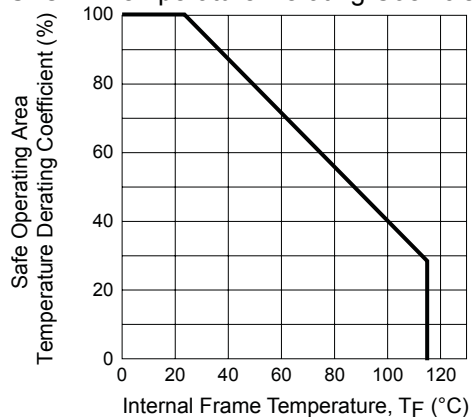


Transient Thermal Resistance Curve

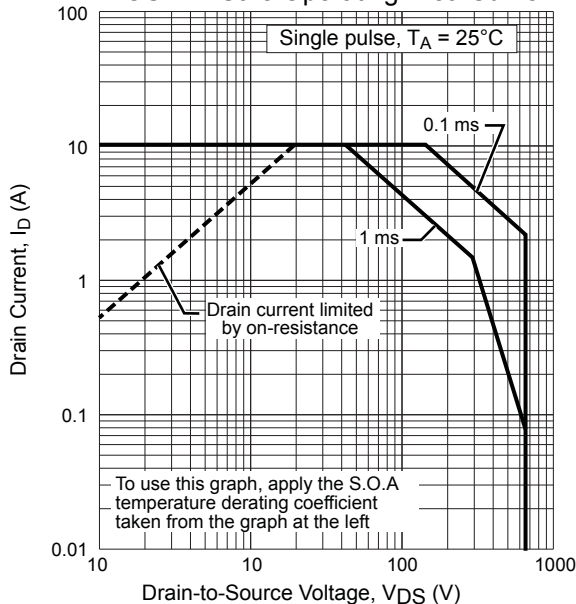


**Typical Characteristic Performance
STR-W6253D**

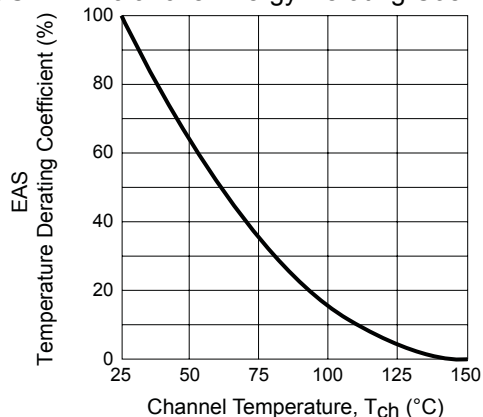
S. O. A. Temperature Derating Coefficient Curve



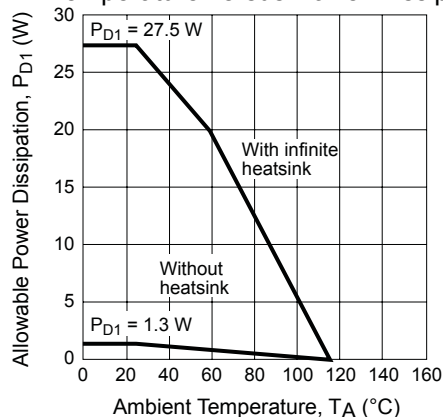
MOSFET Safe Operating Area Curve



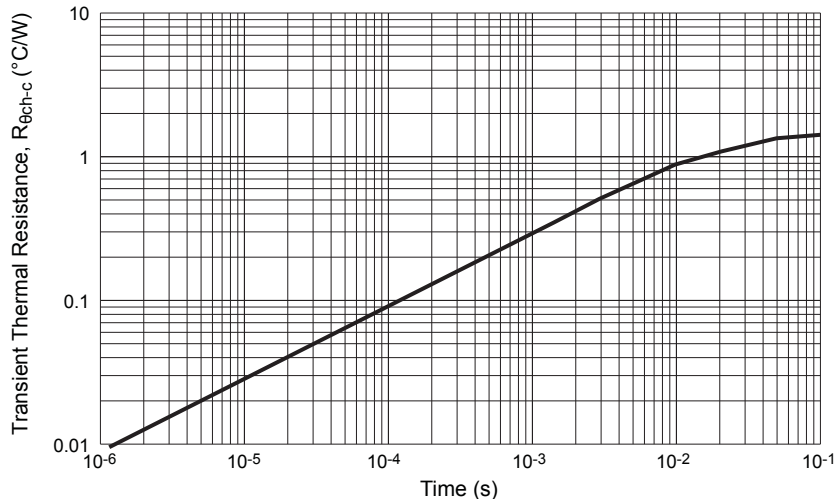
MOSFET Avalanche Energy Derating Coefficient Curve



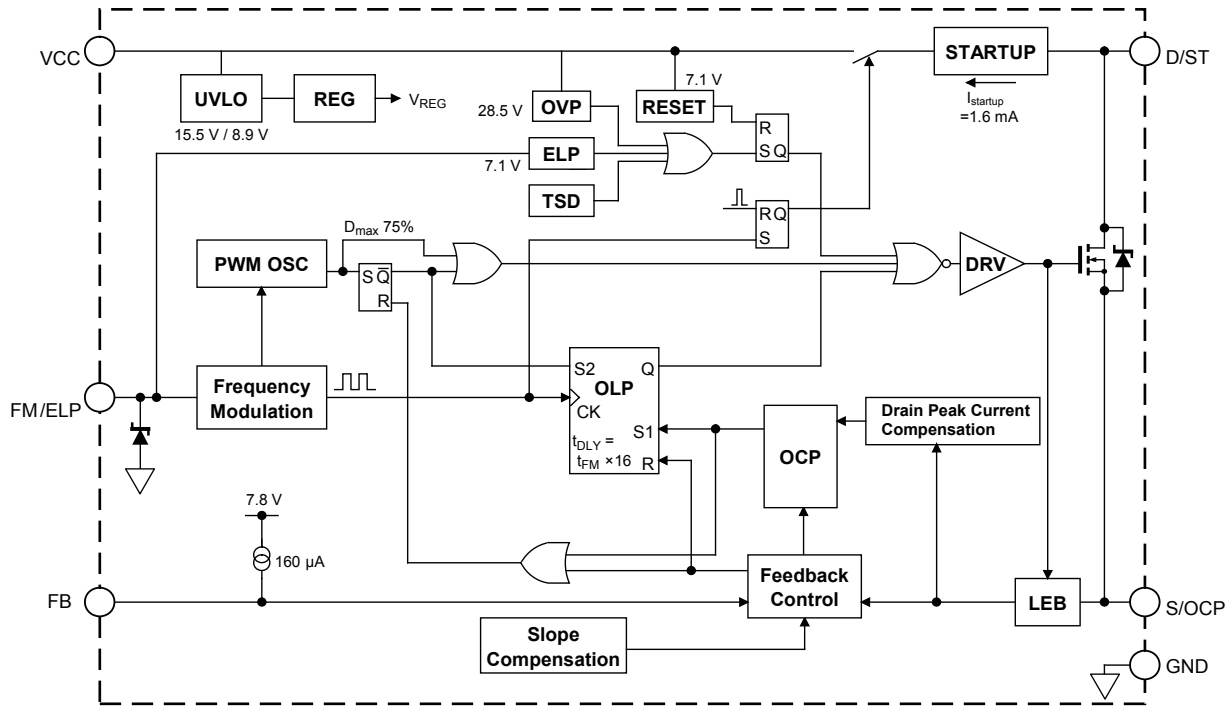
MOSFET Temperature versus Power Dissipation Curve



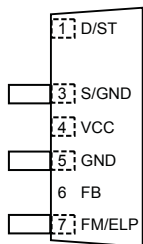
Transient Thermal Resistance Curve



Functional Block Diagram



Pin-out Diagram

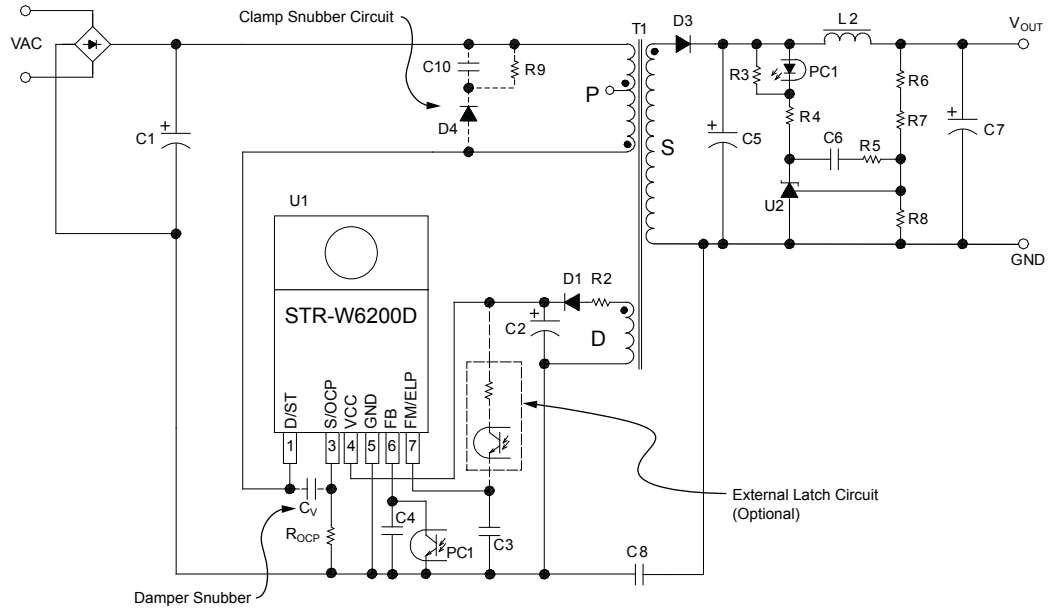


(LF2003)

Pin List Table

Number	Name	Function
1	D/ST	MOSFET drain and input of the startup current
2	-	(Pin removed)
3	S/OCP	MOSFET source and input of Overcurrent Protection (OCP) signal
4	VCC	Power supply voltage input for Control Part and input of Overvoltage Protection (OVP) signal
5	GND	Ground
6	FB	Input for constant voltage control signal
7	FM/ELP	Capacitor connection pin for frequency modulation and input of External Latch Protection

Typical Application Circuit



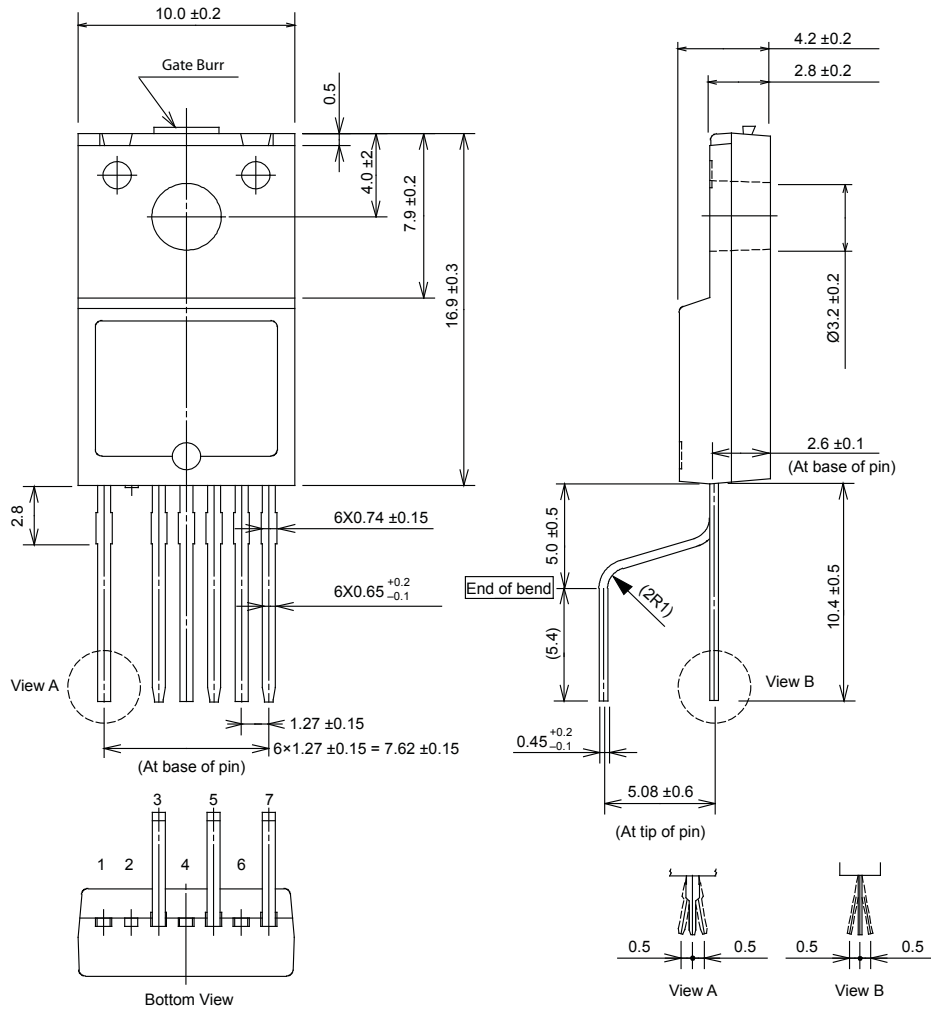
The following design feature should be observed: In applications having a power supply specified such that V_{DS} has large transient surge voltages, a clamp snubber circuit of a capacitor-resistor-diode (CRD) combination should be added on the primary winding, P, or a damper snubber circuit of a capacitor (C) or a resistor-capacitor (CR) combination should be added between the D/ST pins and the S/OCP pin.

**STR-W6251D,
STR-W6252D, and
STR-W6253D**

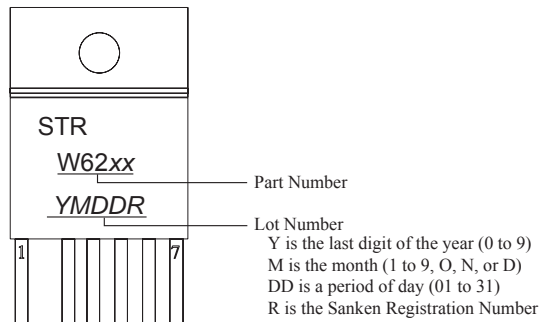
PWM Off-Line Switching Regulator ICs

Package Diagram

TO-220F-6L package
Leadform: 2003



Unit: mm
Dashed line at Gate Burr indicates protrusion of 0.3 mm (max)



Pin treatment Pb-free. Device composition compliant with the RoHS directive.

Because reliability can be affected adversely by improper storage environments and handling methods, please observe the following cautions.

Cautions for Storage

- Ensure that storage conditions comply with the standard temperature (5°C to 35°C) and the standard relative humidity (around 40% to 75%); avoid storage locations that experience extreme changes in temperature or humidity.
- Avoid locations where dust or harmful gases are present and avoid direct sunlight.
- Reinspect for rust on leads and solderability of the products that have been stored for a long time.

Cautions for Testing and Handling

When tests are carried out during inspection testing and other standard test periods, protect the products from power surges from the testing device, shorts between the product pins, and wrong connections. Ensure all test parameters are within the ratings specified by Sanken for the products.

Remarks About Using Silicone Grease with a Heatsink

- When silicone grease is used in mounting the products on a heatsink, it shall be applied evenly and thinly. If more silicone grease than required is applied, it may produce excess stress.
- Volatile-type silicone greases may crack after long periods of time, resulting in reduced heat radiation effect. Silicone greases with low consistency (hard grease) may cause cracks in the mold resin when screwing the products to a heatsink.

Our recommended silicone greases for heat radiation purposes, which will not cause any adverse effect on the product life, are indicated below:

Type	Suppliers
G746	Shin-Etsu Chemical Co., Ltd.
YG6260	Momentive Performance Materials Inc.
SC102	Dow Corning Toray Co., Ltd.

Cautions for Mounting to a Heatsink

- When the flatness around the screw hole is insufficient, such as when mounting the products to a heatsink that has an extruded (burred) screw hole, the products can be damaged, even with a lower than recommended screw torque. For mounting the products, the mounting surface flatness should be 0.05 mm or less.

- Please select suitable screws for the product shape. Do not use a flat-head machine screw because of the stress to the products. Self-tapping screws are not recommended. When using self-tapping screws, the screw may enter the hole diagonally, not vertically, depending on the conditions of hole before threading or the work situation. That may stress the products and may cause failures.
 - Recommended screw torque: 0.588 to 0.785 N•m (6 to 8 kgf•cm).
 - For tightening screws, if a tightening tool (such as a driver) hits the products, the package may crack, and internal stress fractures may occur, which shorten the lifetime of the electrical elements and can cause catastrophic failure. Tightening with an air driver makes a substantial impact. In addition, a screw torque higher than the set torque can be applied and the package may be damaged. Therefore, an electric driver is recommended.
- When the package is tightened at two or more places, first pre-tighten with a lower torque at all places, then tighten with the specified torque. When using a power driver, torque control is mandatory.

Soldering

- When soldering the products, please be sure to minimize the working time, within the following limits:
260±5°C 10±1 s (Flow, 2 times)
380±10°C 3.5±0.5 s (Soldering iron, 1 time)
- Soldering should be at a distance of at least 1.5 mm from the body of the products.

Electrostatic Discharge

- When handling the products, the operator must be grounded. Grounded wrist straps worn should have at least 1 MΩ of resistance from the operator to ground to prevent shock hazard, and it should be placed near the operator.
- Workbenches where the products are handled should be grounded and be provided with conductive table and floor mats.
- When using measuring equipment such as a curve tracer, the equipment should be grounded.
- When soldering the products, the head of soldering irons or the solder bath must be grounded in order to prevent leak voltages generated by them from being applied to the products.
- The products should always be stored and transported in Sanken shipping containers or conductive containers, or be wrapped in aluminum foil.

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- The contents in this document are subject to changes, for improvement and other purposes, without notice. Make sure that this is the latest revision of the document before use.
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In addition, it should be noted that since power devices or IC's including power devices have large self-heating value, the degree of derating of junction temperature affects the reliability significantly.

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