
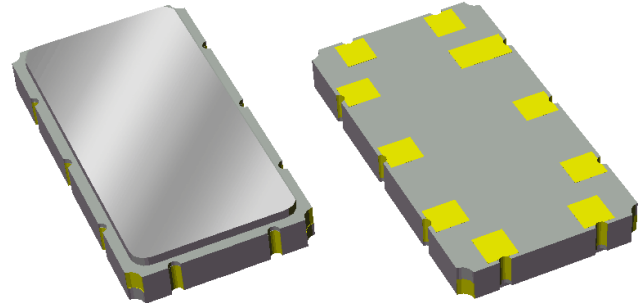


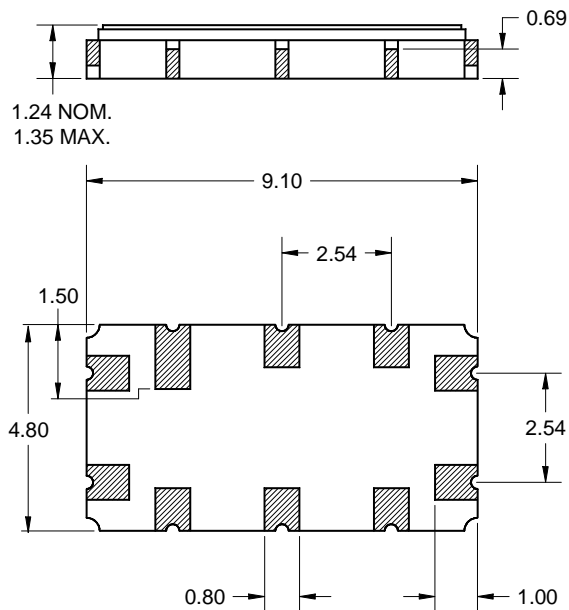
Features

- For multiple applications
- Usable bandwidth 14 MHz
- Low loss
- High attenuation
- Balanced or Single-ended operation
- Ceramic Surface Mount Package (SMP)
- Hermetic
- RoHS compliant (2002/95/EC), Pb-free 



Package

Surface Mount 9.10 x 4.80 x 1.24 mm
SMP-35C

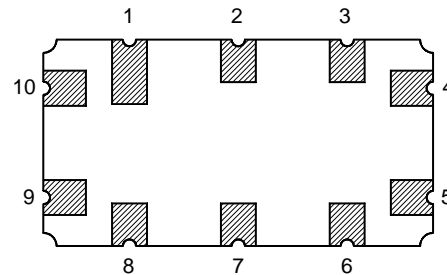


Dimensions shown are nominal in millimeters
All tolerances are ± 0.15 mm except overall
length and width ± 0.10 mm

Body: Al_2O_3 ceramic
Lid: Kovar, Ni plated
Terminations: Au plating 0.5 - 1.0 μ m,
over a 2 - 6 μ m Ni plating

Pin Configuration

Bottom View



Pin No. Balanced	Description
9	Input +
10	Input -
4	Output +
5	Output -
1,2,3,6,7,8	Case Ground

Pin No. Single-Ended	Description
9	Input
10	Ground
4	Output
5	Ground
1,2,3,6,7,8	Case Ground

Electrical Specifications ⁽¹⁾

Operating Temperature Range: ⁽²⁾ -40 to +85 °C

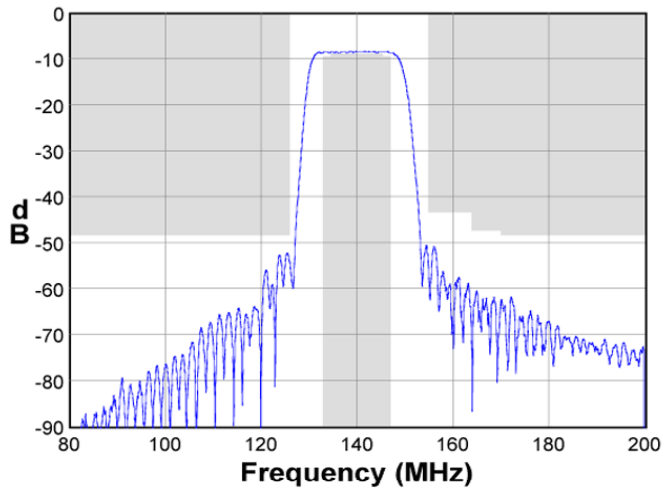
Parameter ⁽³⁾	Minimum	Typical ⁽⁵⁾	Maximum	Unit
Center Frequency	-	140	-	MHz
Insertion Loss @ Center Frequency	-	8.5	10	dB
Amplitude Variation				
134.5 – 145.5 MHz	-	0.3	0.6	dB p-p
133.0 – 147.0 MHz	-	0.3	1	dB p-p
Phase Linearity				
134.5 – 145.5 MHz	-	1.9	5	° p-p
133.0 – 147.0 MHz	-	2.5	5	° p-p
Average Group Delay				
134.5 – 145.5 MHz	0.48	0.53	0.58	µs
Input/Output Return Loss				
133 – 147MHz	7	11.5	-	dB
Relative Attenuation ⁽⁴⁾				
10 – 126 MHz	40	45	-	dB
155 – 164 MHz	35	43	-	dB
164 – 170 MHz	39	50	-	dB
170 – 250 MHz	40	52	-	dB
Triple Transit Suppression	40	46	-	dB
Source Impedance (balanced or single-ended) ⁽⁶⁾	-	50	-	Ω
Load Impedance (balanced or single-ended) ⁽⁶⁾	-	50	-	Ω

Notes:

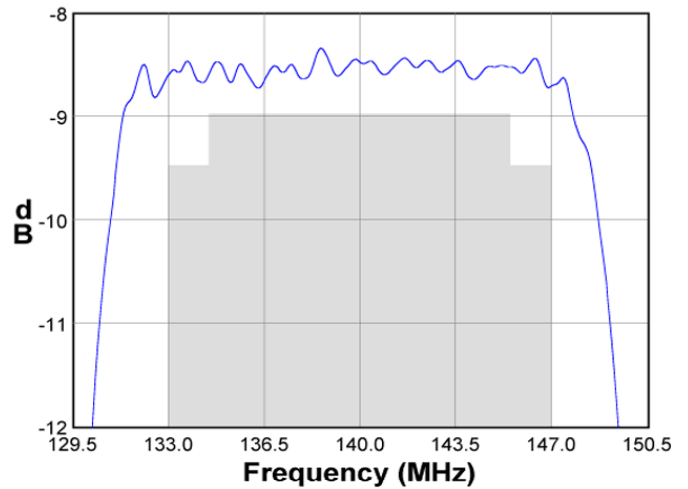
1. All specifications are based on the TriQuint matching schematics shown on page 4
2. In production, devices will be tested at room temperature to a guardbanded specification to ensure electrical compliance over temperature
3. Electrical margin has been built into the design to account for the variations due to temperature drift and manufacturing tolerances
4. Relative to insertion loss at center frequency
5. Typical values are based on average measurements at room temperature
6. This is the optimum impedance in order to achieve the performance shown

Typical Performance (at room temperature)

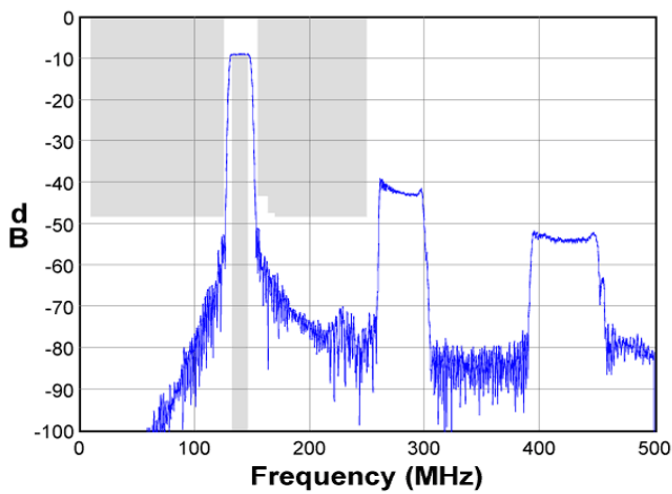
Frequency Response



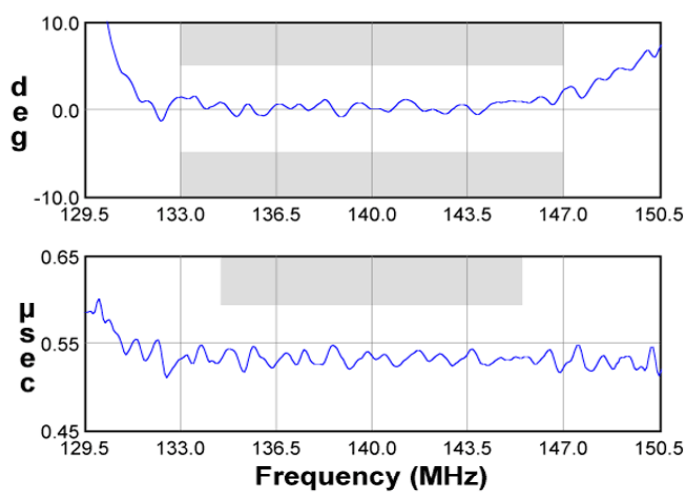
Passband Response



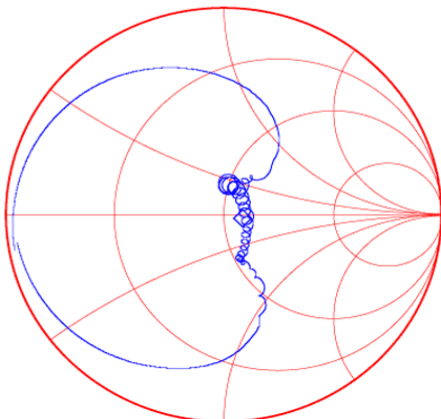
Wideband Response



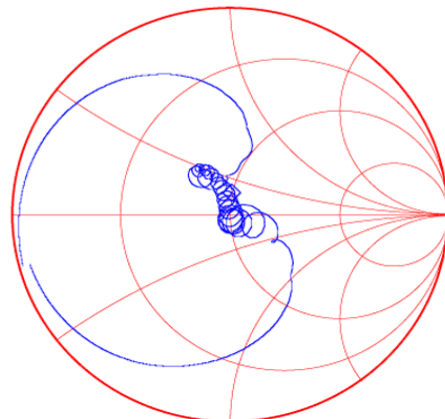
Phase / Group Delay



Input Smith Chart

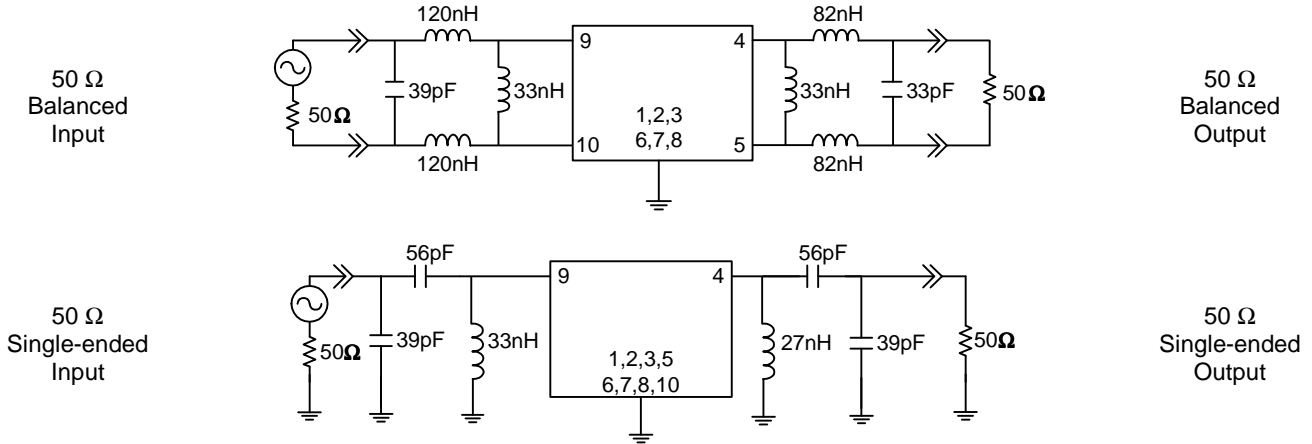


Output Smith Chart

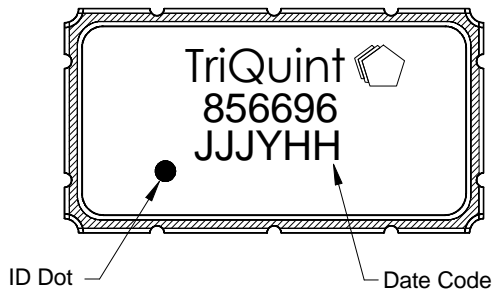


Matching Schematics

Actual matching values may vary due to PCB layout and parasitics

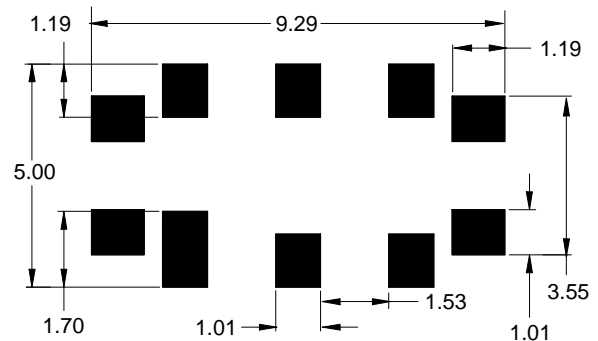


Marking



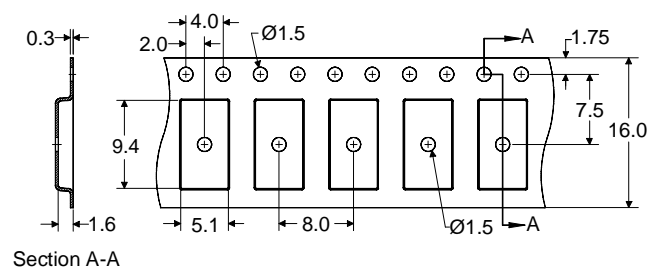
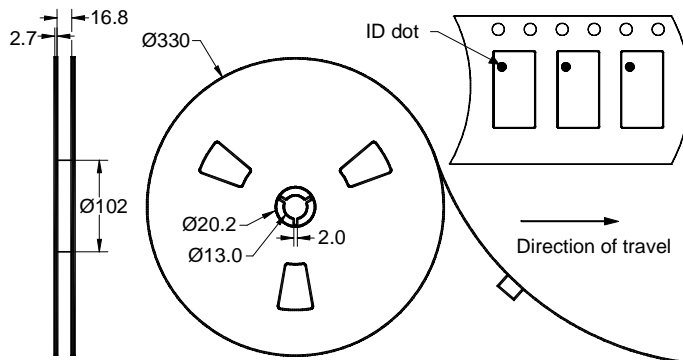
The date code consists of: day of the current year (Julian, 3 digits), last digit of the year (1 digit) and hour (2 digits)

PCB Footprint



This footprint represents a recommendation only
Dimensions shown are nominal in millimeters

Tape and Reel




Dimensions shown are nominal in millimeters
Packaging quantity: 4000 units/reel

Maximum Ratings


Parameter	Symbol	Minimum	Maximum	Unit
Operating Temperature Range	T	-40	+85	°C
Storage Temperature Range	T _{stg}	-55	+125	°C
Pyroelectric Voltage	V _{Pyro}	-	50	mV p-p
Input Power	P _{in}	-	+10	dBm

Important Notes

Warnings

- Electrostatic Sensitive Device (ESD) 
- Avoid ultrasonic exposure

RoHS Compliance

- This product complies with EU directive 2002/95/EC (RoHS) 

Solderability

- Compatible with JEDEC J-STD-020C **Pb-free** process, **260°C** peak reflow temperature ([see soldering profile](#))

Links to Additional Technical Information

[PCB Layout Tips](#)

[Qualification Flowchart](#)

[Soldering Profile](#)

[S-Parameters](#)

[RoHS Information](#)

[Other Technical Information](#)

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