
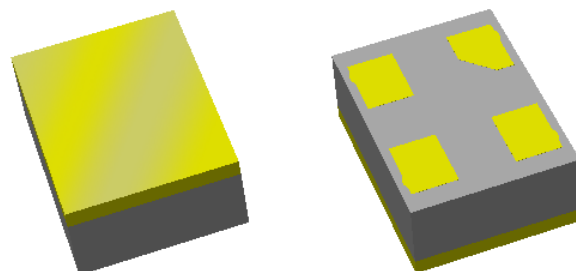


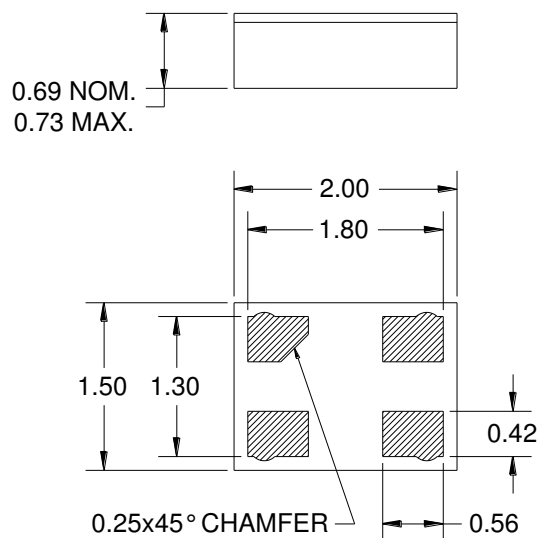
**Features**

- For ISM Band applications
- Usable bandwidth 10 MHz
- Low loss
- Single-ended operation
- Chip Scale Package (CSP)
- Hermetic
- **RoHS** compliant (2002/95/EC), **Pb-free** 



**Package**

Surface Mount 2.00 x 1.50 x 0.69 mm  
CSP-8A

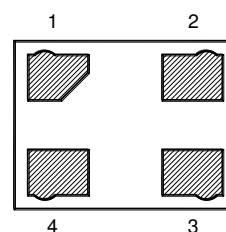


Dimensions shown are nominal in millimeters  
All tolerances are  $\pm 0.10$ mm

Body:  $Al_2O_3$  ceramic  
Lid: Kovar or Alloy 42, Au over Ni plated  
Terminations: Au plating 0.5 - 1.0 $\mu$ m,  
over a 2 - 6 $\mu$ m Ni plating

**Pin Configuration**

Bottom View



Pin No.	Description
1	Input
3	Output
2,4	Case ground

**Electrical Specifications <sup>(1)</sup>**

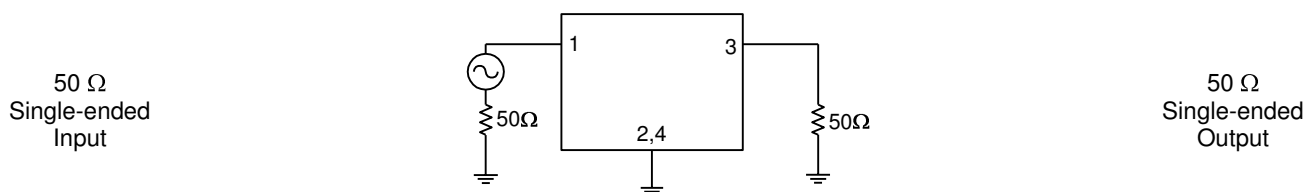
**Operating Temperature Range: <sup>(2)</sup>** -40°C to +85 °C

Parameter <sup>(3)</sup>	Minimum	Typical <sup>(4)</sup>	Maximum	Unit
<b>Center Frequency</b>	-	875	-	MHz
<b>Maximum Insertion Loss</b> 870 – 880 MHz	-	1.8	3	dB
<b>Absolute Attenuation <sup>(5)</sup></b>				
10 – 810 MHz	40	52	-	dB
810 – 848 MHz	35	42	-	dB
924 – 960 MHz	35	48	-	dB
960 – 1110 MHz	40	52	-	dB
1110 – 3000 MHz	30	39	-	dB
<b>Amplitude Variation</b> 870 – 880 MHz	-	0.15	1.0	dB p-p
<b>Group Delay Variation</b> 870 – 880 MHz	-	8	50	ns p-p
<b>Input/Output Return Loss</b> 870 – 880 MHz	10	14	-	dB
<b>Source Impedance (single-ended) <sup>(6)</sup></b>	-	50	-	Ω
<b>Load Impedance (single-ended) <sup>(6)</sup></b>	-	50	-	Ω

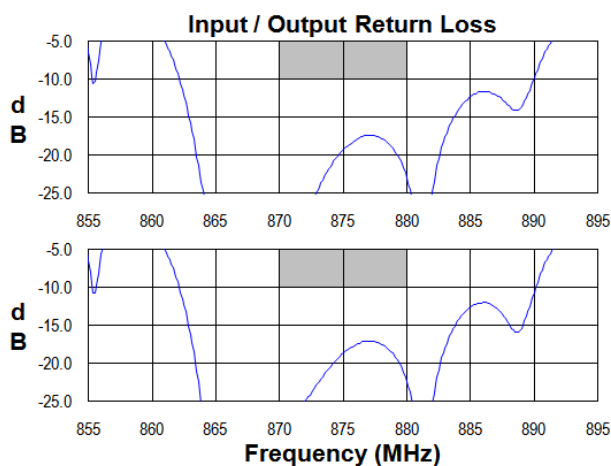
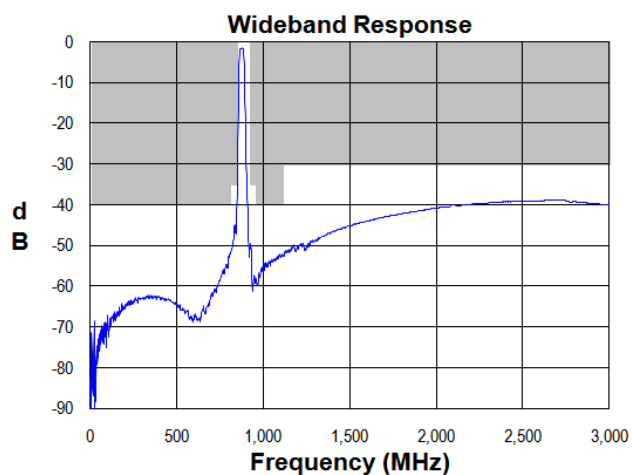
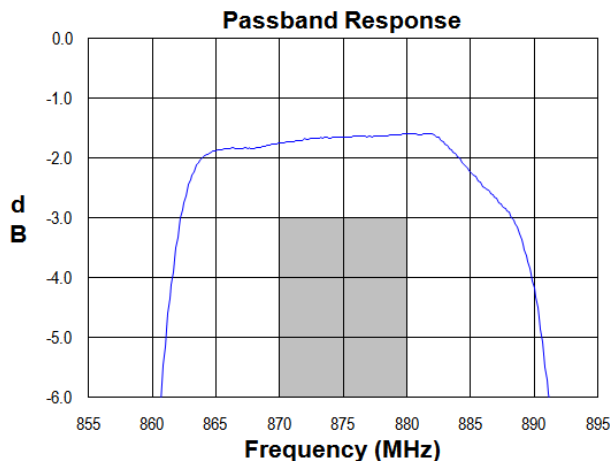
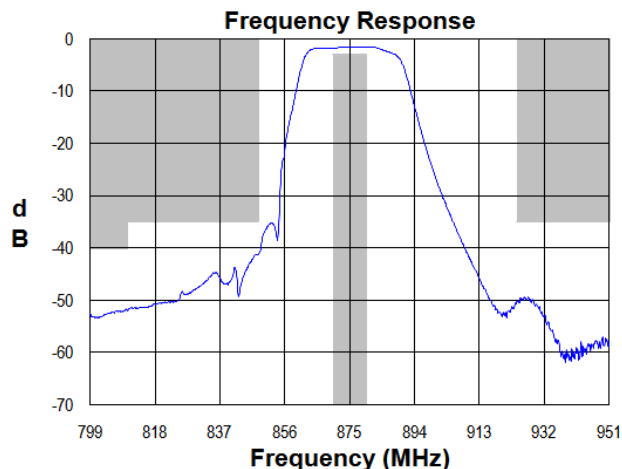
**Notes:**

1. All specifications are based on the test circuit below
2. In production, devices will be tested at room temperature to a guardbanded specification to ensure electrical compliance over temperature
3. Electrical margin has been built into the design to account for the variations due to temperature drift and manufacturing tolerances
4. Typical values are based on average measurements at room temperature
5. Relative to zero dB
6. This is the optimum impedance in order to achieve the performance

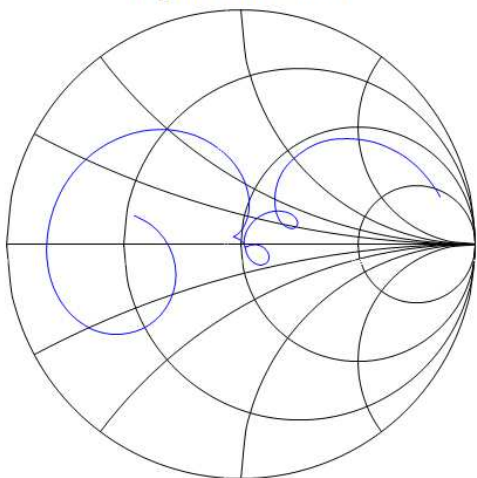
**Test Circuit:**



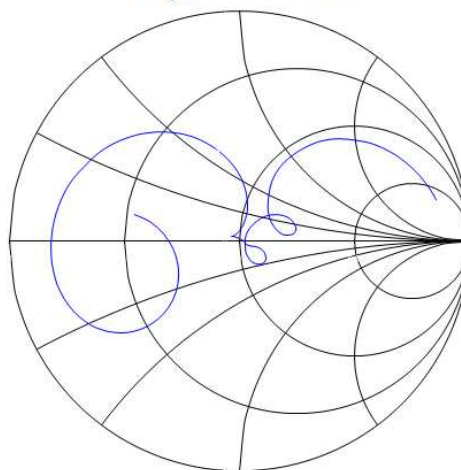
**Typical Performance (at room temperature)**



**Input Smith Chart**

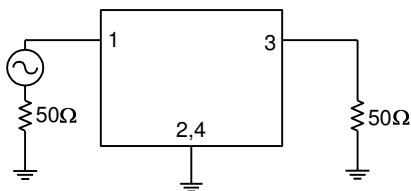


**Output Smith Chart**



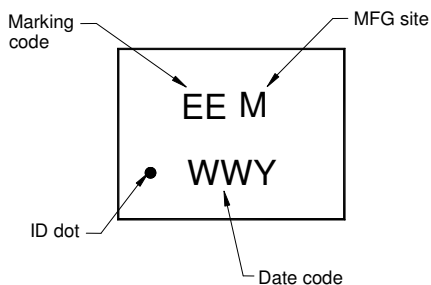
**Matching Schematics**

50 Ω  
Single-ended  
Input



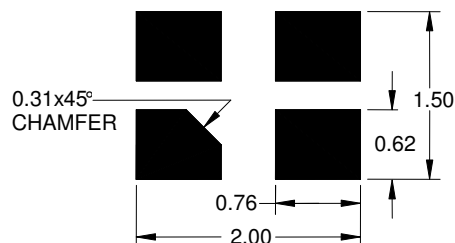
50 Ω  
Single-ended  
Output

**Marking**



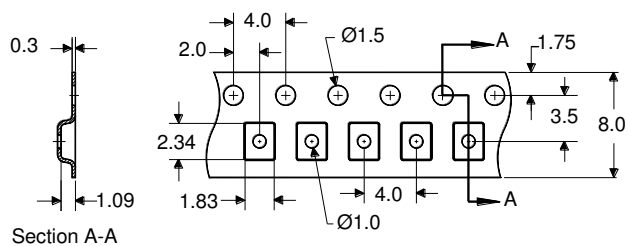
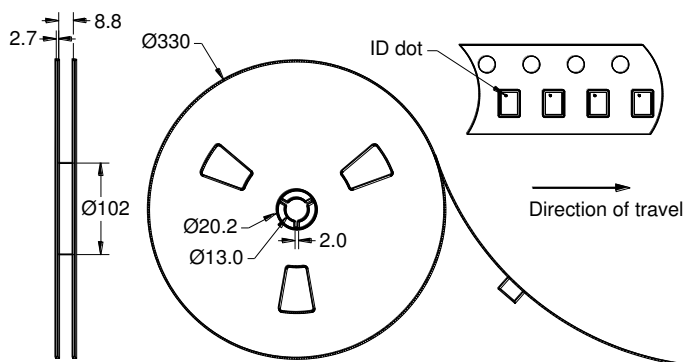
The date code consists of: WW = 2 digit week,  
Y = last digit of year, M = manufacturing site code

**PCB Footprint**



This footprint represents a recommendation only  
Dimensions shown are nominal in millimeters

**Tape and Reel**




Dimensions shown are nominal in millimeters  
Packaging quantity: 10000 units/reel

### Maximum Ratings


Parameter	Symbol	Minimum	Maximum	Unit
Operating Temperature Range	T	-40	+85	°C
Storage Temperature Range	T <sub>stg</sub>	-40	+85	°C

### Important Notes

#### Warnings

- Electrostatic Sensitive Device (ESD) 
- Avoid ultrasonic exposure

#### RoHS Compliance

- This product complies with EU directive 2002/95/EC (RoHS) 

#### Solderability

- Compatible with JEDEC J-STD-020C **Pb**-free process, **260 °C** peak reflow temperature ([see soldering profile](#))

### Links to Additional Technical Information

[PCB Layout Tips](#)

[Qualification Flowchart](#)

[Soldering Profile](#)

[S-Parameters](#)

[RoHS Information](#)

[Other Technical Information](#)

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