### Voltage Controlled Oscillator 12.2 - 13.8 GHz

#### Features

- Low Phase Noise
- Wide Tuning Range
- Divide-by-Two Output
- Integrated Buffer Amplifier
- Excellent Temperature Stability
- +5V Bias
- Lead-Free 5 mm 32-Lead PQFN Package
- Halogen-Free "Green" Mold Compound
- RoHS\* Compliant and 260°C Reflow Compatible

#### Description

The MAOC-009270 is an InGaP HBT-based voltage controlled oscillator for frequency generation. No external matching components are required. This VCO is easily integrated into a phase lock loop using the divide-by-two output. The extremely low phase noise makes this part ideal for many radio applications including high capacity digital radios.

The MAOC-009270 primary applications are Point-to-Point Radio, Point-to-Multipoint Radio, Communications Systems, and Low Phase Noise applications.

The 5 mm PQFN package has a lead-free finish that is RoHS compliant and compatible with a 260°C reflow temperature. The package also features low lead inductance and an excellent thermal path.

### **Ordering Information<sup>1</sup>**

1

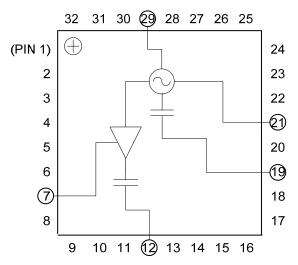
Part Number	Package
MAOC-009270-TR0500	500 piece reel
MAOC-009270-TR1000	1000 piece reel
MAOC-009270-SMB003	Sample Board

1. Reference Application Note M513 for reel size information.



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#### **Block Diagram**



### Pin Designations<sup>2</sup>

Pin	Function	Pin	Function	
1	N/C	17	N/C	
2	N/C	18	N/C	
3	N/C	19	RF	
4	N/C	20	N/C	
5	N/C	21	V <sub>cc</sub>	
6	N/C	22	N/C	
7	VBUFFER	23	N/C	
8	N/C	24	N/C	
9	N/C	25	N/C	
10	N/C	26	N/C	
11	N/C	27	N/C	
12	RF/2	28	N/C	
13	N/C	29	V <sub>TUNE</sub>	
14	N/C	30	N/C	
15	N/C	31	N/C	
16	N/C	32	N/C	

 The exposed pad centered on the package bottom must be connected to RF and DC ground. Connecting all N/C pins to RF/DC Ground in the layout is also recommended.

\* Restrictions on Hazardous Substances, European Union Directive 2002/95/EC.

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# Technology Solutions

## Voltage Controlled Oscillator 12.2 - 13.8 GHz

# Electrical Specifications: $T_A$ =+25°C, $V_{CC}$ = $V_{BUFFER}$ = 5.0 $V^3$ , $Z_0$ = 50 $\Omega$

Parameter	Test Conditions	Units	Min.	Тур.	Max.
Output Power	RF Port, 12.2 - 13.8 GHz RF/2 Port, 6.1 - 6.9 GHz	dBm	3 -3	7 1	_
SSB Phase Noise V <sub>CC</sub> =V <sub>BUFFER</sub> =V <sub>TUNE</sub> =5V	RF Port, 10KHZ Offset RF Port, 100KHZ Offset	dBc/Hz	_	-78 -107	_
Harmonics/Subharmonics $V_{CC}=V_{BUFFER}=V_{TUNE}=5V$	RF Port, <sup>1</sup> /₂ F₀ RF Port, 2 F₀	dBc	_	-18 -38	_
Pulling (Sensitivity to Match) V <sub>CC</sub> =V <sub>BUFFER</sub> =V <sub>TUNE</sub> =5V	RF Port, VSWR = 1.95:1 to 2.25:1	MHz pk-pk	_	17.6	_
Pushing (Sensitivity to Supply Voltage)	RF Port, V <sub>TUNE</sub> = 5 V RF/2 Port, V <sub>TUNE</sub> = 5 V	MHz/V	_	14 7	
Frequency Drift Rate (Sensitivity to Temperature)	RF Port, 12.2 - 13.8 GHz RF/2 Port, 6.1 - 6.9 GHz	MHz/ºC	_	1.2 0.6	
Output Return Loss	RF Port, 12.2 - 13.8 GHz RF/2 Port, 6.1 - 6.9 GHz	dB		3 6	_
Tuning Sensitivity @ RF Port	V <sub>TUNE</sub> = 5 V	GHz/V	_	0.22	
Supply Current	I <sub>total</sub> (I <sub>CC</sub> + I <sub>buffer</sub> ) I <sub>CC</sub> Ibuffer	mA	_	145 125 20	205 175 30
Tune Voltage	V <sub>TUNE</sub>	V	1		13
Tuning Current Leakage	V <sub>TUNE</sub> = 13 V	μA	—	5	10

3. VCO can operate over the 4.75 V to 5.25 V supply voltage range.

## Absolute Maximum Ratings 4,5,6

Parameter	Absolute Maximum
Supply Voltage (V <sub>CC</sub> & V <sub>BUFFER</sub> )	+5.5 Vdc
V <sub>TUNE</sub>	0 to +15 Vdc
Storage Temperature	-55°C to +150°C
Operating Temperature	-40°C to +85°C
Case Temperature (T <sub>C</sub> ) (measured @ exposed pad)	+100°C
Junction Temperature <sup>7</sup>	+135℃

4. Exceeding any one or combination of these limits may cause permanent damage to this device.

- 5. M/A-COM Technology Solutions does not recommend sustained operation near these survivability limits.
- Operating at nominal conditions with T<sub>J</sub> ≤ +135°C will ensure MTBF > 2.5 x 10<sup>6</sup> hours.
- 7. Junction Temperature  $(T_J) = T_C + \Theta jc * (V * I)$ Typical thermal resistance  $(\Theta jc) = 35^{\circ} C/W$ .

a) For  $T_C = 25^{\circ}C$ ,  $T_J = 50^{\circ}C$  @ 5 V, 145 mA

b) For 
$$T_C$$
 = 85°C,  $T_J$  = 111°C @ 5 V, 150 mA

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### **Handling Procedures**

Please observe the following precautions to avoid damage:

### **Static Sensitivity**

Gallium Arsenide Integrated Circuits are sensitive to electrostatic discharge (ESD) and can be damaged by static electricity. Proper ESD control techniques should be used when handling these devices.



ESD Rating: Class 1A

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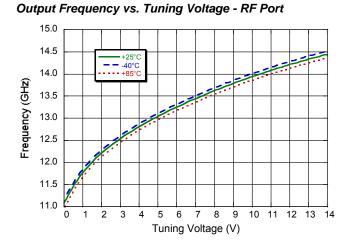
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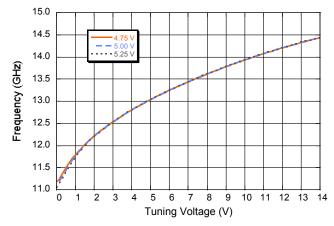


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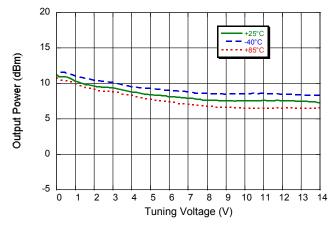
### Typical Performance Curves: $V_{CC} = V_{BUFFER} = 5V$ , $T_A = +25^{\circ}C$ (unless otherwise indicated)



Output Frequency vs. Tuning / Supply Voltage - RF Port





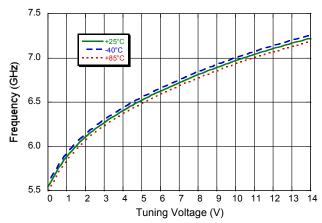


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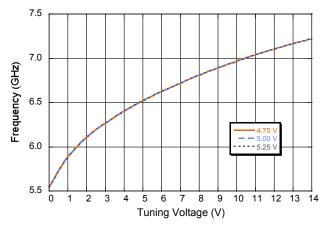
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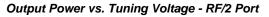
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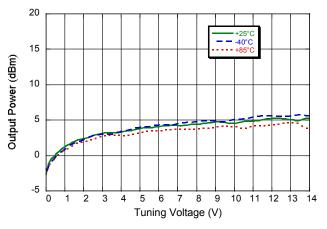
Output Frequency vs. Tuning Voltage - RF/2 Port



Output Frequency vs. Tuning / Supply Voltage - RF/2 Port







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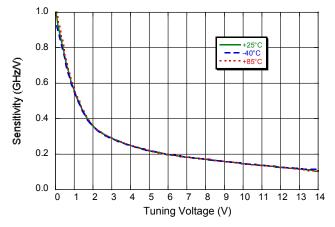


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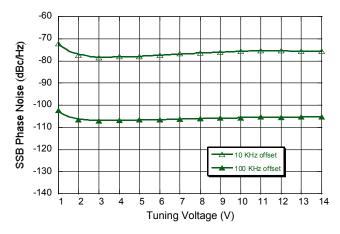
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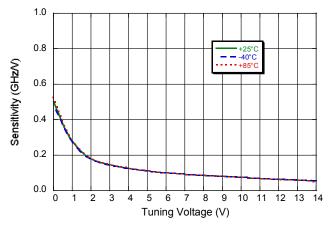
Frequency Sensitivity vs. Tuning Voltage - RF Port



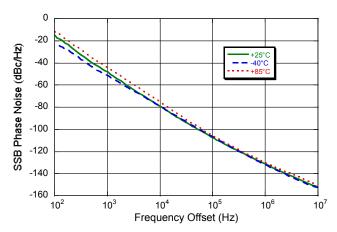
Single Side Band Phase Noise vs. Tuning Voltage RF Port



Frequency Sensitivity vs. Tuning Voltage - RF/2 Port



Single Side Band Phase Noise vs. Frequency Offset RF Port ( $V_{TUNE} = 5V$ )



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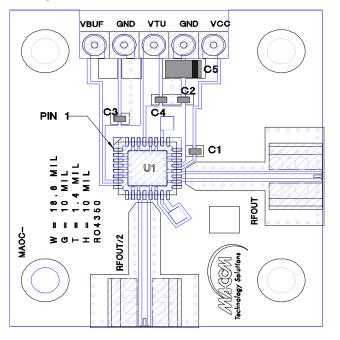
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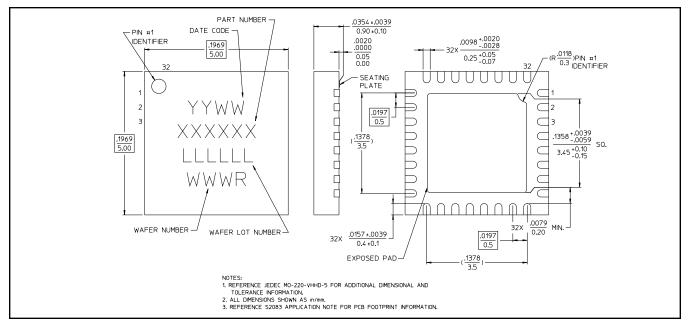
### Sample Board



#### Parts List

Component	Value	Case Size
C1	100 pF	0402
C2, C3, C4	0.1 µF	0402
C5	10 µF Tantalum	1206

### Lead-Free 5 mm 32-Lead PQFN<sup>†</sup>



<sup>†</sup> Reference Application Note S2083 for lead-free solder reflow recommendations. Meets JEDEC moisture sensitivity level 1 requirements. Plating is 100% matte tin over copper.

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