

< IGBT MODULES >

CM100DY-34A

HIGH POWER SWITCHING USE INSULATED TYPE

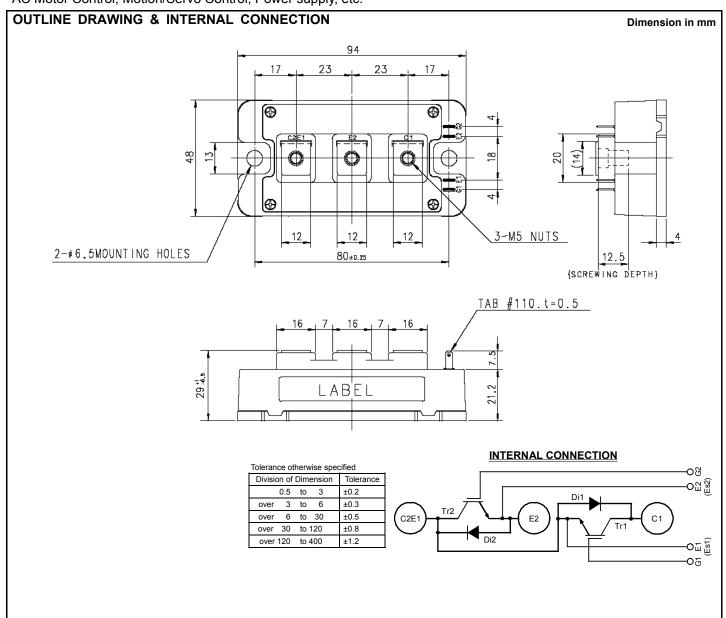


- Flat base Type
- Copper base plate
- •RoHS Directive compliant
- •UL Recognized under UL1557, File E323585

Dual (Half-Bridge)

APPLICATION

AC Motor Control, Motion/Servo Control, Power supply, etc.



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	ABSOLUTE MAXIMUM RATINGS	(T	_i =25 °C,	unless	otherwise s	pecified)	
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Symbol	Item	Conditions	Rating	Unit	
V _{CES}	Collector-emitter voltage	G-E short-circuited	1700	V	
V_{GES}	Gate-emitter voltage	C-E short-circuited	±20	V	
Ic	Collector current	DC, T _C =108 °C (Note.2, 4)	100	^	
I _{CRM}	- Collector current	Pulse, Repetitive (Note.3)	200	Α	
P _{tot}	Total power dissipation	T _C =25 °C (Note.2, 4)	960	W	
I _E (Note.1)	Emitter current	T _C =25 °C (Note.2, 4)	100	^	
I _{ERM} (Note.1)	- Emilier current	Pulse, Repetitive (Note.3)	200	A	
Tj	Junction temperature	-	-40 ~ +150	°C	
T _{stg}	Storage temperature	-	-40 ~ +125		
V _{isol}	Isolation voltage	Terminals to base plate, RMS, f=60 Hz, AC 1 min	3500	V	

ELECTRICAL CHARACTERISTICS (T_i=25 °C, unless otherwise specified)

Symbol	Item	Conditions	Conditions		Limits		Unit
Symbol	item	Conditions		Min.	Тур.	Max.	Offic
I _{CES}	Collector-emitter cut-off current	V _{CE} =V _{CES} , G-E short-circuited		-	-	1.0	mA
I _{GES}	Gate-emitter leakage current	V _{GE} =V _{GES} , C-E short-circuited		-	-	2.0	μΑ
$V_{GE(th)}$	Gate-emitter threshold voltage	I _C =10 mA, V _{CE} =10 V		5.5	7.0	8.5	V
	Collector-emitter saturation voltage	I _C =100 A (Note.5),	I _C =100 A (Note.5) , T _j =25 °C		2.2	2.8	V
V _{CEsat}	Collector-entitler saturation voltage	V _{GE} =15 V	T _j =125 °C	-	2.45	-	V
Cies	Input capacitance		V _{CE} =10 V, G-E short-circuited		-	24.7	nF
Coes	Output capacitance	V _{CE} =10 V, G-E short-circuited			-	2.8	
Cres	Reverse transfer capacitance]		-	-	0.53	
Q _G	Gate charge	V _{CC} =1000 V, I _C =100 A, V _{GE} =15 V		-	670	-	nC
t _{d(on)}	Turn-on delay time	V =1000 V I =100 A V =11	- V _{CC} =1000 V, I _C =100 A, V _{GF} =±15 V,		-	200	- ns
t _r	Rise time	V _{CC} =1000 V, I _C =100 A, V _{GE} =±15 V,		-	-	150	
t _{d(off)}	Turn-off delay time	D =4.0.0 Industrial land	R_G =4.8 Ω, Inductive load		-	550	
t _f	Fall time	$R_{\rm G}$ =4.8 Ω , inductive load		-	-	350	
V _{EC} (Note.1)	Emitter-collector voltage	I _E =100 A (Note.5), G-E short-cir	I _E =100 A (Note.5), G-E short-circuited		2.3	3.0	V
t _{rr} (Note.1)	Reverse recovery time	V _{CC} =1000 V, I _E =100 A, V _{GE} =±15 V,		-	-	300	ns
Q _{rr} (Note.1)	Reverse recovery charge	R_G =4.8 Ω , Inductive load		-	10	-	μC
Eon	Turn-on switching energy per pulse	V _{CC} =1000 V, I _C =I _E =100 A,		-	21.3	-	1
E _{off}	Turn-off switching energy per pulse	V_{GE} =±15 V, R_{G} =4.8 Ω , T_{j} =125	V_{GE} =±15 V, R _G =4.8 Ω , T _j =125 °C,		30	-	mJ
E _{rr} (Note.1)	Reverse recovery energy per pulse	Inductive load	Inductive load		33	-	mJ
r _g	Internal gate resistance	Per switch, T _c =25 °C	Per switch, T _c =25 °C		0	-	Ω

THERMAL RESISTANCE CHARACTERISTICS

Symbol Item	Itom	Conditions	Limits			Unit
	Conditions	Min.	Тур.	Max.	Uill	
$R_{th(j-c)Q}$	Thermal resistance (Note.2)	Junction to case, per IGBT	-	-	0.13	K/W
$R_{th(j-c)D}$	Thermal resistance	Junction to case, per FWDi	-	-	0.21	K/W
R _{th(c-s)} Contact thermal resistan	Contact thermal registance (Note.2)	Case to heat sink, per 1/2 module,	-	0.022	-	K/kW
	Contact thermal resistance	Thermal grease applied (Note.6)				

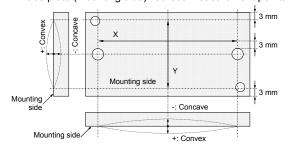
MECHANICAL CHARACTERISTICS

Symbol	Item	Conditions		Limits			Unit
Symbol		Conditions		Min.	Тур.	Max.	Offic
M_t	Mounting torque	Main terminals	M 5 screw	2.5	3.0	3.5	N·m
Ms	Modriting torque	Mounting to heat sink	M 6 screw	3.5	4.0	4.5	N·m
m	Weight	-		-	310	-	g
ec	Flatness of base plate	On the centerline X, Y (Note.7)		-100	-	+100	μm

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Note1. Represent ratings and characteristics of the anti-parallel, emitter-collector free wheeling diode (FWDi).

- 2. Case temperature (T_C) and heat sink temperature (T_s) are defined on the each surface (mounting side) of base plate and heat sink just under the chips. Refer to the figure of chip location.
 - The heat sink thermal resistance should measure just under the chips.
- 3. Pulse width and repetition rate should be such that the device junction temperature (T_i) dose not exceed T_{jmax} rating.
- 4. Junction temperature (T_j) should not increase beyond T_{jmax} rating.
- 5. Pulse width and repetition rate should be such as to cause negligible temperature rise. Refer to the figure of test circuit.
- 6. Typical value is measured by using thermally conductive grease of λ =0.9 W/(m·K).
- 7. Base plate (mounting side) flatness measurement points (X, Y) are as follows of the following figure.

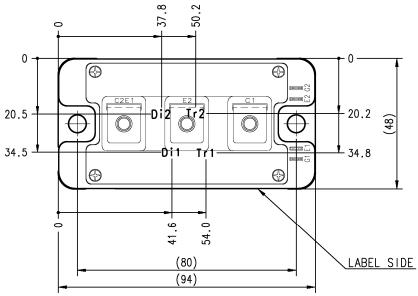


RECOMMENDED OPERATING CONDITIONS

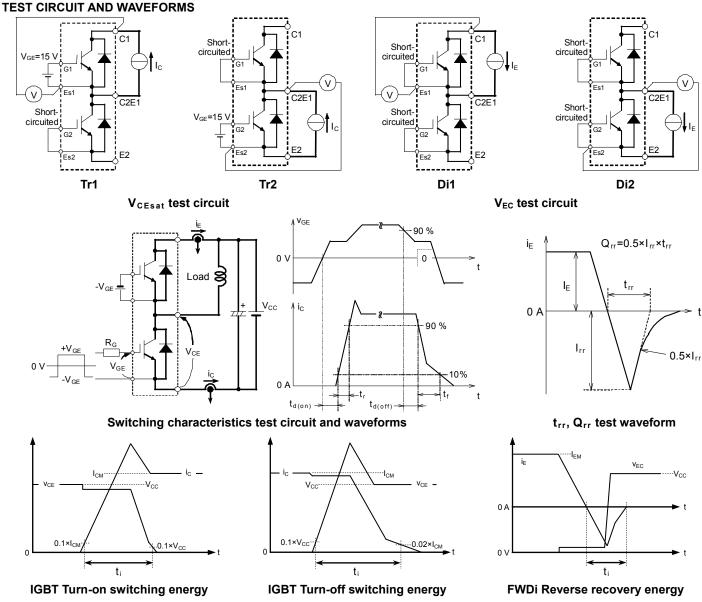
Symbol Item	Itom	Conditions	Limits			Unit
	Conditions	Min.	Тур.	Max.	Offic	
V _{CC}	(DC) Supply voltage	Applied across C1-E2	-	1000	1100	V
V_{GEon}	Gate (-emitter drive) voltage	Applied across G1-Es1/G2-Es2	13.5	15.0	16.5	V
R _G	External gate resistance	Per switch	4.8	1	48	Ω

CHIP LOCATION (Top view)

Dimension in mm, tolerance: ±1 mm

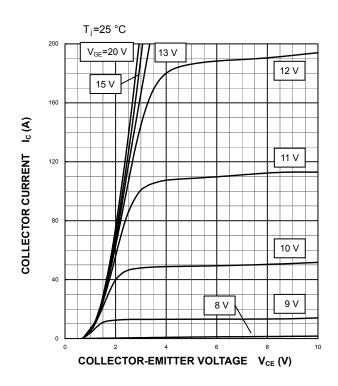


Tr1/Tr2: IGBT, Di1/Di2: FWDi

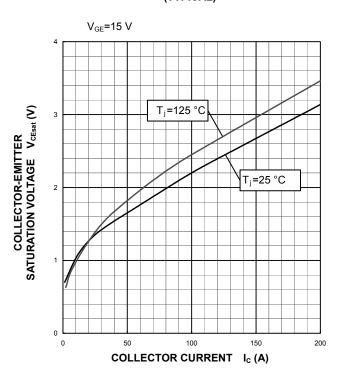


PERFORMANCE CURVES

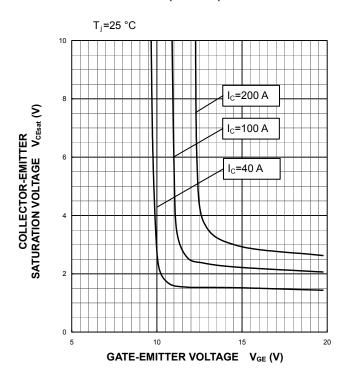
OUTPUT CHARACTERISTICS (TYPICAL)



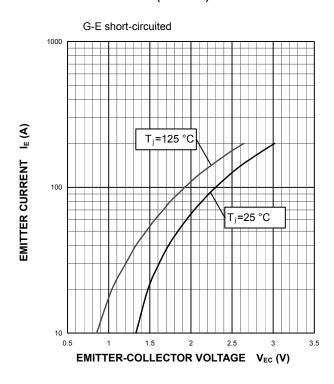
COLLECTOR-EMITTER SATURATION VOLTAGE CHARACTERISTICS (TYPICAL)



COLLECTOR-EMITTER SATURATION VOLTAGE CHARACTERISTICS (TYPICAL)



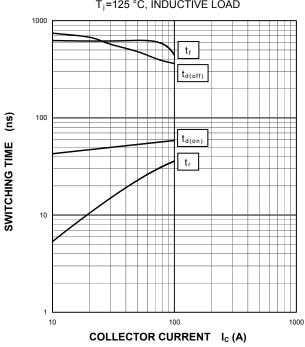
FREE WHEELING DIODE FORWARD CHARACTERISTICS (TYPICAL)



PERFORMANCE CURVES

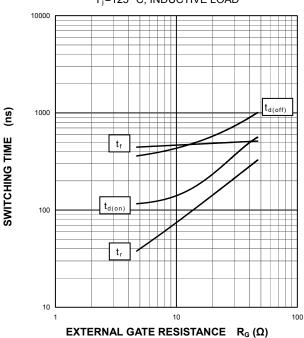
HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)

 V_{CC} =1000 V, V_{GE} =±15 V, R_{G} =4.8 Ω , T_j=125 °C, INDUCTIVE LOAD



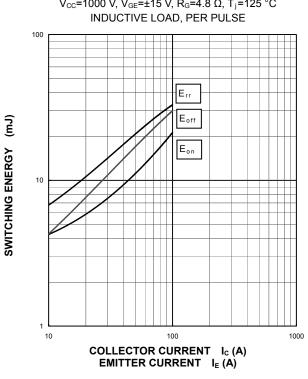
HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)

 V_{CC} =1000 V, I_{C} =100 A, V_{GE} =±15 V, T_j=125 °C, INDUCTIVE LOAD



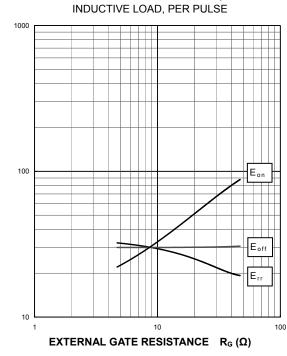
HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)

 V_{CC} =1000 V, V_{GE} =±15 V, R_{G} =4.8 Ω , T_{j} =125 °C



HALF-BRIDGE **SWITCHING CHARACTERISTICS** (TYPICAL)

 V_{CC} =1000 V, I_C/I_E =100 A, V_{GE} =±15 V, T_j =125 °C



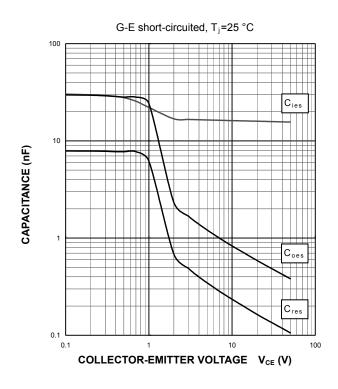
E)

SWITCHING ENERGY (mJ) REVERSE RECOVERY ENERGY

REVERSE RECOVERY ENERGY

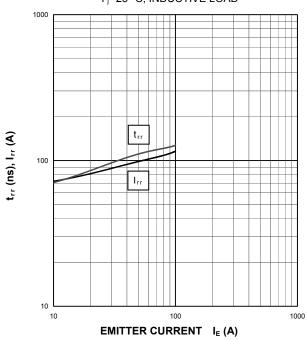
PERFORMANCE CURVES

CAPACITANCE CHARACTERISTICS (TYPICAL)

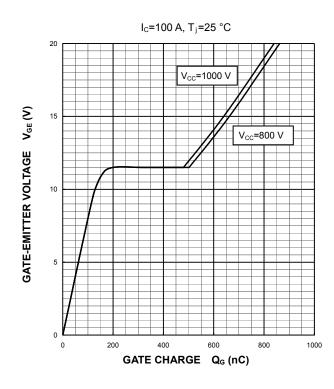


FREE WHEELING DIODE REVERSE RECOVERY CHARACTERISTICS (TYPICAL)

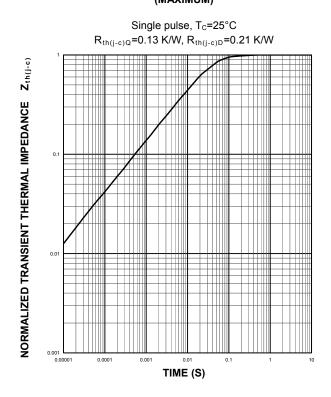
 V_{CC} =1000 V, V_{GE} =±15 V, R_{G} =4.8 Ω , T_{j} =25 °C, INDUCTIVE LOAD



GATE CHARGE CHARACTERISTICS (TYPICAL)



TRANSIENT THERMAL IMPEDANCE CHARACTERISTICS (MAXIMUM)



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