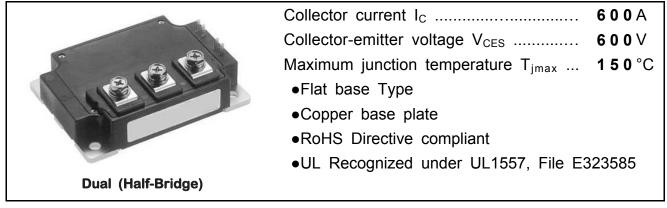


< IGBT MODULES >

# CM600DU-12NFH

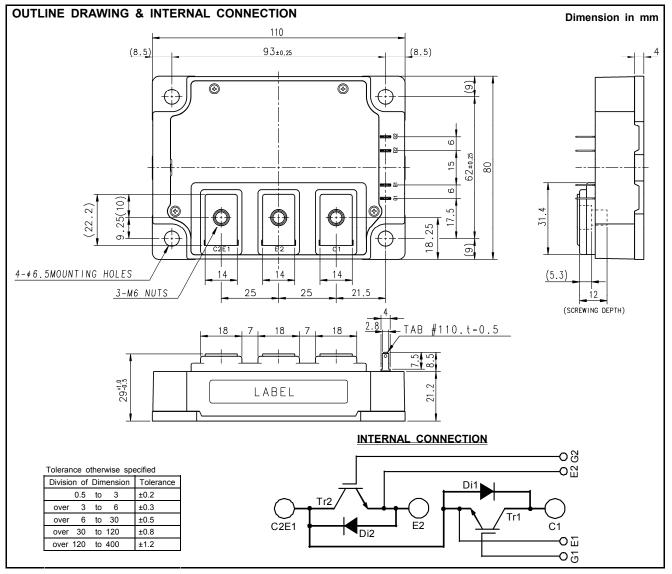
HIGH POWER HIGH FREQUENTLY SWITCHING USE

**INSULATED TYPE** 



#### APPLICATION

High freqency (30 kHz  $\sim$  60 kHz) switching use: Gradient anplifier, Induction heating, Power supply, etc.



ABSOLUTE	MAXIMUM RATINGS $(I_j=25 \text{ °C})$	uniess otherwise specified)				
Symbol Item		Conditions	Rating	Unit		
V <sub>CES</sub>	Collector-emitter voltage	G-E short-circuited	600	V		
V <sub>GES</sub>	Gate-emitter voltage	C-E short-circuited	±20	V		
		Operation (Note.5)	600			
I <sub>C</sub>	Collector current	Operation, RMS (Note.5)	400 A			
I <sub>CRM</sub>		Pulse, Repetitive (Note.4)	1200			
P <sub>tot</sub>	Total newer dissinction	T <sub>C</sub> =25 °C <sup>(Note.2, 5)</sup>	1130			
P <sub>tot</sub> '	Total power dissipation	T <sub>C</sub> '=25 °C, RMS <sup>(Note.3, 5)</sup>	2350			
(Note.1)	Emitter current	Operation (Note.5)	600			
I <sub>E</sub> (Note:1)		Operation, RMS (Note.5)	400	Α		
(Note.1)		Pulse, Repetitive (Note.4)	1200			
Tj	Junction temperature	-	-40 ~ +150	°C		
T <sub>stg</sub>	Storage temperature	-40 ~ +125				
Visol	Isolation voltage	Terminals to base plate, RMS, f=60 Hz, AC 1 min	2500	V		

#### ABSOLUTE MAXIMUM RATINGS (T<sub>i</sub>=25 °C, unless otherwise specified)

#### ELECTRICAL CHARACTERISTICS (T<sub>j</sub>=25 °C, unless otherwise specified)

Symbol	Item	Conditions		Limits			Unit
Symbol	liem			Min.	Тур.	Max.	Unit
ICES	Collector-emitter cut-off current	V <sub>CE</sub> =V <sub>CES</sub> , G-E short-circu	ited	-	-	1	mA
I <sub>GES</sub>	Gate-emitter leakage current	V <sub>GE</sub> =V <sub>GES</sub> , C-E short-circu	iited	-	-	0.5	μA
$V_{\text{GE(th)}}$	Gate-emitter threshold voltage	I <sub>C</sub> =60 mA, V <sub>CE</sub> =10 V		5	6	7	V
V <sub>CEsat</sub>	Collector-emitter saturation voltage	I <sub>C</sub> =600 A <sup>(Note.6)</sup> ,	T <sub>j</sub> =25 °C	-	2.0	2.7	V
- 01381	g-	V <sub>GE</sub> =15 V	T <sub>j</sub> =125 °C	-	1.95	-	
Cies	Input capacitance			-	-	166	nF
Coes	Output capacitance	V <sub>CE</sub> =10 V, G-E short-circu	uited	-	-	11	
Cres	Reverse transfer capacitance			-	-	6.0	
$Q_{G}$	Gate charge	V <sub>CC</sub> =300 V, I <sub>C</sub> =600 A, V <sub>GE</sub> =15 V		-	3720	-	nC
t <sub>d(on)</sub>	Turn-on delay time	V <sub>CC</sub> =300 V, I <sub>C</sub> =600 A, V <sub>GE</sub> =±15 V,		-	-	650	- ns
tr	Rise time	$\nabla_{CC} = 300 \ \text{v}, \ \text{I}_{C} = 000 \ \text{A}, \ \text{v}$	V() 000 V, I( 000 X, V() 10 V,		-	250	
$t_{\text{d(off)}}$	Turn-off delay time	$R_{G}$ =2.0 $\Omega$ , Inductive load		-	-	800	115
t <sub>f</sub>	Fall time	$R_{G}$ =2.0 $\Omega$ , inductive load		-	-	150	
V <sub>EC</sub> (Note.1)	Emitter-collector voltage	I <sub>E</sub> =600 A <sup>(Note.6)</sup> , G-E short-circuited		-	2.0	2.6	V
t <sub>rr</sub> (Note.1)	Reverse recovery time	$V_{CC}$ =300 V, I <sub>E</sub> =600 A, V <sub>GE</sub> =±15 V,		-	-	200	ns
Q <sub>rr</sub> (Note.1)	Reverse recovery charge	$R_G=2.0 \Omega$ , Inductive load		-	11	-	μC
Eon	Turn-on switching energy per pulse	V <sub>CC</sub> =300 V, I <sub>C</sub> =I <sub>E</sub> =600 A	,	-	11	-	mJ
E <sub>off</sub>	Turn-off switching energy per pulse	V <sub>GE</sub> =±15 V, R <sub>G</sub> =2.0 Ω, T <sub>j</sub> =125 °C,		-	27	-	IIIJ
Err (Note.1)	Reverse recovery energy per pulse	Inductive load		-	6.3	-	mJ
r <sub>g</sub>	Internal gate resistance	Per switch, T <sub>C</sub> =25 °C		-	0.8	-	Ω

#### THERMAL RESISTANCE CHARACTERISTICS

Symbol	Item	Conditions	Limits			Unit
		Conditions	Min.	Тур.	Max.	Unit
$R_{th(j-c)Q}$	Thermal resistance (Note.2)	Junction to case, per IGBT	-	-	0.11	K/W
R <sub>th(j-c)D</sub>		Junction to case, per FWDi	-	-	0.12	K/W
R <sub>th(c-s)</sub>	Contact thermal resistance (Note.2)	Case to heat sink, per 1/2 module, Thermal grease applied <sup>(Note.7)</sup>	-	20	-	K/kW
R <sub>th(j-c')Q</sub>	Thermal resistance (Note.3)	Junction to case, per IGBT	-	-	53	K/kW
R <sub>th(j-c')D</sub>		Junction to case, per FWDi	-	-	78	K/kW

#### **MECHANICAL CHARACTERISTICS**

Symbol Item	Itom	Conditions		Limits			Unit
	Conditions		Min.	Тур.	Max.	Unit	
Mt	Mounting torque	Main terminals	M 6 screw	3.5	4.0	4.5	N∙m
Ms		Mounting to heat sink	M 6 screw	3.5	4.0	4.5	11.111
m	Weight	-		-	580	-	g
e <sub>c</sub>	Flatness of base plate	On the centerline X, Y	(Note.8)	-100	-	+100	μm

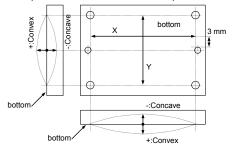
#### RECOMMENDED OPERATING CONDITIONS (T<sub>a</sub>=25 °C)

Symbol Item	Itom	Conditions		Limits	Unit	
	item	Conditions	Min.	Тур.	Max.	Unit
Vcc	(DC) Supply voltage	Applied across C1-E2	-	300	400	V
V <sub>GEon</sub>	Gate (-emitter drive) voltage	Applied across G1-Es1/G2-Es2	13.5	15.0	16.5	v
R <sub>G</sub>	External gate resistance	Per switch	1.0	-	10	Ω

Note.1: Represent ratings and characteristics of the anti-parallel, emitter-collector free wheeling diode (FWDi).

2: Case temperature  $(T_c)$  measured point is base plate side. (Refer to the figure of chip location)

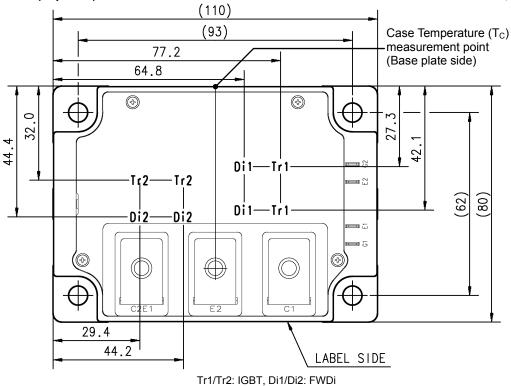
- 3: Case temperature (T<sub>c</sub>') and heat sink temperature (T<sub>s</sub>') are defined on the each surface of base plate and heat sink just under the chips. (Refer to the figure of chip location)
- 4: Pulse width and repetition rate should be such that the device junction temperature (T<sub>j</sub>) dose not exceed T<sub>jmax</sub> rating.
- 5: Junction temperature  $(T_j)$  should not increase beyond  $T_{jmax}$  rating.
- 6: Pulse width and repetition rate should be such as to cause negligible temperature rise.
- (Refer to the figure of test circuit) 7: Typical value is measured by using thermally conductive grease of  $\lambda$ =0.9 W/(m K).
- 8: Base plate flatness measurement points are as in the following figure.



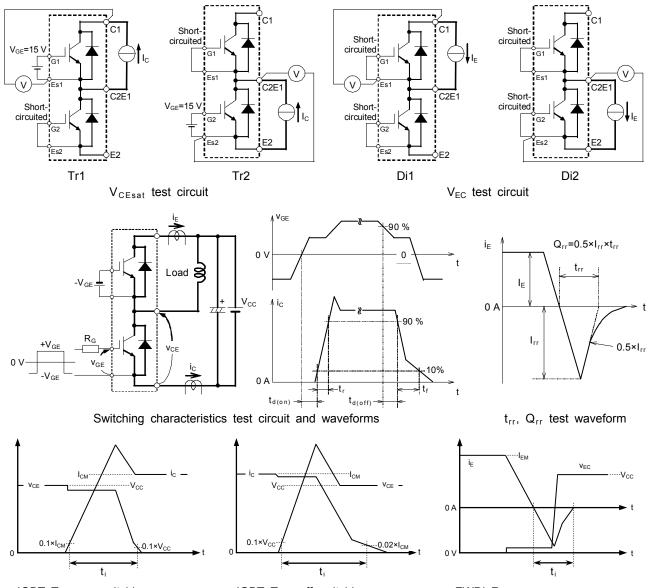
9: No short circuit capability is designed.

#### CHIP LOCATION (Top view)

Dimension in mm, tolerance: ±1 mm

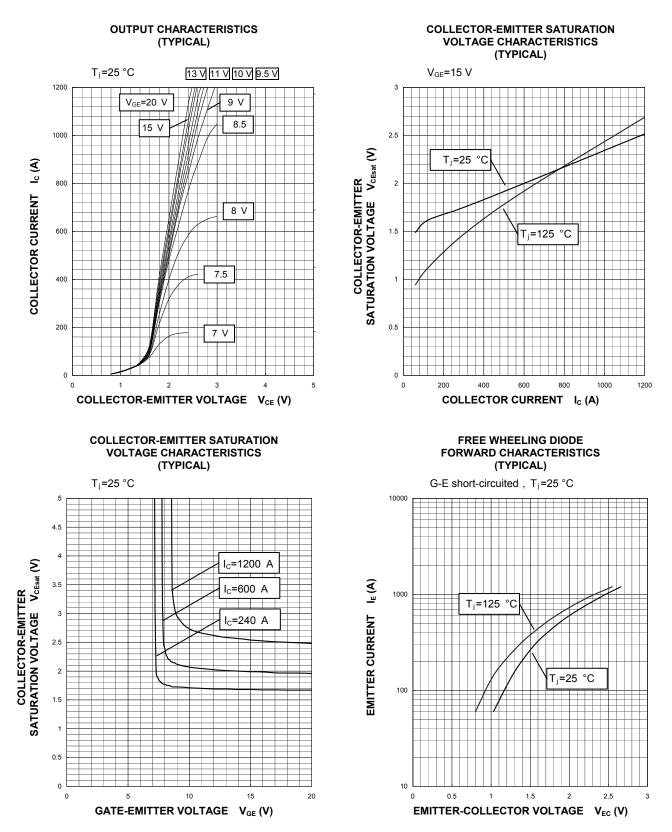


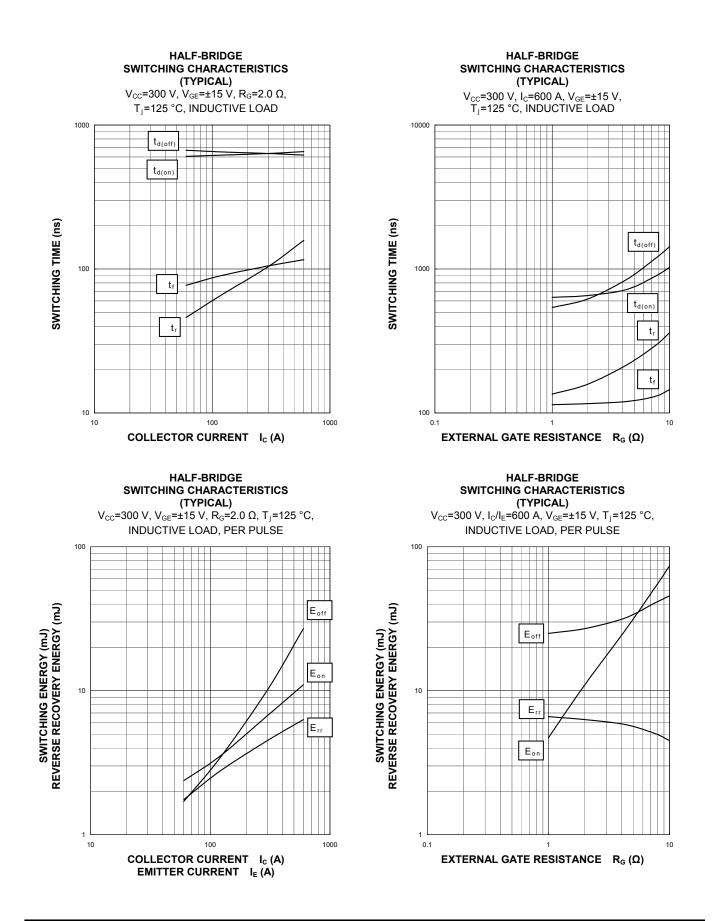
#### TEST CIRCUIT AND WAVEFORMS

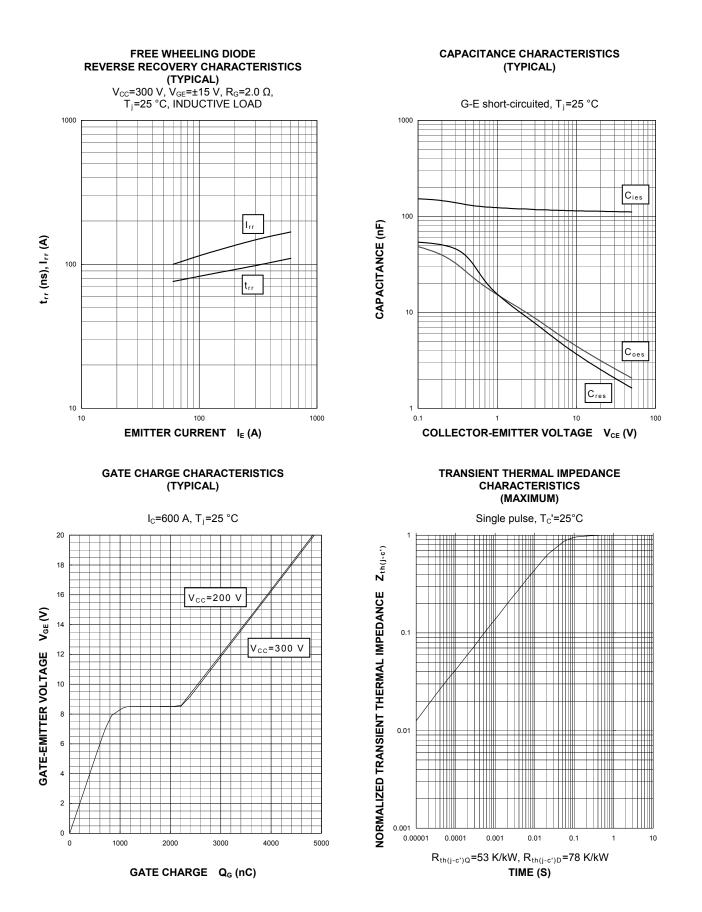


IGBT Turn-on switching energyIGBT Turn-off switching energyFWDi Reverse recovery energyTurn-on / Turn-off switching energy and Reverse recovery energy test waveforms (Integral time instruction drawing)

#### PERFORMANCE CURVES







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