# **NPN High Power Silicon Transistors**

### 2N3902 & 2N5157

#### **Features**

- · Available in JAN, JANTX, and JANTXV per MIL-PRF-19500/371
- TO-3 (TO-204AA) Package





### **Maximum Ratings**

Ratings	Symbol	2N3902 2N5157		Units
Collector - Emitter Voltage	V <sub>CEO</sub>	400 500		Vdc
Emitter - Base Voltage	V <sub>EBO</sub>	5.0	6.0	Vdc
Collector - Base Voltage	V <sub>CBO</sub>	7.0		Vdc
Base Current	Ι <sub>Β</sub>	2.0		Adc
Collector Current	IC	3.5		Adc
Total Power Dissipation @ $T_A = +25 ^{\circ}C$ (1) @ $T_A = +25 ^{\circ}C$ (2)	P <sub>T</sub>	5.0 100		W
Operating & Storage Temperature Range	T <sub>j</sub> , T <sub>stg</sub>	-65 to +200		°C

### **Thermal Characteristics**

Characteristics	Symbol	Maximum	Units
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	1.25	°C/W

<sup>1)</sup> Derate linearly @ 28.57 mW/°C for  $T_A > +25$ °C 2) Derate linearly @ 0.8 mW/°C for  $T_C > +75$ °C

### **Electrical Characteristics**

OFF Characteristics	Symbol	Mimimum	Maximum	Units
	ICEO		250 250	μAdc
Collector - Emitter Cutoff Current $V_{BE} = 1.5 \text{ Vdc}, V_{CE} = 700 \text{ Vdc}$	ICEX		500	μAdc
	I <sub>EBO</sub>		200 200	μAdc
OFF Characteristics				
Base - Emitter Saturation Voltage $I_C = 1.0$ Adc, $I_B = 0.1$ Vdc $I_C = 3.5$ Adc, $I_B = 0.7$ Vdc	V <sub>BE(sat)</sub>		1.5 2.0	Vdc
Collector - Emitter Saturation Voltage $I_C = 1.0$ Adc, $I_B = 0.1$ Adc $I_C = 3.5$ Adc, $I_B = 0.7$ Adc	V <sub>CE(sat)</sub>		0.8 2.5	Vdc





### **Electrical Characteristics -con't**

ON Characteristics (2) (con't)	Symbol	Minimum	Maximum	Unit
Forward Current Transfer Ratio $I_{C} = 0.5 \text{ Adc, V}_{CE} = 5.0 \text{ Vdc}$		25 30	90	
$I_C = 1.0 \text{ Adc}, V_{CE} = 5.0 \text{ Vdc}$ $I_C = 2.5 \text{ Adc}, V_{CE} = 5.0 \text{ Vdc}$ $I_C = 3.5 \text{ Adc}, V_{CF} = 5.0 \text{ Vdc}$	H <sub>FE</sub>	10 5	90	
Collector - Emitter Sustaining Voltage  I <sub>C</sub> = 100 mAdc 2N3902 2N5157	V <sub>CE(sat)</sub>		1.0 2.5	Vdc
DYNAMIC Characteristic		_		
Small-Signal Short-Circuit Forward Current Transfer Ratio $I_C = 0.2$ Adc, $V_{CE} = 10$ Vdc, $f = 1$ MHz	h <sub>fe</sub>	2.5	25	
Output Capacitance $V_{CB} = 10 \text{ Vdc}, \ I_E = 0, \ 100 \ \text{kHz} \ \leq f \leq \ 1.0 \ \text{MHz}$	C <sub>obo</sub>		500	pF
Switching Characteristic				
Turn-On Time $V_{CC} = 125 \text{ Vdc}, I_C = 1.0 \text{ Adc}, I_{B1} = 0.1 \text{ Adc}$	t <sub>on</sub>		0.8	μs
Tum-Off Time $V_{CC}$ =125 Vdc, $I_{C}$ = 1.0 Adc, $I_{B1}$ = 0.1 Adc, - $I_{B2}$ = 0.50 Adc	t <sub>off</sub>		1.7	μs

#### **SAFE OPERATING AREA**

**DC Tests:**  $T_C = +25$  °C, 1 Cycle, t = 1.0 s (See Figure 3 of MIL-PRF-19500/371)

Test 1:  $V_{CE} = 28.6 \text{ Vdc}, I_{C} = 3.5 \text{ Adc}$ Test 2:  $V_{CE} = 70 \text{ Vdc}, I_{C} = 1.43 \text{ Adc}$ 

**TEST 3:**  $V_{CE} = 325 \text{ Vdc}, I_{C} = 55 \text{ mAdc}$  2N3902

 $V_{CF} = 400 \text{ Vdc}, I_{C} = 35 \text{ mAdc}$  2N5157

#### Switching Test:

#### Load condition C (unclamped inductive load)

 $T_C = 25$ °C, duty cycle  $\leq$  10%;  $R_S = 0.1 \Omega$  (See Figure 4 of MIL-PRF-19500/371)

Test 1:  $t_P = \text{approximately 3 ms (vary to obtain I}_C$ ),  $R_{BB1} = 20 \Omega$ ,  $V_{BB1} = 10 \text{ Vdc}$ ;  $R_{BB2} = 3 \text{ k}\Omega$ ,

 $V_{BB2} = 1.5$  Vdc,  $V_{CC} = 50$  Vdc,  $I_{C} = 3.5$  Adc, L = 60 mH, R = 3  $\Omega$ ;  $R_{L} \le 14$   $\Omega$ 

**Test 2:**  $t_P = \text{approximately 3 ms (vary to obtain I<sub>C</sub>)}, R_{BB1} = 100 \Omega, V_{BB1} = 10 \text{ Vdc}; R_{BB2} = 3 \text{ k}\Omega,$ 

 $V_{BB2} = 1.5 \text{ Vdc}$ ,  $I_C = 0.6 \text{ Adc}$ ,  $V_{CC} = 50 \text{ Vdc}$ , L = 200 mH,  $R = 8 \Omega$ ;  $R_L \le 83 \Omega$ 

#### Switching Tests:

#### Load condition (clamped inductive load)

 $T_C = 25$ °C, duty cycle  $\leq 10\%$  (See Figure 5 of MIL-PRF-19500/371)

Test 1:  $t_P = \text{approximately 30 ms (vary to obtain I}_C), R_S = 0.1 \Omega, R_{BB1} = 20 \Omega, V_{BB1} = 10 \text{ Vdc};$ 

 $R_{BB2} = 100 \Omega$ ,  $V_{BB2} = 1.5 \text{ Vdc}$ ,  $V_{CC} = 50 \text{ Vdc}$ ,  $I_C = 3.5 \text{ Adc}$ , L = 60 mH,  $R = 3 \Omega$ ;  $R_L \le 0 \Omega$ 

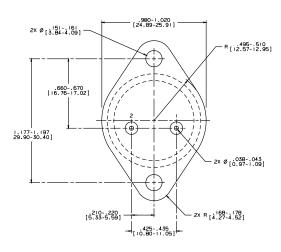
(A suitable clamping circuit or diode can be used.) Clamp Voltage = 400 +0, -5 Vdc 2N3902 Clamp Voltage = 500 +0, -5 Vdc 2N5157

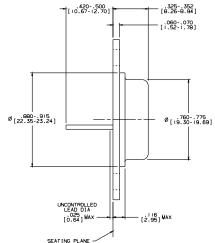
(Clamped voltage must be reached)

(2) Pulse Test: Pulse Width = 300  $\mu$ s, Duty Cycle  $\leq$ 2.0%.



### **Outline Drawing**





- VOTES:

  1. STANDARD HEADER TYPE SOLID BASE.

  2. STANDARD LEAD FINISH-PER MIL-M-39510 TYPE X OR EQUIVALENT.

  3. LEAD NOT BERNT GREATER THAN 15'.

  4. DIMENSIONS BASED ON JEDEC STANDARD TO-3 PUBLICATION 95, PA

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