Standard Products

Preliminary

PCS5038
Octal Precision Current Source
Radiation Tolerant

www.aeroflex.com/pcs

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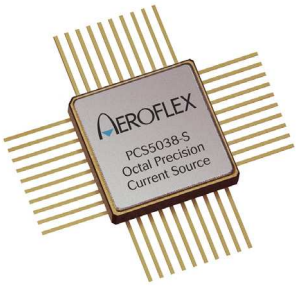

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A passion for performance.

FEATURES

- **Radiation Performance**
  - Total dose ≥ 100 krad (Si), Dose rate = 50 - 300 rads(Si)/s
- Designed for temperature monitoring with Thermistors and RTDs (Resistance Temperature Detectors)
- **Eight high precision current sources**
  - One external resistor to set source current
  - Source current range from 100uA to 2mA
  - Accuracy to less than ± 1% over full operating range
  - Sensor voltage monitoring comparator on each precision current source output
  - Turn all eight sources ON/OFF with ENA_PCS_H input pin
  - Long term stability
  - Low Drift
- **Internal Band Gap regulator**
  - High Precision 2.0V output
  - Provided for use to generate VREF comparator inputs
- **Packaging** - Hermetic ceramic
  - 40 leads, 0.600"Sq x 0.120"Ht quad flat pack
  - Weight: 4.5 gm max
- **Eight voltage monitoring comparators**
  - High impedance sensor interface
  - Two groups of 4 comparators with separate VREF and Enable input pins
  - Provided for built-in monitoring of the voltage generated by the current sources
  - Can be used as an octal comparator by turning off all current sources with ENA_PCS_H set to logic 0
- Zener protected inputs
- Single +5V supply voltage
- Low supply current
- Designed for aerospace and high reliability space applications
- **Aeroflex Plainview's Radiation Hardness Assurance Plan is DLA Certified to MIL-PRF-38534, Appendix G.**

GENERAL DESCRIPTION

The Aeroflex Plainview PCS5038 contains eight precision current sources that can be set with a single external resistor connected to the R_Iset pin. The current source was designed for thermistor current monitor and resistive sensor applications.

The precision current source outputs can be set to source from 100uA to 2mA each. Two-to-eight of the current source outputs can be paralleled to produce multiple current source outputs levels. The output current of each current source will follow the current set by the external resistor and the precision voltage at the R_Iset pin. The output current precision is a function of the voltage at the R_Iset pin and the chosen resistor's characteristics. The comparator inputs will monitor the current source output voltage, with a trip point set by an external reference voltage or the internal reference voltage.

A precision internal 2.0V reference is provided. The comparators are arranged as two banks of four with separate Enable and voltage reference inputs. A Current Source Enable is also provided. See Figure 1. All Enable inputs are active high. All comparators are inverting with their outputs pulled-up internally by a nominal 10k ohm resistor. Any unused current source outputs can be left open circuit without affecting other I/O's. Each current source output will track the other seven current source outputs as long as the voltage generated by the resistor sensor does not exceed 2.0V below the supply voltage (VCC-2.0V). Higher sensor voltages can be accomodated by increasing the supply voltage.

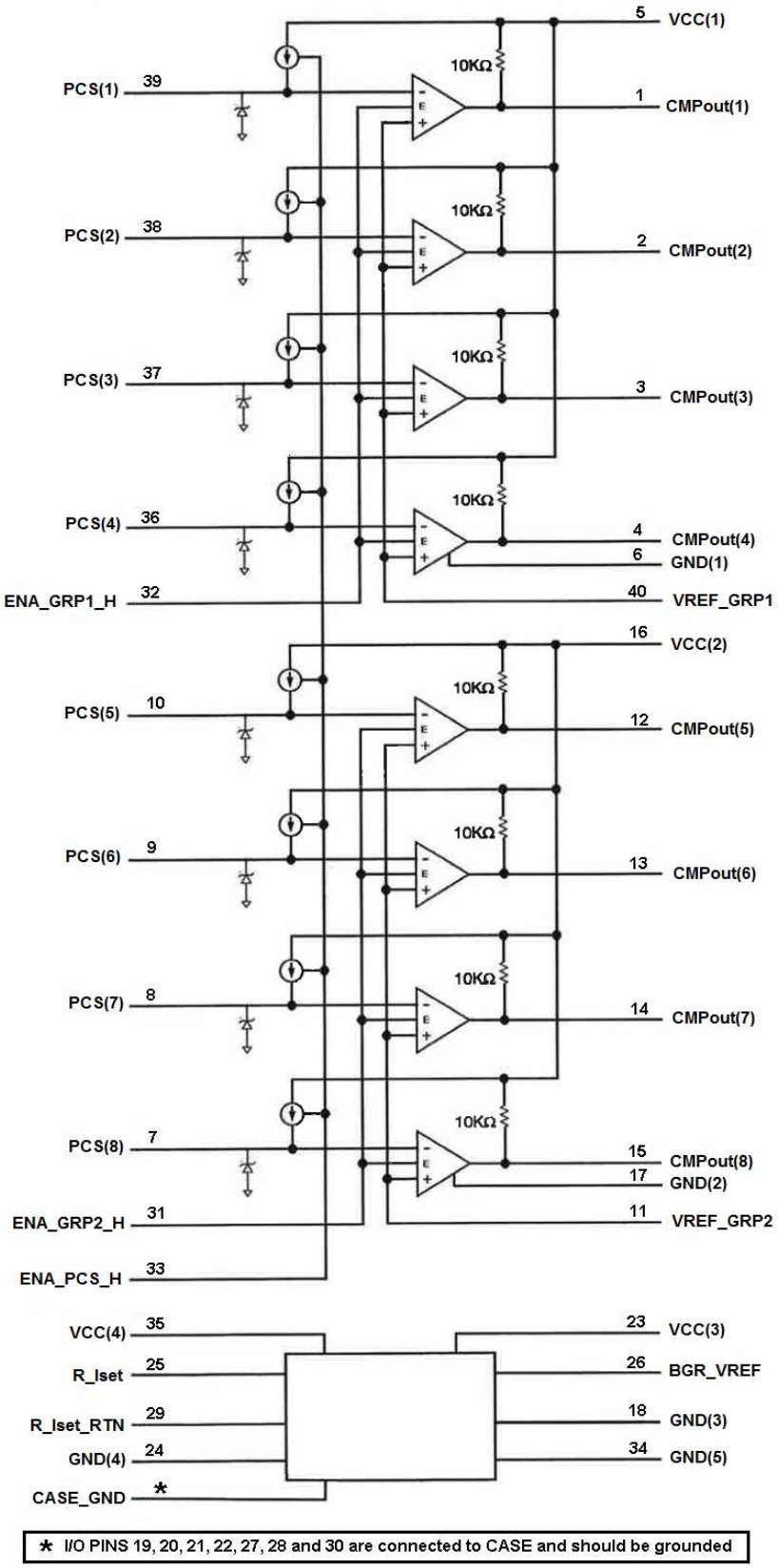


FIGURE 1 – Block Diagram

ABSOLUTE MAXIMUM RATINGS

Parameter	Rating
Operating Case Temperature	-55°C to +125°C
Storage Case Temperature	-65°C to +150°C
Power Supply Voltages (V_{CC})	+7.0 V_{DC}

NOTICE: Stresses above those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress rating only; functional operation beyond the "Operation Conditions" is not recommended and extended exposure beyond the "Operation Conditions" may affect device reliability.

ELECTRICAL CHARACTERISTICS

(V_{CC} = +5.0V_{DC} ±5%, T_C = -55°C TO +125°C, Unless otherwise specified)

Parameter	Symbol	Condition	Min	Max	Unit
Precision Current Source Out 1/ 100ua ≤ I_{PCS} ≤ 2ma External Set Resistor Tolerance = +/- 0.1% 1/ TC ≤ +/- 10ppm	$I_{PCS_{ON}}$	ENA_PCS_H = Hi ENA_GRP1_H = Hi ENA_GRP2_H = Hi	I_{PCS} -0.75%	I_{PCS} +0.75%	mA
	$I_{PCS_{OFF}}$	ENA_PCS_H = Lo ENA_GRP1_H = Hi ENA_GRP2_H = Hi	-	1000	nA
Current Source Matching Accuracy	I_{MATCH}	Relative to Mean of $I_{PCS_{ON}}$(1:8) @ 25°C	-0.75	0.75	%
Comparator In Voltage 2/	V_{IN}	Input or Reference	0	3.0	V
Comparator Ref In Current	I_{REF}	VREF_GRP1 = 0V VREF_GRP2 = 0V	-	2.0	uA
Enable Input Voltage Low ENA_PCS_H (pin 33) ENA_GRP1_H (pin 32) ENA_GRP2_H (pin 31)	V_{IL}		-	0.8	V
Enable Input Voltage High ENA_PCS_H (pin 33) ENA_GRP1_H (pin 32) ENA_GRP2_H (pin 31)	V_{IH}		2.0	-	V
Output Voltage Low CMPout (1:8) (pins 1-4 and 12-15)	V_{OL}	Isink ≤ 2.0mA	-	0.4	V
Output Voltage High CMPout (1:8) (pins 1-4 and 12-15)	V_{OH}		4.4		V
Internal Output Pull-Up Resistor 2/	R_{INT}		8	12	KΩ
Input Open Circuit Voltage	V_{INOC}			V_{CC}	V
R_Iset pin Voltage	V_{SET}	@ 25°C	1.998	2.002	V
R_Iset Temp Coefficient	V_{SET-TC}		-0.75	0.75	%
Band Gap Output Voltage BGR_VREF (pin 26)	V_{BG}		1.980	2.020	V
Band Gap Load Regulation 2/ BGR_VREF (pin 26)	$\frac{\Delta V_{BG}}{\Delta I_{BG}}$	BG Iout ≤ 2mA	-	5	mV
Comparator Pulse Delay 3/	t_{DLH1}	CL = 30pF, Vref = 1.0Vdc Vin overdrive ≥ 50mV See Figure 2	-	300	nS
	t_{DHL1}		-	200	nS
Comparator Enablet Delay 3/	t_{DLH2}	CL = 30pF, Vref = 1.0Vdc Vin = 1.05vdc See Figure 2	-	1000	nS
	t_{DHL2}		-	750	nS
I_{PCS} Enable Delay 3/	t_{DLH3}	CL = 30pF, Vref = 1.0Vdc Rin = 1KΩ, R_Iset = 1KΩ I_{PCS}= 2mA, See Figure 2	-	3.0	uS
	t_{DHL3}		-	500	nS

ELECTRICAL CHARACTERISTICS

(V_{CC} = +5.0V_{DC} ±5%, T_C = -55°C TO +125°C, Unless otherwise specified)

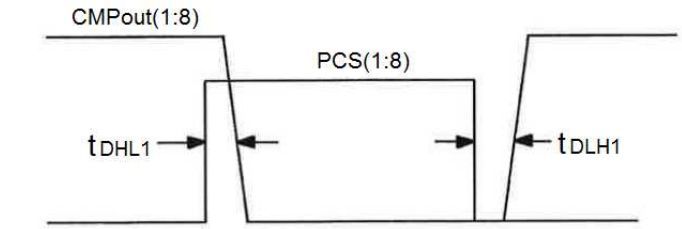
Parameter		Symbol	Condition	Min	Max	Unit
Short Circuit Current		I_{SC}	R_Iset = 0Ω, PCS and Comparator Groups Enabled @ 25°C		60	mA
Output Current Noise @ 1KHz See Curve 2/		Inoise 1	I_{PCS} = 100uA @ 25°C, Typ = 11.5	-	-	pA/√Hz
		Inoise 2	I_{PCS} = 2mA @ 25°C, Typ = 63.0	-	-	
Supply current	PCS and Comparator Groups Disabled 1/	I_{CCq}	ENA_PCS_H = Lo ENA_GRP1_H = Lo ENA_GRP2_H = Lo	-	10	mA
	PCS and Comparator Groups Enabled 1/	I_{CC}	R_Iset = 1KΩ I_{PCS} = 2mA ENA_PCS = Hi ENA_GRP1_H = Hi ENA_GRP2_H = Hi CMPout(1:8) = Hi		30	mA

Notes:

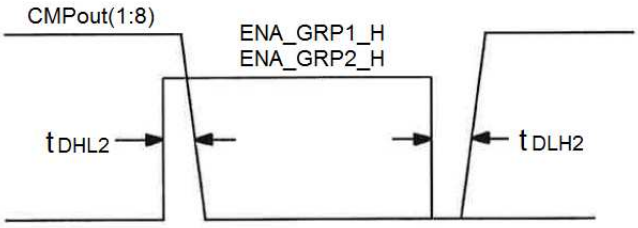
1/ Hi ≥ V_{IH}, Lo ≤ V_{IL}.

2/ Guaranteed by design, but not tested.

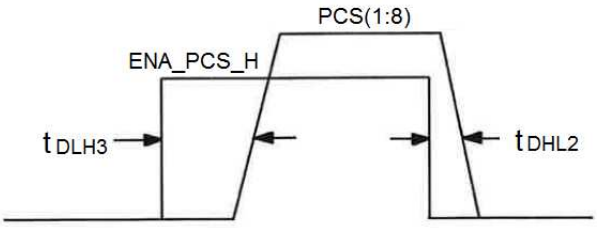
3/ Test fixture node capacitance plus 10pF scope capacitance.



Comparator Pulse Delay



Comparator Enable Delay



I_{PCS} Enable Delay

FIGURE 2 – Timing Diagram

One primary application for the Aeroflex PCS5038, as shown in Figure 3 below, would be to monitor the temperature of different electronic bay areas in a Satellite, and report when any bay area has exceeded a maximum temperature limit.

Thermistors with defined temperature characteristics are used as the sensors, and by providing a constant stable current to the external Thermistor (Temp T1 to Temp T8), an accurate analog voltage of a specific temperature trip point can be established. The comparator voltage reference input can be tailored to set the trip point for the characteristic of the particular thermistor sensor used. The on-board precision 2 Volt reference can be used directly for voltages of 2 Volts or less, or can be scaled with an external operational amplifier to provide a voltage between 2 Volts and the maximum input of 3.0 Volts, for a +5v supply. Various temperature trip points could be accomplished by using thermistors with different temperature characteristics.

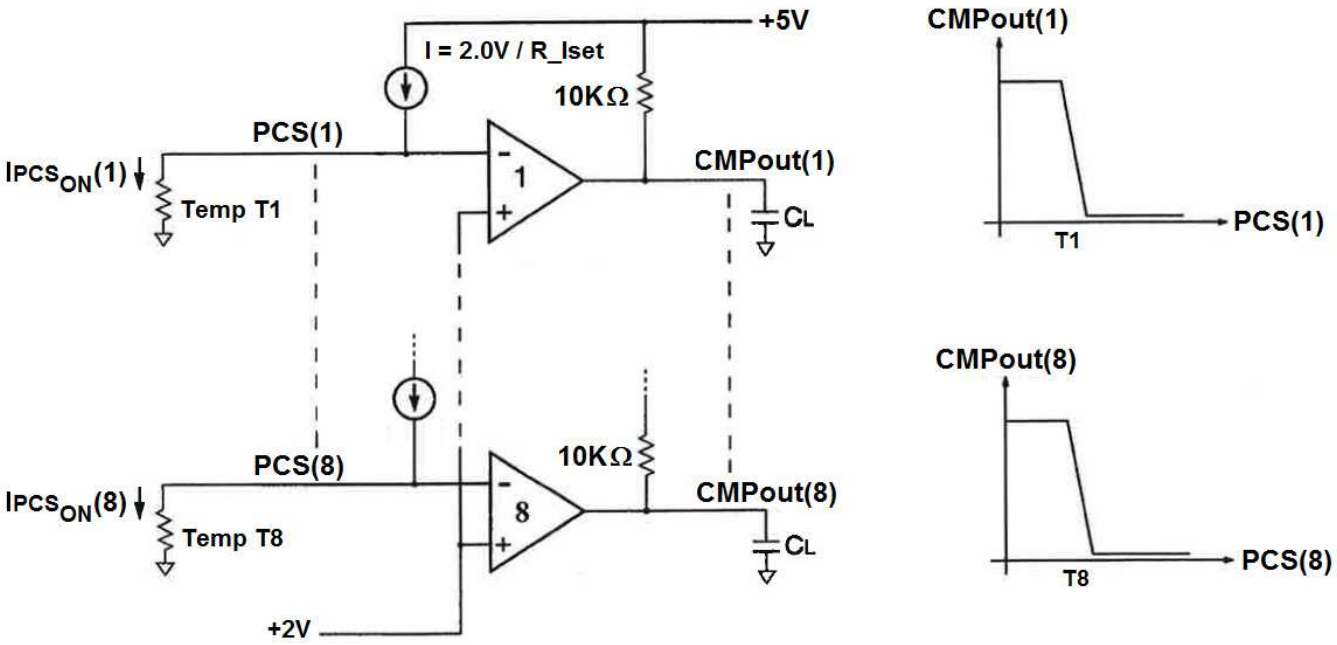


FIGURE 3 – Typical Temperature Sensing Application

When more "Temperature Sensing" inputs are needed, two PCS5038s can be used as shown below in Figure 4.

The output of the two PCS5038s can be wired together to produce a wired "OR" function. Each bank of 8 inputs can then be separately controlled by its associated Enable input.

One application of the multiple input arrangement would be circuit board temperature monitoring using the temperature characteristics of a PN (diode) junction. With a constant current to the diode sensor, the diode voltage can be a calibrated function of temperature. Diodes can be placed on or near vital electronic components to monitor their case temperatures so that they can be shutdown before a catastrophic temperature failure occurs.

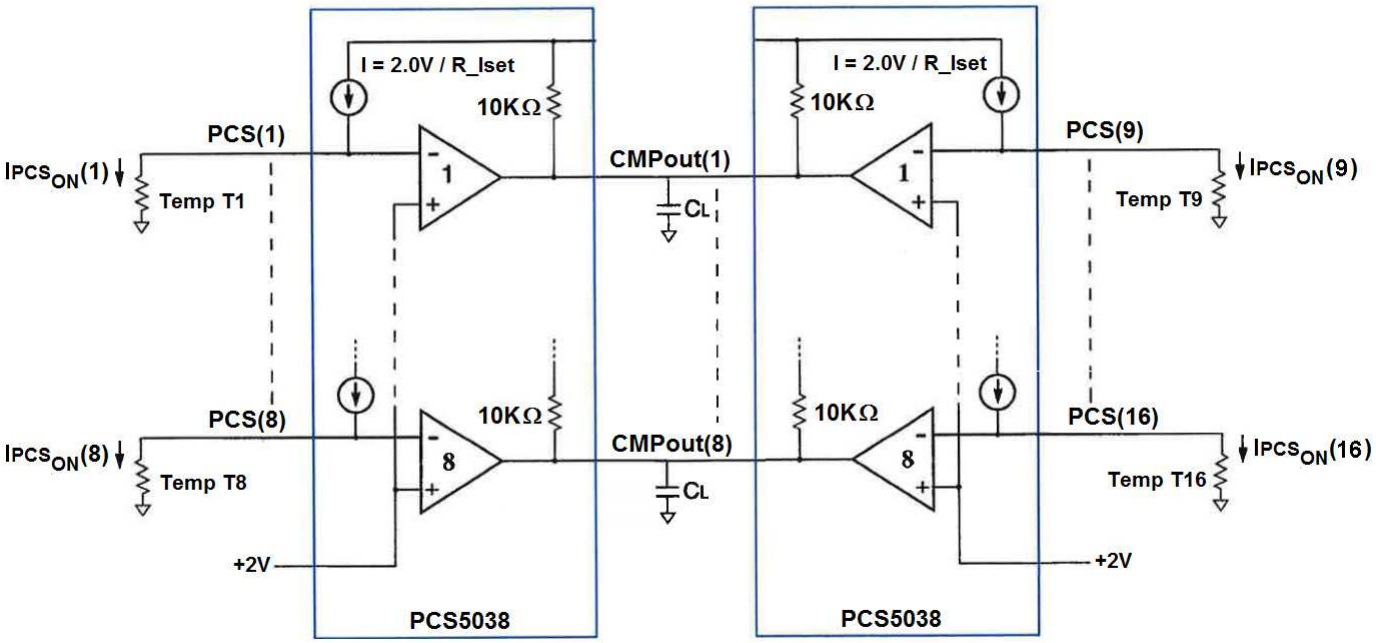


FIGURE 4 – 16 Input, 8 Output Wired "OR" Application

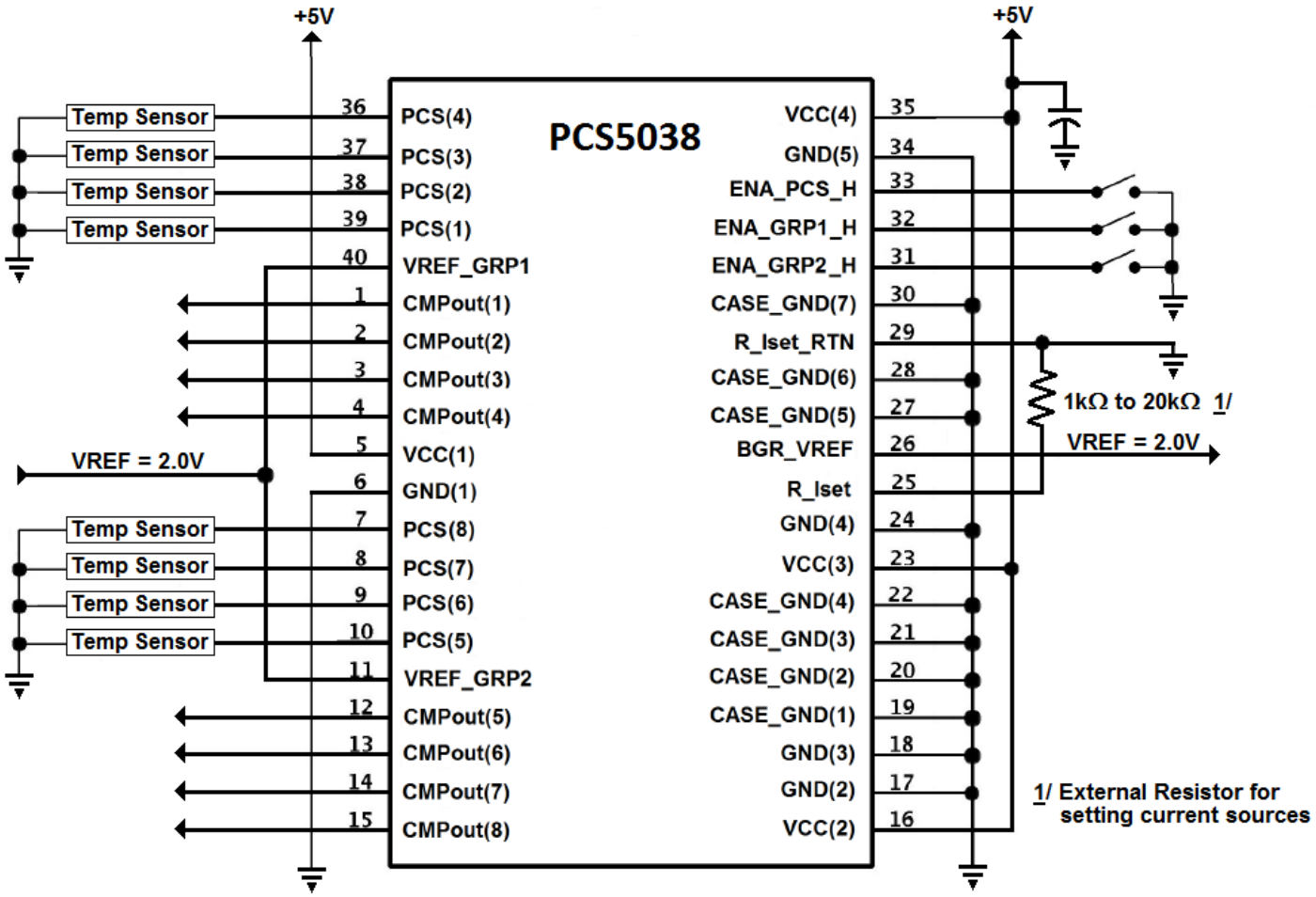


FIGURE 5 – Typical Connection

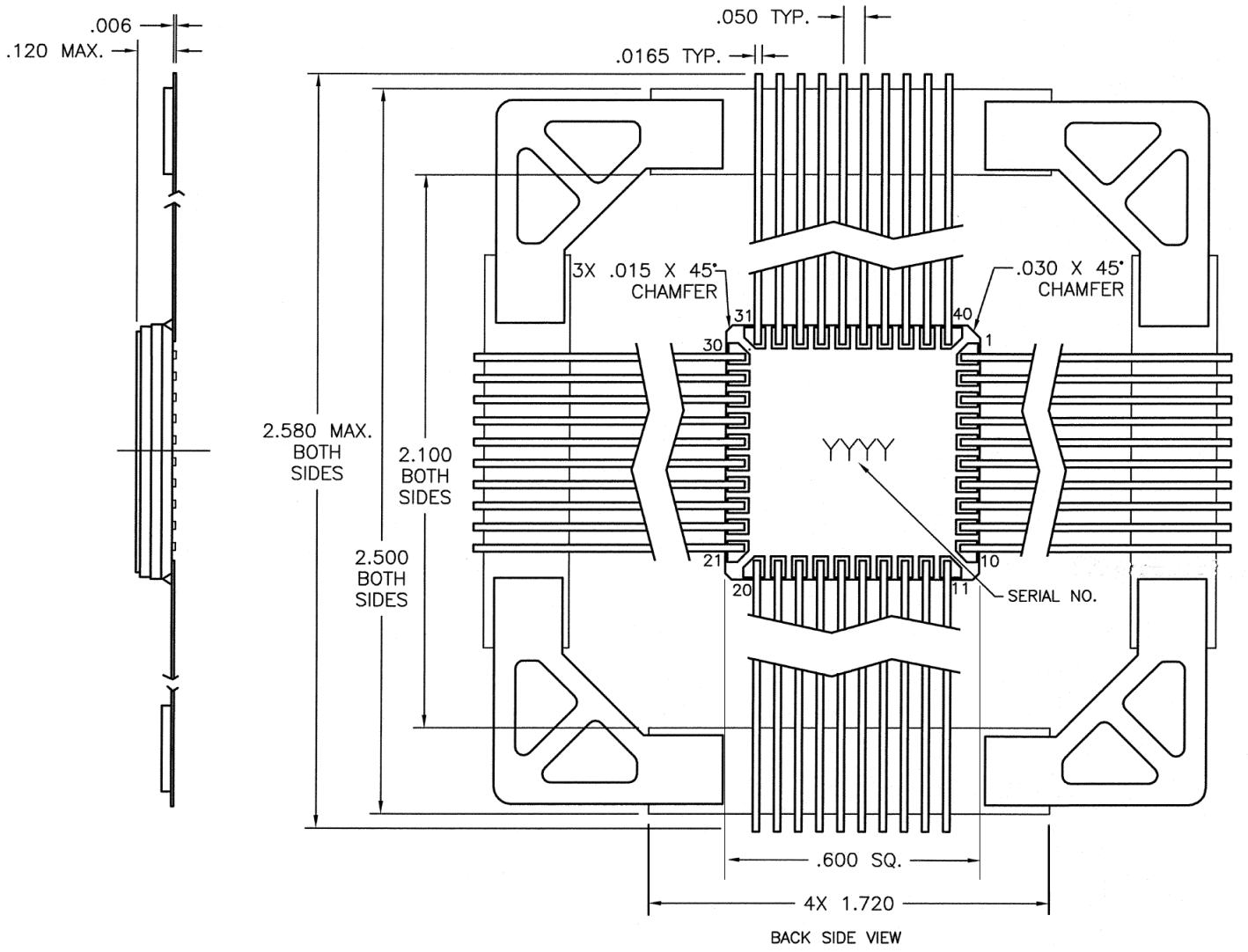


FIGURE 6 – Package Outline

PIN DESCRIPTIONS

Signal Name		Pin#	Type	Description
PCS(1:8)	PCS(1)	39	I/O	Eight Precision Current Sources. Output currents are settable from 100uA to 2.0mA with one external resistor connected between R_Iset pin (25) and R_Iset_RTN pin (29).
	PCS(2)	38	I/O	
	PCS(3)	37	I/O	
	PCS(4)	36	I/O	
	PCS(5)	10	I/O	Each precision current source has a voltage monitoring comparator that will trigger the associated CMPout(1:8) when the sensor voltage increases to greater than VREF_GRP1 for CMPout(1:4) and VREF_GRP2 for CMPout(5:8).
	PCS(6)	9	I/O	
	PCS(7)	8	I/O	
	PCS(8)	7	I/O	
	ENA_GRP1_H	32	Input	Logic 1 state on ENA_GRP1_H enables CMPout(1:4).
	ENA_GRP2_H	31	Input	Logic 1 state on ENA_GRP2_H enables CMPout(5:8).
	ENA_PCS_H	33	Input	Logic 1 state input on the ENA_PCS_H enables all eight precision current sources. Logic 0 state input on the ENA_PCS_H places PCS(1:8) in high impedance state. The sensor voltage monitoring comparators all remain functional.
	VREF_GRP1	40	Input	External Voltage Reference Input to sensor monitoring comparators for PCS(1:4).
	VREF_GRP2	11	Input	External Voltage Reference Input to sensor monitoring comparators for PCS(5:8).
	R_Iset	25	Input	Connect to the External Resistor to set the current level for PCS(1:8).
	R_Iset_RTN	29	Input	Return for the External Resistor. Must be grounded close to pin.
CMPOUT(1:8)	CMPout(1)	1	Output	Sensor voltage monitoring comparator output for each precision current source PCS(1:8).
	CMPout(2)	2	Output	
	CMPout(3)	3	Output	
	CMPout(4)	4	Output	The CMPout(1:8) outputs are active low and trigger to a logic 0 state when sensor voltage on the respective PCS(1:8) pin increases to greater than the associated VREF comparator input voltage.
	CMPout(5)	12	Output	
	CMPout(6)	13	Output	
	CMPout(7)	14	Output	
	CMPout(8)	15	Output	
	BGR_VREF	26	Output	Band Gap Regulator. Precision Output 2.0VDC.
VCC(1:4)	VCC(1)	5	Power	+5 Volt power supply connections.
	VCC(2)	16	Power	
	VCC(3)	23	Power	
	VCC(4)	35	Power	
GND(1:5)	GND(1)	6	Power	Power supply return connections
	GND(2)	17	Power	
	GND(3)	18	Power	
	GND(4)	24	Power	
	GND(5)	34	Power	
CASE_GND(1:7)	CASE_GND(1)	19	Case	All case CASE_GND pins shall be connected together and grounded. The CASE_GND pins are connected to the cover to prevent static charge build-up.
	CASE_GND(2)	20	Case	
	CASE_GND(3)	21	Case	
	CASE_GND(4)	22	Case	
	CASE_GND(5)	27	Case	
	CASE_GND(6)	28	Case	
	CASE_GND(7)	30	Case	

ORDERING INFORMATION

MODEL	DLA SMD #	SCREENING	PACKAGE
PCS5038-7	-	Commercial Flow, +25°C testing only	40-lead Ceramic Quad Flat Pack
PCS5038-S	-	Military Temperature, -55°C to +125°C Screened in accordance with the individual Test Methods of MIL-STD-883 for Space Applications	
PCS5038-201-1S	5962-1223201KXC	DLA SMD Pending	
PCS5038-901-1S	5962R1223201KXC	DLA SMD and Radiation Certification Pending	

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AEROFLEX
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