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ELECTRICAL CHARACTERISTICS @ 25°C

ELECTRICAL SPECIFICATIONS: : 0° C TO +70° C

1.0 TURNS RATIO:(P1-P2-P3): (J1-J2) : 1CT : 1 ± 3% (P4-P5-P6) : (J3-J6): 1CT : 1 ± 3%

2.0 INDUCTANCE: (P6-P4) : 350uH MIN. @ 0.1V,100KHz, 8mA DC Bias (P3-P1) : 350uH MIN. @ 0.1V, 100KHz, 8mA DC Bias RD+ P4

3.0 LEAKAGE INDUCTANCE: P6-P4 (WITH J6 AND J3 SHORT) : 0.3uH MAX. @ 1MHz P3-P1 (WITH J2 AND J1 SHORT) : 0.3uH MAX. @ 1MHz

4.0 INTERWINDING CAPACITANCKP6,P5,P4) TO (J6,J3) : 30pf TYP @ 1MHz (P3,P2,P1) TO (J2,J1) : 30pf TYP @ 1MHZ

5.0 DC RESISTANCE:(J6-J3)=(J1-J2): 1.2 ohms Max. (P6-P4) : .55 ohms Max.

(P3-P1) : .55 ohms Max.

RECEIVE

6.0 RETURN LOSS1MHz TO 30MHz : 18dB MIN. 60MHz TO 80MHz : 12dB MIN.

7.0 DIELECTRIC WITHSTAND(J1, J2) TO (P1, P2) : 1500 VAC VAC : 1500

(J3, J6) TO (P3,P6)

8.0 INSERTION LOSSRS=RL=100 ohms 100KHz TO 100MHz : 1.1 dB TYP

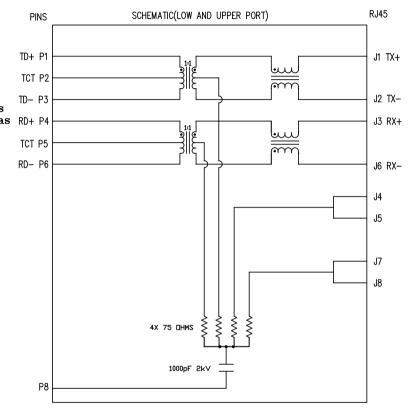
9.0 RISE TIME: RS=100 OHMS AND RL = 100 OHMS

OUTPUT VOLTAGE = 1 V peak: 3.0 nS MAX PULSE WIDTH= 112nS : 3.0 nS MAX

10.0 CROSS TALK: 1MHz TO 100MHz : 40 dB TYP

11.0 COMMON TO COMMON MODE ATTENUATIONMHz TO 100MHz : 35dB TYP

TITLE



NOTES

1.0 PINS WITHOUT ELECTRICAL CONNECTION ARE OMITTED.

ORIGINATED BY	DATE
Zeng Xiao Chun	2011-08-18
DRAWN BY	DATE
Zhang Ru Sheng	2011-08-18

10/100BT STACK JACK, SHIELDED

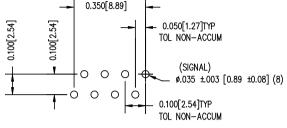
PATENTED

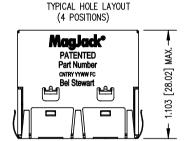
PART NO. / DRAWING NO.	STANDARD DIM.		[] METRIC DIM. AS REFERENCE	
SI-30108-F	TOL. IN	INCH	UNIT : INCH [mm]	REV.: A0
FILE NAME	x		SCALE: N/A	SIZE: A4
SI-30108-F.DWG	.XX		A 1	
31-30100-1.0443	.xxx	±0.005		PAGE: 2



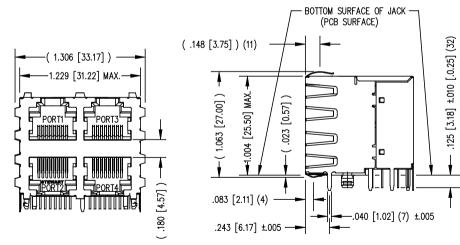
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0.350[8.89]





TITLE



0.950[24.13] ø.062 ±.003 [1.57 ±0.08] (6) (SHIELD) 0.525[13.34] -0.400[10.16] PIN #1 (2) 0 0 0 ф 0 0 0 0 0 0 0 0.609[15.47] 0 0 0 0 0 0 0 0 0.660[16.76] 0.350[8.89] 0.448[11.38] - 0 0 0 0 0 0 PIN #1 (2) 0.231[5.87] \oplus 0.180[4.57] 0.110[2.79] (2) 1.000[25.40] 1.084 27.53 Ø0.128 3.25 0.125 3.17 1.110[28.20] (2) P.C.B. RECOMMENDED HOLE LAYOUT SEEN FROM COMPONENT SIDE

NOTES:

1. CONNECTOR MATERIALS:
HOUSING: THERMOPLASTIC UL94 V-0
CONTACT/SHIELD: COPPER ALLOY
SHIELD PLATING: NICKEL OR TIN
CONTACT PLATING: SELECTIVE GOLD,
50 MICRO-INCHES MIN. IN CONTACT AREA.

ALL CENTERLINE DIMENSIONS ARE BASIC.

- 2. PIN NOT ELECTRICALLY CONNECTED MAYBE OMITTED. SEE ELECTRICAL DRAWING FOR OMITTED PINS.
- 3. TOLERANCES COMPLY WITH F.C.C. DIMENSION REQUIREMENTS.
- 4. ALL TOLERANCES NOT OTHERWISE SPECIFIED TO BE ±.005 [0.13]
- 5. WAVE SOLDER COMPATIBLE PREHEAT 125°C/90SECS.

ORIGINATED BY

Zeng Xiao Chun

DRAWN BY

Zhang Ru Sheng

DATE

2011-08-18

MagJack®

STACK JACK, SHIELDED

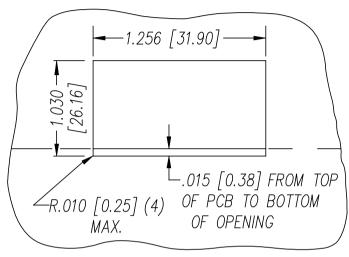
PART NO. / DRAWING NO.	TOL. IN INCH		[] METRIC DIM. AS REFERENCE	
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FILE NAME	.X		SCALE: N/A	SIZE: A4
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	.XXX	±0.005		PAGE: 3



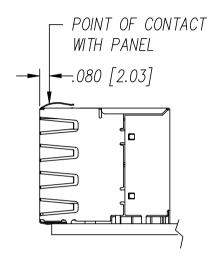
RoHS

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SUGGESTED PANEL OPENING (N.T.S.)



- 1. THE SUGGESTED PANEL OPENING IS INTENDED TO GIVE THE USER THE ABILITY TO HAVE REASONABLE JACK / PANEL CLEARANCES YET MAINTAIN RELIABLE GROUNDING CAPABILITY.
- 2. ALL TOLERANCES NOT OTHERWISE SPECIFIED TO BE ±.005 [0.13]

ORIGINATED BY	DATE	TITLE
Zeng Xiao Chun	2011-08-18	
DRAWN BY	DATE	
Zhang Ru Sheng	2011-08-18	

MagJack®
10/100BT
STACK JACK, SHIELDED
PATENTED

PART NO. / DRAWING NO.	STANDARD DIM. TOL. IN INCH		STANDARD DIM. [] METRIC DIM. AS REFERENCE	
SI-30108-F			UNIT : INCH [mm]	REV.: A0
FILE NAME	<u>.x</u>		SCALE: N/A	SIZE : A4
SI-30108-F.DWG	.XX.		SCALE. N/A	SIZE . A4
	.xxx	±0.005	(()	PAGE: 4

