





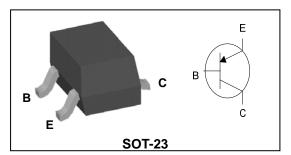
# **Description**

• General small signal amplifier

#### **Features**

- Low collector saturation voltage :  $V_{CE(sat)} = -0.3V(Max.)$
- Low output capacitance : Cob=4pF(Typ.)
- Complementary pair with 2SC5343S

# **PIN Connection**



# **Ordering Information**

Type NO.	Marking	Package Code
2SA1980S	<u>CA</u> □ □ □	SOT-23

<sup>1)</sup> Device Code 2) hFE Rank 3) Year & Week Code

### **Absolute Maximum Ratings**

(Ta=25°C)

Characteristic	Symbol	Rating	Unit
Collector-base voltage	$V_{CBO}$	-50	V
Collector-emitter voltage	$V_{CEO}$	-50	V
Emitter-base voltage	$V_{EBO}$	-5	V
Collector current	I <sub>C</sub>	-150	m A
Collector power dissipation	P <sub>C</sub> *	350	m W
Junction temperature	T <sub>j</sub>	150	°C
Storage temperature range	T <sub>stg</sub>	-55~150	°C

<sup>\*</sup> Package mounted on 99.5% alumina 10×8×0.6mm

#### **Electrical Characteristics**

 $(Ta=25^{\circ}C)$ 

Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Collector-emitter breakdown voltage	BV <sub>CEO</sub>	$I_{C} = -1  \text{m A}, I_{B} = 0$	-50	-	-	V
Collector cut-off current	I <sub>CBO</sub>	$V_{CB} = -50 \text{ V}, I_{E} = 0$	-	-	-0.1	μΑ
Emitter cut-off current	I <sub>EBO</sub>	V <sub>EB</sub> = -5 V, I <sub>C</sub> = 0	-	-	-0.1	μΑ
DC current gain	h <sub>FE</sub> *	$V_{CE} = -6V, I_{C} = -2mA$	70	-	700	-
Collector-emitter saturation voltage	$V_{CE(sat)}$	$I_C = -100 \text{ mA}, I_B = -10 \text{ mA}$	-	-	-0.3	V
Transition frequency	f <sub>T</sub>	$V_{CE}$ = -10V, $I_{C}$ = -1mA	80	-	-	MHz
Collector output capacitance	C <sub>ob</sub>	$V_{CB}$ = -10 V, $I_{E}$ = 0, $f$ = 1 MHz	-	4	-	pF
Noise figure	NF	$V_{CE}$ = -6V, $I_{C}$ = -0.1mA f= 1KHz, Rg= 10K $\Omega$	-	10	-	dB

<sup>\*:</sup> h<sub>FE</sub> rank / O: 70~140, Y: 120~240, G: 200~400, L: 300~700.

# **Electrical Characteristic Curves**

Fig. 1 P<sub>C</sub> - T<sub>a</sub>

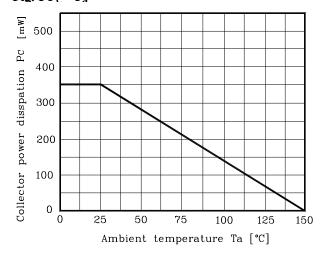


Fig. 3  $I_{\text{C}}$  -  $V_{\text{CE}}$ 

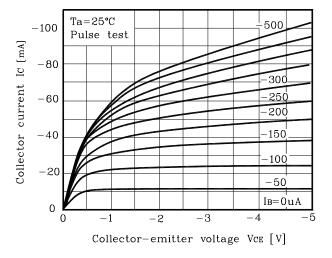


Fig. 5  $V_{\text{CE}(\text{sat})}$  -  $I_{\text{C}}$ 

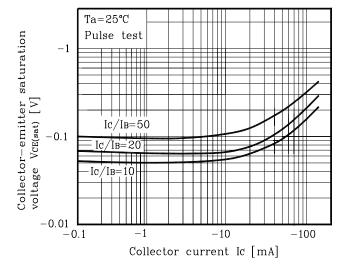


Fig. 2  $I_C$  -  $V_{BE}$ 

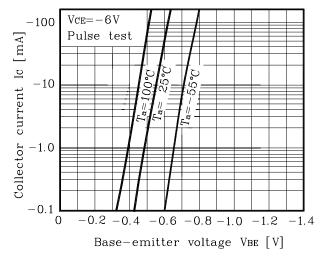
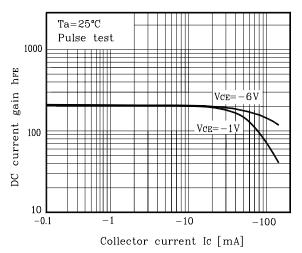
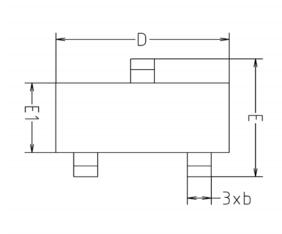
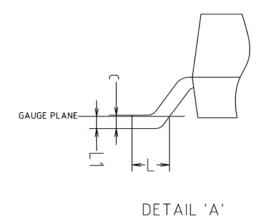


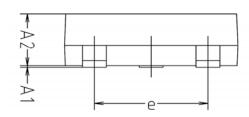
Fig. 4  $h_{\text{FE}}$  -  $I_{\text{C}}$ 

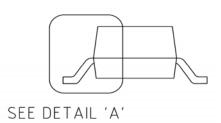


# **Outline Dimension**



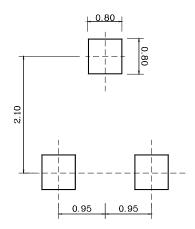






SYMBOL	MILLIMETERS			NOTE
STITLDOL	MINIMUM	NOMINAL	MAXIMUM	NOTE
Α1	0.00	-	0.10	
A2	0.82	-	1.02	
Ь	0.39	0.42	0.45	
С	0.09	0.12	0.15	
D	2.80	2.90	3.00	
Ε	2.20	2.40	2.60	
E1	1.20	1.30	1.40	
е	1.90BSC			
L	0.20	-	-	
L1	0.12BSC			

# \*Recommend PCB solder land [Unit: mm]



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