

# 2SA1980SF

**PNP Silicon Transistor** 

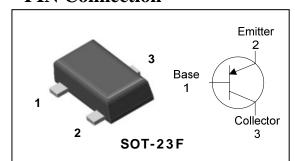
### **Description**

• General small signal amplifier

#### **Features**

- Low collector saturation voltage :  $V_{CE(sat)} = -0.3V(Max.)$
- Low output capacitance :  $C_{ob} = 4pF(Typ.)$
- Complementary pair with 2SC5343SF

#### **PIN Connection**



## **Ordering Information**

Type NO.	Marking	Package Code
2SA1980SF	<u>CA</u> □ □ □ 0 3 3	SOT-23F

1) Device Code 2) hFE Rank 3) Year & Week Code

## **Absolute maximum ratings**

(Ta=25°C)

Characteristic	Symbol	Ratings	Unit
Collector-Base voltage	$V_{CBO}$	-50	V
Collector-Emitter voltage	$V_{CEO}$	-50	V
Emitter-Base voltage	$V_{EBO}$	-5	V
Collector current	I <sub>C</sub>	-150	m A
Collector dissipation	P <sub>C</sub>	200	m W
Junction temperature	$T_j$	150	°C
Storage temperature	T <sub>stg</sub>	-55~ 150	°C

#### **Electrical Characteristics**

(Ta=25°C)

Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Collector-Base breakdown voltage	BV <sub>CBO</sub>	$I_C = -100 \mu A, I_E = 0$	-50	-	-	V
Collector-Emitter breakdown voltage	BV <sub>CEO</sub>	$I_C = -1 \text{mA}, I_B = 0$	-50	-	-	V
Emitter-Base breakdown voltage	BV <sub>EBO</sub>	$I_E = -10 \mu A, I_C = 0$	-5	-	-	V
Collector cut-off current	I <sub>CBO</sub>	$V_{CB} = -50 \text{ V}, I_{E} = 0$	-	-	-0.1	μΑ
Emitter cut-off current	I <sub>EBO</sub>	V <sub>EB</sub> = -5V, I <sub>C</sub> = 0	-	-	-0.1	μΑ
DC current gain	h <sub>FE</sub> *	$V_{CE} = -6V, I_{C} = -2mA$	70	-	700	-
Collector-Emitter saturation voltage	V <sub>CE(sat)</sub>	$I_C = -100 \text{ mA}, I_B = -10 \text{ mA}$	-	-	-0.3	V
Transition frequency	f <sub>T</sub>	$V_{CE}$ = -10V, $I_{C}$ = -1mA	80	-	-	MHz
Collector output capacitance	C <sub>ob</sub>	$V_{CB} = -10 \text{ V}, I_{E} = 0, f = 1 \text{ MHz}$	-	4	7	pF
Noise figure	NF	$V_{CE}$ = -6V, $I_{C}$ = -0.1mA f= 1KHz, Rg= 10K $\Omega$	-	-	10	dB

<sup>\*:</sup>  $h_{FE}$  rank / O : 70~140, Y : 120~240, G : 200~400, L : 300~700.

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## **Electrical Characteristic Curves**

Fig. 1 P<sub>C</sub>-T<sub>a</sub>

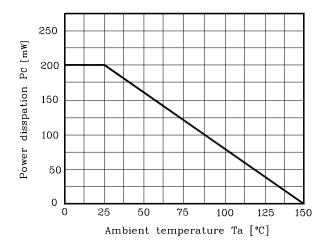


Fig. 3  $I_{\text{C-V}_{\text{CE}}}$ 

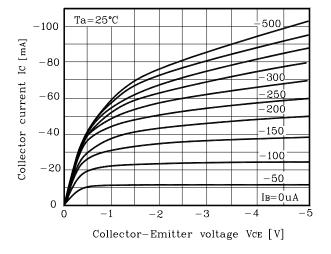


Fig. 5  $V_{CE(sat)}$ - $I_C$ 

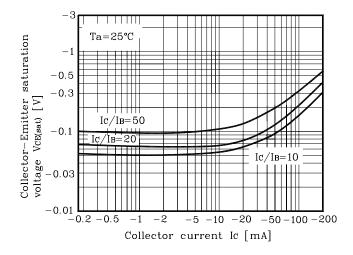


Fig. 2  $I_{\text{C-}}V_{\text{BE}}$ 

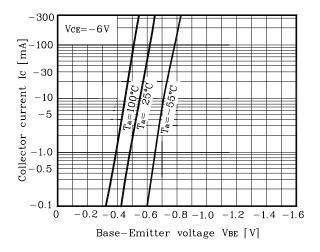
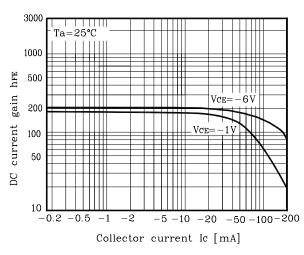


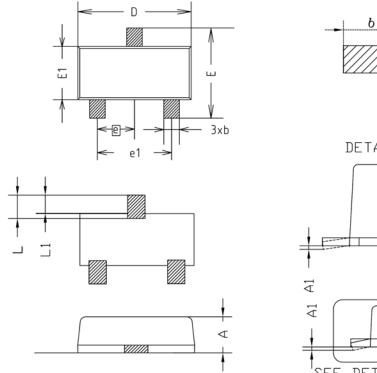
Fig. 4 h<sub>FE</sub>-I<sub>C</sub>

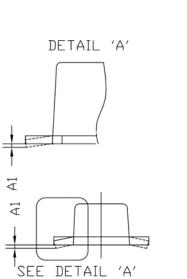


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SECTION

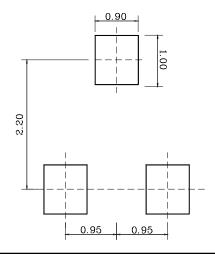
# **Outline Dimension**





SYMBOL	١	NOTE		
STADUL	MINIMUM	NDMINAL	MAXIMUM	NUIL
Α	0.80	0.90	1.00	
A1	0.00	_	0.10	
b	0.35	0.40	0.45	
C	0.10	0.15	0.20	
D	2.80	2.90	3.00	
Ε	2.30	2.40	2.50	
E1	1.50	1.60	1.70	
е	0.95BSC			
e1	1.80	1.90	2.00	
L	0.48	0.58	0.68	
L1	0.30	-	0.50	

## \*Recommend PCB solder land [Unit: mm]



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