

# 2SC5342SF

**NPN Silicon Transistor** 

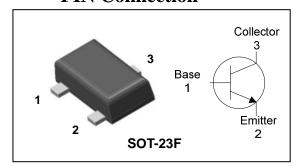
## **Description**

• Medium power amplifier

### **Features**

- Large collector current : I<sub>C</sub>= 500mA
- Low collector saturation voltage enabling low-voltage operation
- Complementary pair with 2SA1979SF

### **PIN Connection**



# **Ordering Information**

Type NO.	Marking	Package Code
2SC5342SF	<u>BA</u> □ □ □ 3	SOT-23F
	<u> </u>	-

①Device Code ②hFE Rank ③Year&Week Code

# **Absolute maximum ratings**

(Ta=25°C)

Characteristic	Symbol	Ratings	Unit
Collector-Base voltage	$V_{CBO}$	40	V
Collector-Emitter voltage	$V_{CEO}$	32	V
Emitter-Base voltage	$V_{EBO}$	5	V
Collector current	Ic	500	m A
Collector dissipation	P <sub>C</sub>	200	m W
Junction temperature	T <sub>j</sub>	150	°C
Storage temperature	T <sub>stg</sub>	-55~150	°C

### **Electrical Characteristics**

(Ta=25°C)

Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Collector-Base breakdown voltage	BV <sub>CBO</sub>	$I_C = 100 \mu A, I_E = 0$	40	-	-	V
Collector-Emitter breakdown voltage	BV <sub>CEO</sub>	$I_{C}=1 \text{ m A}, I_{B}=0$	32	-	-	V
Emitter-Base breakdown voltage	BV <sub>EBO</sub>	$I_E = 10 \mu A, I_C = 0$	5	-	-	V
Collector cut-off current	I <sub>CBO</sub>	V <sub>CB</sub> = 40V, I <sub>E</sub> = 0	-	-	0.1	μА
Emitter cut-off current	I <sub>EBO</sub>	$V_{EB} = 5 V$ , $I_{C} = 0$	-	-	0.1	μΑ
DC current gain	h <sub>FE</sub> *	$V_{CE} = 1V, I_{C} = 100 \text{ m A}$	70	-	240	-
Collector-Emitter saturation voltage	V <sub>CE(sat)</sub>	I <sub>C</sub> = 100mA, I <sub>B</sub> = 10mA	-	-	0.25	V
Transition frequency	f <sub>T</sub>	$V_{CE} = 6V, I_{C} = 20mA$	-	300	-	MHz
Collector output capacitance	C <sub>ob</sub>	$V_{CB} = 6V, I_{E} = 0, f = 1MHz$	-	7.0	-	pF

<sup>\*:</sup> h<sub>FE</sub> Rank / O: 70~140, Y: 120~240

KSD-T5C049-001

### **Electrical Characteristic Curves**

Fig. 1 Pc - Ta

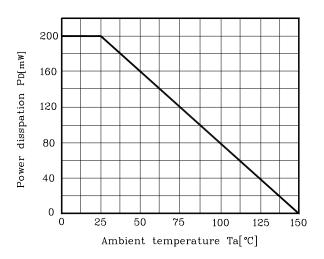


Fig. 3  $I_C$  -  $V_{CE}$ 

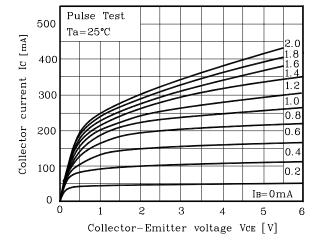


Fig. 5  $h_{\text{FE}}$  -  $I_{\text{C}}$ 

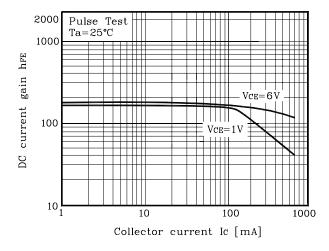


Fig. 2  $I_{\text{C}}$  -  $V_{\text{BE}}$ 

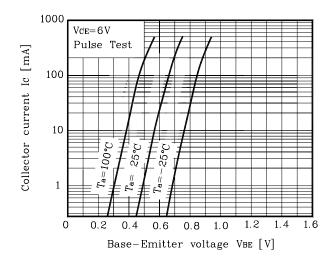
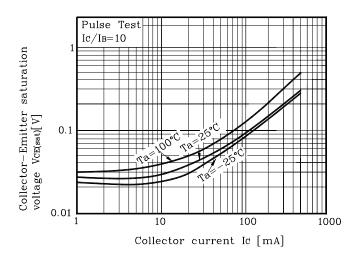


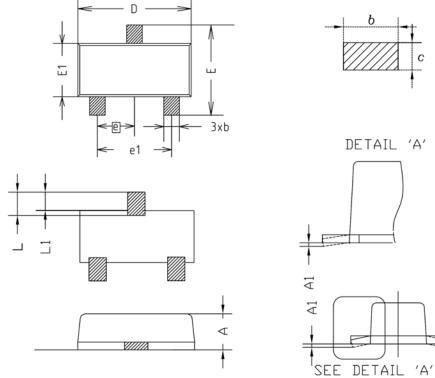
Fig. 4  $V_{\text{CE(SAT)}}$  -  $I_{\text{C}}$ 

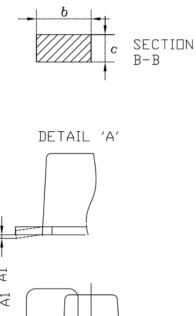


KSD-T5C049-001 2

3

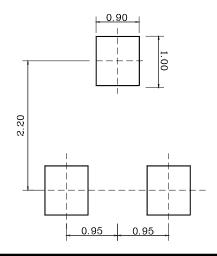
# **Outline Dimension**





SYMBOL	MILLIMETER(mm)			NOTE
STINDUC	MINIMUM	NDMINAL	MAXIMUM	ואטוב
Α	0.80	0.90	1.00	
A1	0.00	-	0.10	
b	0.35	0.40	0.45	
C	0.10	0.15	0.20	
D	2.80	2.90	3.00	
Ε	2.30	2.40	2.50	
E1	1.50	1.60	1.70	
е	0.95BSC			
e1	1.80	1.90	2.00	
L	0.48	0.58	0.68	
L1	0.30	_	0.50	

## \*Recommend PCB solder land [Unit: mm]



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KSD-T5C049-001 4