Bi-directional ESD Protection TVS Diode

General Description

The NDB171 device is help protect sensitive electronic equipment against electrostatic discharge (ESD). The NDB171 device is safely dissipate ESD strikes, exceeding the IEC 61000-4-2 International Standard, Level 4 (±8kV contact discharge and ±15kV air discharge).

Features and Benefits

- Low capacitance and fast response time
- Bidirectional type pin configuration structure
- Compact SMD package saves board space and facilitates layout in space-critical applications
- Full lead(Pb)-free device and RoHS compliant
- Available in "Green" device

Applications

• ESD Protection of PC ports, including USB ports, Cell phone handsets and accessories, etc.

Ordering Information

Part Number	Marking Code	Package	Packaging
NDB171	B	SOD-923	Tape & Reel

Marking Information



B = Specific Device Code

□ = Year & Week Code Marking



Pinning Information

Pin	Description	Simplified Outline	Graphic Symbol	
1	Cathode			
2	Anode			

KSD-D6E010-001







Absolute Maximum Ratings (Tamb=25°C, Unless otherwise specified)

Characteristic	Symbol	Ratings	Unit	
ESD withstand valtage per IEC 61000.4.2 standard	$V_{ESD(Air)}$	±15	kV	
ESD withstand voltage per IEC 61000-4-2 standard	V _{ESD(Contact)}	±8		
Peak pulse power (tp = 8/20us)	P _{PK}	100	W	
Power dissipation ¹⁾	P _D	100	mW	
Junction temperature	TJ	150	°C	
Storage temperature range	T _{stg}	-55 ~ +150	°C	

¹⁾ Device mounted on FR-4 board with recommended pad layout.

Thermal Characteristics (T_{amb}=25°C, Unless otherwise specified)

Characteristic	Symbol	Ratings	Unit
Thermal resistance, junction to ambient ¹⁾	$R_{th(j-a)}$	1250	°C/W

¹⁾ Device mounted on FR-4 board with recommended pad layout.

Electrical Characteristics (T_{amb}=25°C, Unless otherwise specified)

Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Breakdown voltage	V_{BR}	I _R =1mA	5.78	-	7.82	V
Reverse current	I _R	V _R =3.5V	-	-	0.5	uA
Total capacitance	Ст	V _R =0V, f=1MHz	-	25	-	pF

Rating and Characteristic Curves







Fig. 2) Power Dissipation vs. Ambient Temperature



Fig. 4) Power Dissipation vs. Ambient Temperature



Package Outline Dimensions (Unit : mm)









SYMBOL	١	NOTE		
STIDUL	MINIMUM	NOMINAL	MAXIMUM	NUTL
A	0.39	0.40	0.41	
A1	-	-	0.05	
A2	-	-	0.43	
b	0.17	0.22	0.27	
С	0.08	0.11	0.14	
D	0.55	0.60	0.65	
E	0.90	1.00	1.10	
E1	0.75	0.80	0.85	
L	0.10	0.18	0.26	
L1	0.05	0.10	0.15	
θ		5° REF		

※ Recommend PCB solder land (Unit : mm)



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