

Low Drop output Voltage Regulator

### **Descriptions**

• Three Terminal Positive Low Dropout Voltage Regulator

#### **Features**

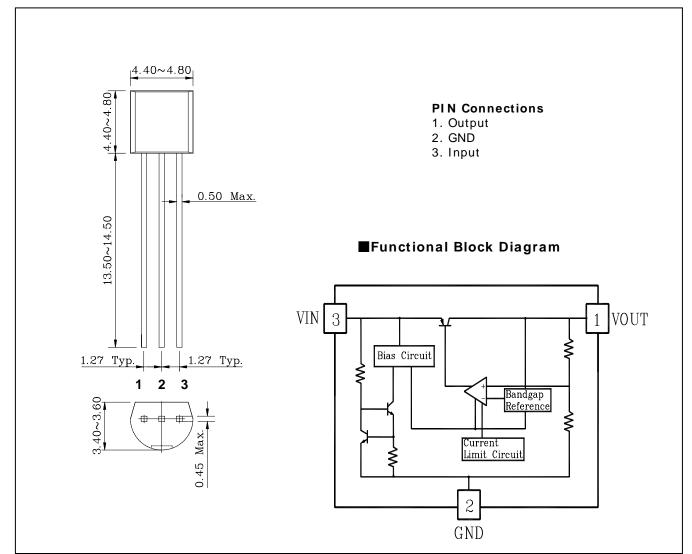
- Low Standby Current Consumption (500 # Typ.)
- Maximum Output Current (180 mA Max.)
- Less I/O voltage Difference (250 mV Max.)

# **Ordering Information**

Type NO.	Marking	Package Code
S78DL33	S78DL33	TO-92

### **Outline Dimensions**

unit: mm



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# **Maximum ratings**

Ta=25°C

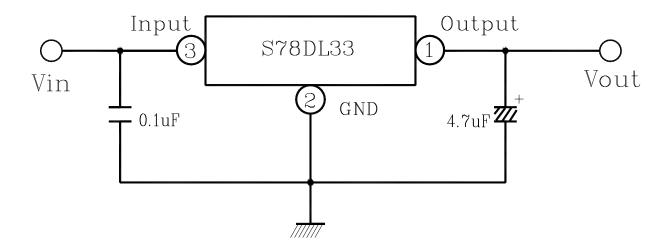
Characteristic	Symbol	Ratings	Unit
Operating Input voltage	$V_{IN}$	16	V
Power Dissipation	$P_D$	625	m W
Operating Temperature Range	$T_OPR$	-40~+85	°C
Junction Temperature	$T_j$	150	°C
Storage Temperature Range	T <sub>stg</sub>	-55~150	°C
Lead Temperature Time	T <sub>sol</sub>	260 (10 Sec)	°C

### **Electrical Characteristics**

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Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Output voltage	V <sub>OUT</sub>	V <sub>IN</sub> = 4.3 V, I <sub>OUT</sub> = 100uA	3.168	3.3	3.432	V
Voltage Regulation	△ V <sub>OUT</sub> (1)	V <sub>IN</sub> = 4.3 V~ 10 V, I <sub>OUT</sub> = 100 uA	-	2	15	m V
Load Regulation	∆ V <sub>OUT</sub> (2)	$V_{IN}$ = 4.3 $V$ , $I_{OUT}$ = 1~100 $m$ A	-	7	28	m V
Dropout Voltage	V	I <sub>OUT</sub> =50mA	-	110	230	m V
	$V_{DROP}$	I <sub>OUT</sub> = 100m A	-	150	300	
Ripple Rejection Ratio	RR	f= 100Hz, I <sub>OUT</sub> = 100uA	-	75	-	dB
Ground pin Current		V <sub>IN</sub> = 4.3V , I <sub>OUT</sub> = 100uA		200	400	uA
	I <sub>GND</sub>	$V_{IN}=4.3V$ , $I_{OUT}=50mA$		0.9	1.8	m A
		$V_{IN}$ = 4.3 $V$ , $I_{OUT}$ = 100m A		2.1	4	m A

# **■** Test circuit



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### **Electrical Characteristic Curves**

Fig. 1. Vin - Vout

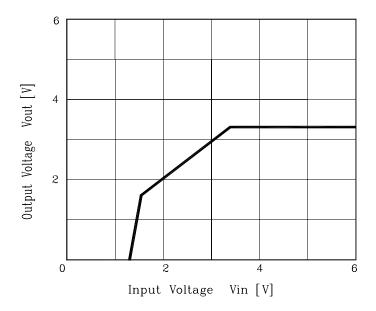


Fig. 3. Pd - Ta

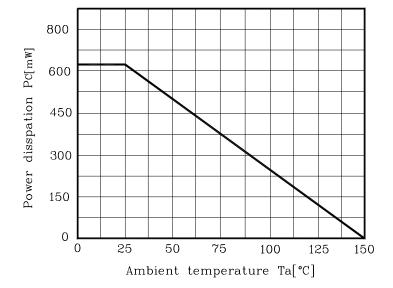


Fig. 2 |Vout - Vin| -I OUT

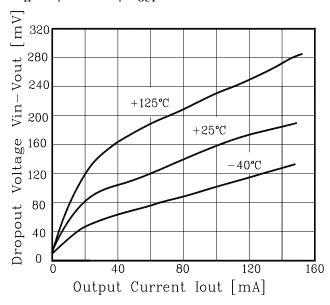
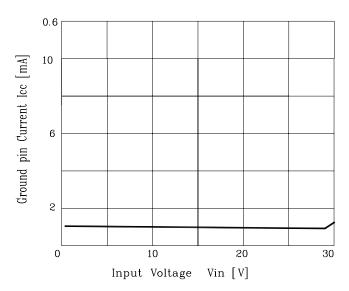


Fig. 4. Input voltage – Ground pin Current



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