

NPN Silicon Transistor

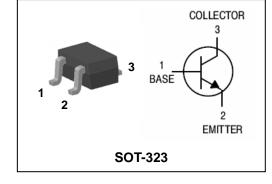
Descriptions

- General purpose application
- Switching application

Features

- Low Leakage current
- Low collector saturation voltage enabling low voltage operation
- Complementary pair with SBT2907AU

Ordering Information



PIN Connection

Type NO.	Marking	Package Code
SBT2222AU	<u>IQ</u> □ ① ②	SOT-323

①Device Code ② Year&Week Code

Absolute maximum ratings

Ta=25°C

Characteristic	Symbol	Ratings	Unit
Collector-Base voltage	V_{CBO}	75	V
Collector-Emitter voltage	V_{CEO}	40	V
Emitter-base voltage	V_{EBO}	5	V
Collector current	I _C	600	m A
Collector dissipation	$P_C^{^\star}$	350	m W
Junction temperature	T_{j}	150	°C
Storage temperature range	T_{stg}	-55~ 150	°C

^{* :} Package mounted on 99.5% alumina 10×8×0.6mm

Electrical Characteristics

Ta=25°C

Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Collector-Base breakdown voltage	BV _{CBO}	$I_C = 10 \mu A, I_E = 0$	75	-	-	V
Collector-Emitter breakdown voltage	BV _{CEO}	$I_{C}=1 \text{ m A}, I_{B}=0$	40	-	-	V
Emitter-Base breakdown voltage	BV _{EBO}	$I_E = 10 \mu A, I_C = 0$	5	-	-	V
Collector cut-off current	I _{CBO}	$V_{CB} = 75 \text{ V}, \ I_{E} = 0$	-	-	20	nA
DC current gain	h _{FE}	V _{CE} = 10V, I _C = 10m A	100	-	-	-
Collector-Emitter saturation voltage	$V_{CE(sat)}$	I _C = 150mA, I _B = 15mA	-	-	0.4	V
Transition frequency	f _T	V_{CE} = 20V, I_{C} = 20mA, f = 100MHz	250	-	-	MHz
Collector output capacitance	C _{ob}	$V_{CB} = 10 \text{ V}, I_{E} = 0, f = 1 \text{ MHz}$	-	ı	8	pF
Delay time	t _d	$V_{CC} = 30V_{dc}, V_{BE(off)} = 0.5V_{dc},$	-	-	10	ns
Rise time	t _r	$I_{C} = 150 \text{ m A}_{dc}, I_{B1} = 15 \text{ m A}_{dc}$	-	-	25	ns
Storage time	ts	$V_{CC} = 30V_{dc}, I_{C} = 150 \text{ m A}_{dc},$	-	-	225	ns
Fall Time	t _f	$I_{B1} = I_{B2} = 15 \text{ m A}_{dc}$	-	-	60	ns

Electrical Characteristic Curves

Fig. 1 P_C-T_a

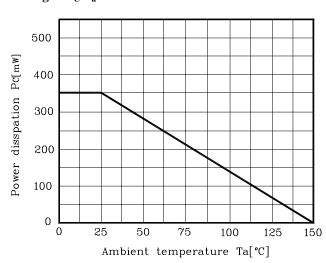


Fig. 2 h_{FE} I_C

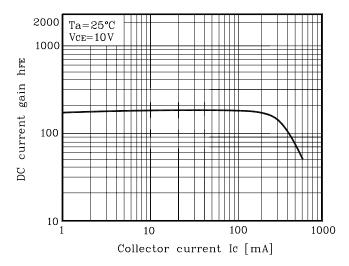


Fig. 3 $V_{\text{CE(sat)}}\text{-}I_{\text{C}}$

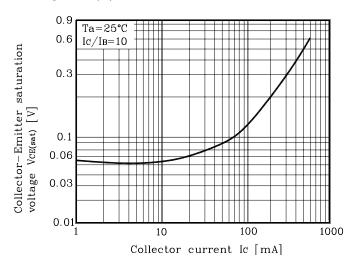
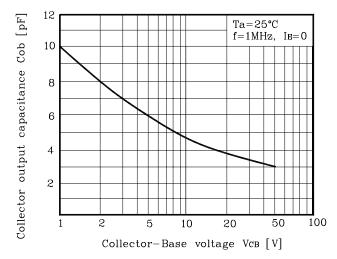
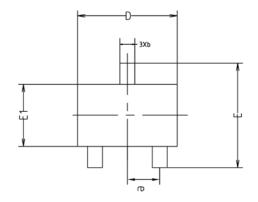
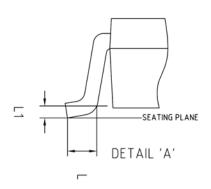


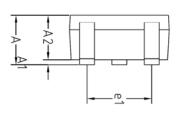
Fig. 4 C_{ob} - V_{CB}

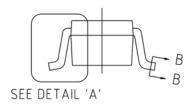


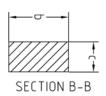
Outline Dimension





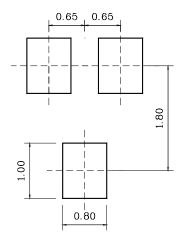






SYMBOL	1	NOTE		
3 THEOL	MINIMUM	NOMINAL	MAXIMUM	NUTE
Α	0.90	-	1.25	
A1	0.00	-	0.10	
A2	0.85	0.90	0.95	
Ь	0.30	-	0.40	
С	0.10	-	0.25	
D	1.90	2.00	2.10	
Ε	1.95	2.10	2.25	
E1	1.15	1.25	1.35	
е				
e1	1.20	-	1.40	
L	0.10	-	-	
L1	0.12BSC			

*Recommend PCB solder land [Unit: mm]



KSD-T5D003-000

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