

# **SRA2205SF**

**PNP Silicon Transistor** 

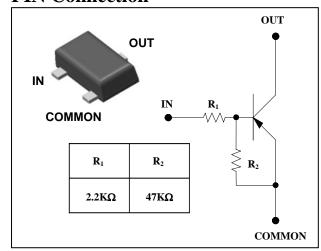
## **Descriptions**

- Switching application
- Interface circuit and driver circuit application

#### **Features**

- With built-in bias resistors
- Simplify circuit design
- Reduce a quantity of parts and manufacturing process
- High packing density

## **PIN Connection**



## **Ordering Information**

Type NO.	Marking	Package Code	
SRA2205SF	<u>RA5</u> □ 0	SOT-23F	

1) Device Code 2) Year & Week Code

## **Absolute Maximum Ratings**

(Ta=25°C)

Characteristic	Symbol	Rating	Unit
Output voltage	Vo	-50	V
Input voltage	V <sub>I</sub>	-15, 5	V
Output current	Io	-100	m A
Power dissipation	P <sub>D</sub>	200	m W
Junction temperature	TJ	150	°C
Storage temperature range	T <sub>stg</sub>	-55 ~ 150	°C

#### **Electrical Characteristics**

(Ta=25°C)

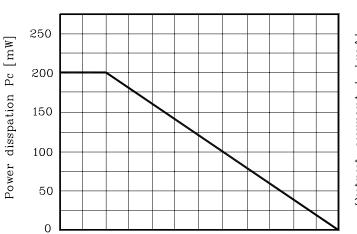
Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Output cut-off current	I <sub>O(OFF)</sub>	$V_0 = -50 \text{ V}, \ V_1 = 0$	-	-	-500	nΑ
DC current gain	Gı	$V_0 = -5V$ , $I_0 = -10 \text{ m A}$	80	200	-	-
Output voltage	V <sub>O(ON)</sub>	$I_{O} = -10 \text{ m A}, I_{I} = -0.5 \text{ m A}$	-	-0.1	-0.3	V
Input voltage (ON)	$V_{I(ON)}$	$V_0 = -0.2V$ , $I_0 = -5 \text{ m A}$	-	-	-1.1	V
Input voltage (OFF)	V <sub>I(OFF)</sub>	$V_O = -5V$ , $I_O = -0.1 \text{mA}$	-0.5	-	-	V
Transition frequency	f <sub>T</sub> *	$V_O = -10V$ , $I_O = -5mA$ , $f = 1MHz$	-	200	-	MHz
Input current	I <sub>1</sub>	$V_1 = -5 V$ , $I_0 = 0$	-	-	-3.6	m A
Input resistor (Input to base)	R <sub>1</sub>	-	1.54	2.2	2.86	<b>K</b> Ω
Input resistor (Base to common)	$R_2$	-	33	47	61	<b>K</b> Ω

<sup>\* :</sup> Characteristic of transistor only

KSD-R5C032-000

## **Electrical Characteristic Curves**





75

Ambient temperature Ta [°C]

100

125

Input on voltage Vi(ON) [V]

Fig. 3  $I_O$  -  $V_{I(OFF)}$ 

25

50

0

Fig. 1 Pc - Ta

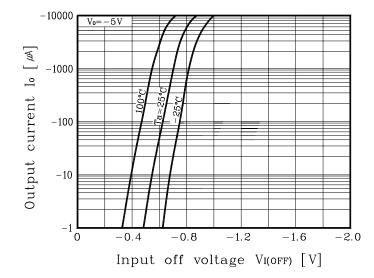
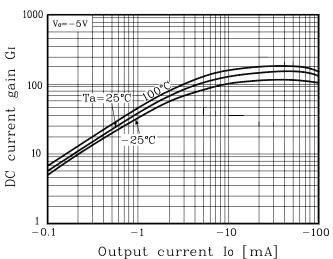


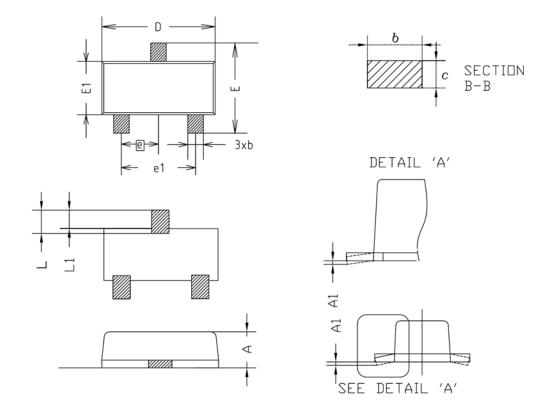
Fig. 4 G<sub>I</sub> - I<sub>O</sub>

Fig. 2  $I_{O}$  -  $V_{I\left(ON\right)}$ 



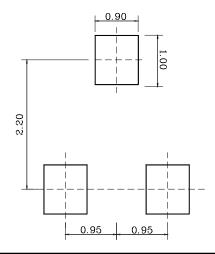
# SRA2205SF

# **Outline Dimension**



SYMBOL	1	NOTE		
STADUL	MINIMUM	NDMINAL	MAXIMUM	NUIL
Α	0.80	0.90	1.00	
A1	0.00	-	0.10	
b	0.35	0.40	0.45	
C	0.10	0.15	0.20	
D	2.80	2.90	3.00	
Ε	2.30	2.40	2.50	
E1	1.50	1.60	1.70	
е	0.95BSC			
e1	1.80	1.90	2.00	
L	0.48	0.58	0.68	
L1	0.30	-	0.50	

## \*Recommend PCB solder land [Unit: mm]



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