

PNP Silicon Transistor

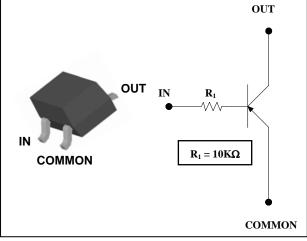
Descriptions

- Switching application
- Interface circuit and driver circuit application

Features

- With built-in bias resistor
- Simplify circuit design
- Reduce a quantity of parts and manufacturing process
- High packing density

PIN Connection



Ordering Information

Type NO.	Marking	Package Code
SRA2211S	<u>RAD</u> □ ① ②	SOT-23
	Dovice Code OVeereWeek Code	

①Device Code ②Year&Week Code

Absolute Maximum Ratings

Absolute Maximum Ratings			(Ta=25°C)
Characteristic	Symbol	Rating	Unit
Output voltage	Vo	-50	V
Input voltage	Vı	-30, 5	V
Output current	Ι _ο	-100	mA
Power dissipation	P _D	200	mW
Junction temperature	TJ	150	°C
Storage temperature range	T _{stg}	-55 ~ 150	°C

Electrical Characteristics

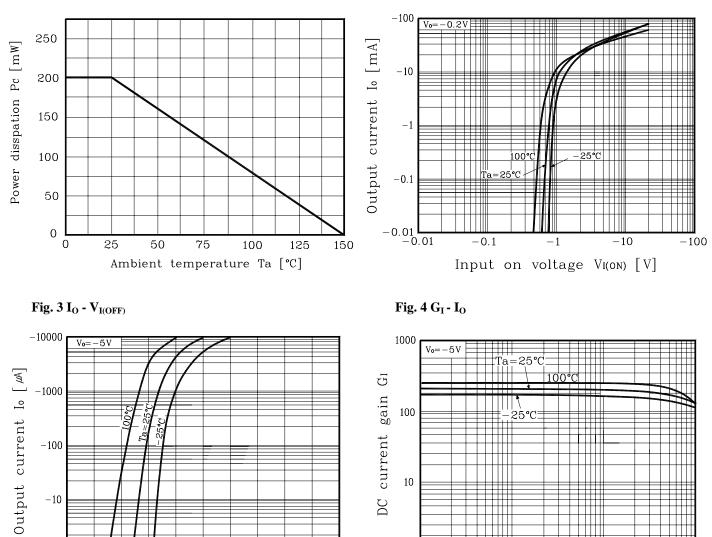
Electrical Characteristics (Ta=25					=25°C)	
Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Output cut-off current	I _{O(OFF)}	$V_0 = -50V, V_1 = 0$	-	-	-500	nA
DC current gain	Gı	$V_0 = -5V, I_0 = -10mA$	120	-	-	-
Output voltage	V _{O(ON)}	$I_0 = -10 \text{ mA}, I_1 = -0.5 \text{ mA}$	-	-0.1	-0.3	V
Input voltage (ON)	V _{I(ON)}	$V_0 = -0.2 V$, $I_0 = -5 m A$	-	-0.9	-1.4	V
Input voltage (OFF)	$V_{I(OFF)}$	$V_0 = -5V, I_0 = -0.1 \text{ mA}$	-0.3	-0.55	-	V
Transition frequency	f _T *	V_0 = -10V, I_0 = -5mA, f= 1MHz	-	200	-	MHz
Input current	I ₁	V ₁ =-5V, I ₀ =0	-	-	-0.88	mА
Input resistor (Input to base)	R ₁	-	7	10	13	KΩ

* : Characteristic of transistor only

Electrical Characteristic Curves



Fig. 2 $I_O - V_{I(ON)}$

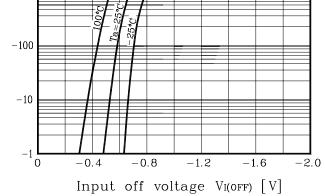


10

1

-0.1

DC



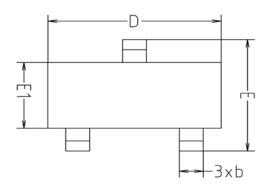
-100

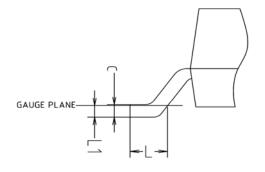
-10

Output current Io [mA]

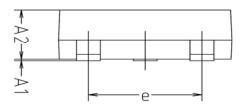
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Outline Dimension





DETAIL 'A'

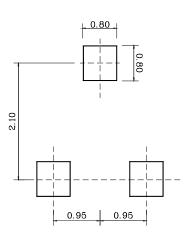




SEE DETAIL 'A'

SYMBOL	MILLIMETERS			NOTE
STIDUL	MINIMUM	NOMINAL	MAXIMUM	NOTE
A1	0.00	-	0.10	
A2	0.82	-	1.02	
Ь	0.39	0.42	0.45	
с	0.09	0.12	0.15	
D	2.80	2.90	3.00	
E	2.20	2.40	2.60	
E1	1.20	1.30	1.40	
е	1.90BSC			
L	0.20	-	-	
L1	0.12BSC			

*Recommend PCB solder land [Unit: mm]



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