

SRA2211SF

PNP Silicon Transistor

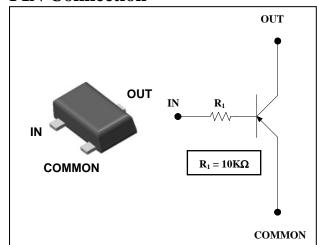
Descriptions

- Switching application
- Interface circuit and driver circuit application

Features

- With built-in bias resistor
- Simplify circuit design
- Reduce a quantity of parts and manufacturing process
- High packing density

PIN Connection



Ordering Information

Type NO.	Marking	Package Code	
SRA2211SF	<u>RAD</u> □ ① ②	SOT-23F	

1 Device Code 2 Year & Week Code

Absolute Maximum Ratings

(Ta=25°C)

Characteristic	Symbol	Rating	Unit
Output voltage	Vo	-50	V
Input voltage	VI	-30, 5	V
Output current	Io	-100	m A
Power dissipation	P_{D}	200	m W
Junction temperature	T_J	150	°C
Storage temperature range	T_{stg}	-55 ~ 150	°C

Electrical Characteristics

(Ta=25°C)

Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Output cut-off current	I _{O(OFF)}	$V_0 = -50 \text{V}, \ \ V_1 = 0$	-	-	-500	nA
DC current gain	Gı	$V_O = -5 V$, $I_O = -10 m A$	120	-	-	-
Output voltage	V _{O(ON)}	$I_{O} = -10 \text{mA}, \ I_{I} = -0.5 \text{mA}$	-	-0.1	-0.3	V
Input voltage (ON)	$V_{I(ON)}$	$V_O = -0.2 \text{ V}, I_O = -5 \text{ m A}$	-	-0.9	-1.4	V
Input voltage (OFF)	$V_{I(OFF)}$	$V_O = -5 V$, $I_O = -0.1 \text{ m A}$	-0.3	-0.55	-	V
Transition frequency	f _T *	$V_O = -10V$, $I_O = -5mA$, $f = 1MHz$	-	200	-	MHz
Input current	I ₁	$V_1 = -5V, I_0 = 0$	-	-	-0.88	m A
Input resistor (Input to base)	R ₁	-	7	10	13	ΚΩ

^{* :} Characteristic of transistor only

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Electrical Characteristic Curves

Fig. 1 Pc - Ta

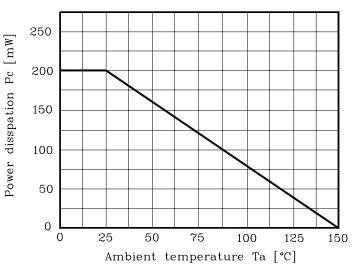


Fig. 2 I_O - $V_{I(ON)}$

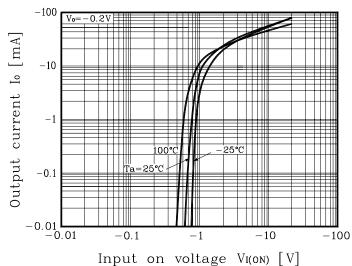


Fig. 3 $I_{\rm O}$ - $V_{\rm I(OFF)}$

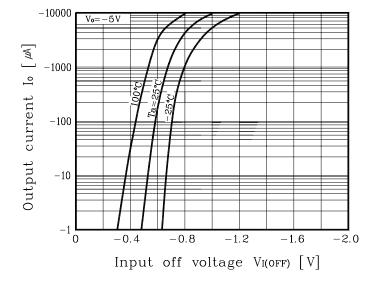
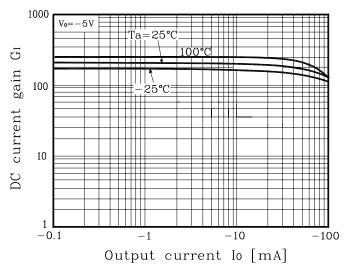


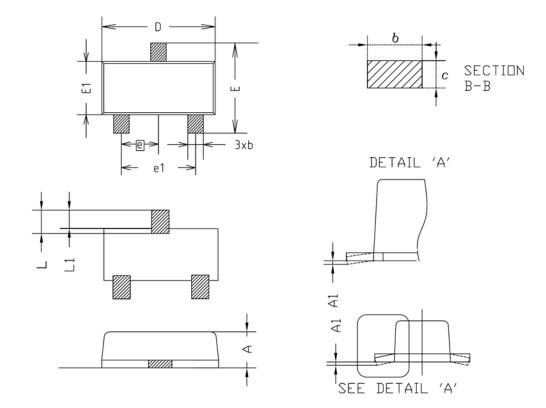
Fig. 4 G_I - I_O



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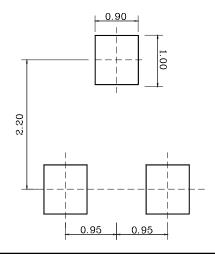
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Outline Dimension



SYMBOL	MILLIMETER(mm)			NOTE	
STADUL	MINIMUM	NDMINAL	MAXIMUM	NUIL	
Α	0.80	0.90	1.00		
A1	0.00	_	0.10		
b	0.35	0.40	0.45		
C	0.10	0.15	0.20		
D	2.80	2.90	3.00		
E	2.30	2.40	2.50		
E1	1.50	1.60	1.70		
е	0.95BSC				
e1	1.80	1.90	2.00		
L	0.48	0.58	0.68		
L1	0.30	-	0.50		

*Recommend PCB solder land [Unit: mm]



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