

NPN Silicon Transistor

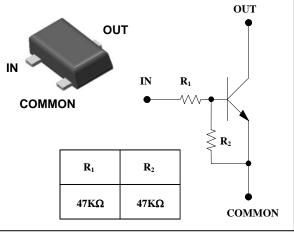
Descriptions

- Switching application
- Interface circuit and driver circuit application

Features

- With built-in bias resistors
- Simplify circuit design
- Reduce a quantity of parts and manufacturing process
- High packing density

PIN Connection



Ordering Information

Type NO.	Marking	Package Code
SRC1204SF	<u>RC4</u> <u>□</u> ① ②	SOT-23F
	Device Cede OVeerQVMeels Ce	

①Device Code ②Year&Week Code

Absolute Maximum Ratings

Absolute Maximum Ratings			(Ta=25°C)
Characteristic	Symbol	Rating	Unit
Output voltage	Vo	50	V
Input voltage	VI	40,-10	V
Output current	Ι _ο	100	mA
Power dissipation	P _D	200	mW
Junction temperature	TJ	150	°C
Storage temperature range	T _{stg}	-55 ~ 150	°C

Electrical Characteristics

Electrical Characteristics					(Ta:	=25°C)
Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Output cut-off current	I _{O(OFF)}	$V_0 = 50 V, V_1 = 0$	-	-	500	nA
DC current gain	Gı	V _O = 5V, I _O = 10m A	80	200	-	-
Output voltage	V _{O(ON)}	$I_0 = 10 \text{ mA}, I_1 = 0.5 \text{ mA}$	-	0.1	0.3	V
Input voltage (ON)	V _{I(ON)}	$V_0 = 0.2V, I_0 = 5mA$	-	2.8	5.0	V
Input voltage (OFF)	V _{I(OFF)}	$V_0 = 5V, I_0 = 0.1 \text{ mA}$	1.0	1.2	-	V
Transition frequency	f _T *	V_0 = 10V, I_0 = 5mA, f= 1MHz	-	200	-	MHz
Input current	I ₁	V ₁ =5V, I ₀ =0	-	-	0.18	mA
Input resistor (Input to base)	R ₁	-	33	47	61	KΩ
Input resistor (Base to common)	R ₂	-	33	47	61	KΩ

* : Characteristic of transistor only

Electrical Characteristic Curves

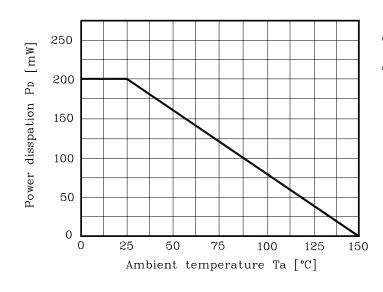


Fig. 1 P_D - Ta

Fig. 2 I_O - $V_{I(ON)}$

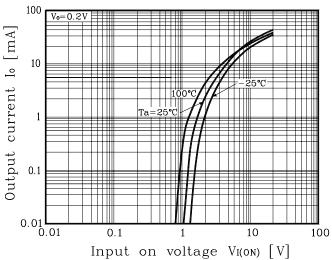
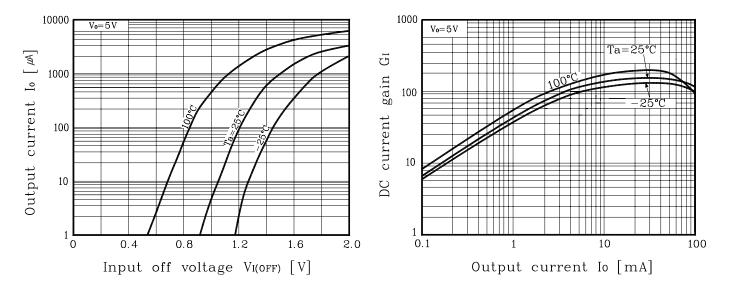
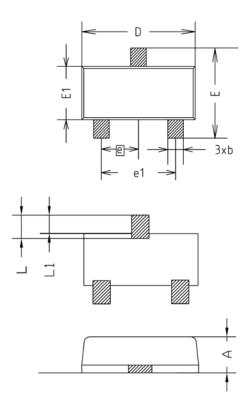


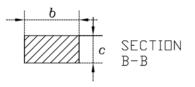
Fig. 3 I_O - V_{I(OFF)}

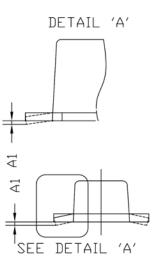
Fig. 4 G_I **- I**_O



Outline Dimension

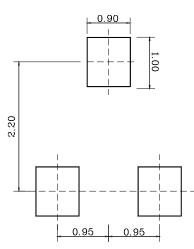






SYMBOL	MILLIMETER(mm)			NOTE
0 mbbbc	MINIMUM	NOMINAL	MAXIMUM	NUIC
A	0.80	0.90	1.00	
A1	0.00	-	0.10	
b	0.35	0.40	0.45	
С	0.10	0.15	0.20	
D	2.80	2.90	3.00	
E	2.30	2.40	2.50	
E1	1.50	1.60	1.70	
e	0.95BSC			
e1	1.80	1.90	2.00	
L	0.48	0.58	0.68	
L1	0.30	-	0.50	

*Recommend PCB solder land [Unit: mm]



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