

SRC1204UF

NPN Silicon Transistor

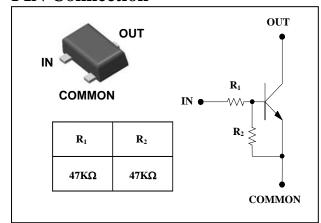
Descriptions

- Switching application
- Interface circuit and driver circuit application

Features

- With built-in bias resistors
- Simplify circuit design
- Reduce a quantity of parts and manufacturing process
- High packing density

PIN Connection



Ordering Information

Type NO.	Marking	Package Code	
SRC1204UF	<u>R4</u> <u></u>	SOT-323F	
	1 2		

①Device Code ② Year&Week Code

Absolute Maximum Ratings

(Ta=25°C)

Characteristic	Symbol	Rating	Unit
Output voltage	Vo	50	V
Input voltage	VI	40,-10	V
Output current	Io	100	m A
Power dissipation	P_D	200	m W
Junction temperature	T_J	150	°C
Storage temperature range	T_{stg}	-55 ~ 150	°C

Electrical Characteristics

(Ta=25°C)

Characteristic	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Output cut-off current	I _{O(OFF)}	$V_0 = 50 V, V_1 = 0$	-	-	500	nA
DC current gain	Gı	$V_0 = 5V$, $I_0 = 10 \text{ m A}$	80	200	-	-
Output voltage	$V_{O(ON)}$	$I_{O}=10 \text{ mA}, I_{I}=0.5 \text{ mA}$	-	0.1	0.3	V
Input voltage (ON)	$V_{I(ON)}$	$V_0 = 0.2V$, $I_0 = 5 \text{ m A}$	-	2.8	5.0	V
Input voltage (OFF)	$V_{I(OFF)}$	$V_O = 5V$, $I_O = 0.1 \text{m A}$	1.0	1.2	-	V
Transition frequency	f _T *	$V_0 = 10 \text{ V}, I_0 = 5 \text{ mA}, f = 1 \text{ MHz}$	-	200	-	MHz
Input current	I_1	$V_1 = 5 V$, $I_0 = 0$	-	•	0.18	m A
Input resistor (Input to base)	R ₁	-	33	47	61	K Ω
Input resistor (Base to common)	R ₂	-	33	47	61	K Ω

^{* :} Characteristic of transistor only

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Electrical Characteristic Curves

Fig. 1 I_{O} - $V_{I\left(ON\right)}$

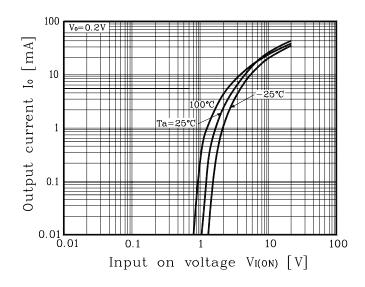


Fig. 2 I_{O} - $V_{I(OFF)}$

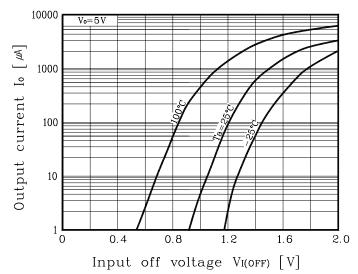
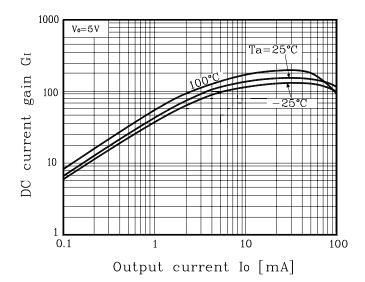
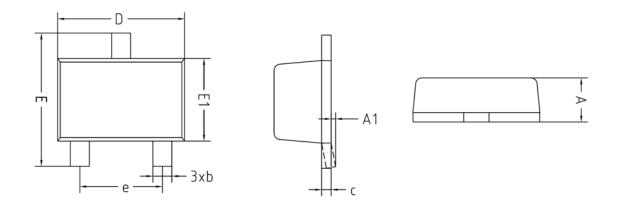


Fig. 3 G_I - I_O



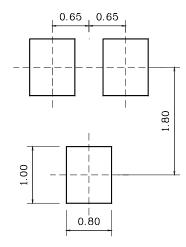
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Outline Dimension



SYMBOL	MILLIMETERS			NOTE
STINDUL	MINIMUM	NOMINAL	MAXIMUM	NOTE
А	0.60	-	0.80	
A1	0.00	-	0.10	
Ь	0.30	-	0.40	
С	0.08	-	0.16	
D	1.90	2.00	2.10	
Е	1.95	2.10	2.25	
E1	1.20	1.30	1.40	
е	1.30BSC			

*Recommend PCB solder land [Unit: mm]



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