

NPN Silicon Transistor

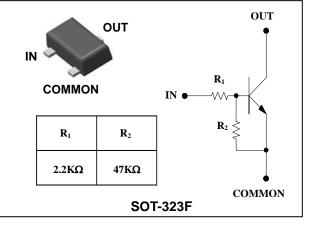
Descriptions

- Switching application
- Interface circuit and driver circuit application

Features

- With built-in bias resistors
- Simplify circuit design
- Reduce a quantity of parts and manufacturing process
- High packing density

PIN Connection



Ordering Information

Type NO.	Marking	Package Code
SRC1205UF	<u>R5</u> ① ②	SOT-323F
	Device Code 2 Year&Week Code	

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Absolute Maximum Ratings

Absolute Maximum Ratings			(Ta=25°C)
Characteristic	Symbol	Rating	Unit
Output voltage	Vo	50	V
Input voltage	VI	15,-5	V
Output current	Ι _ο	100	m A
Power dissipation	P _D	200	mW
Junction temperature	TJ	150	٥C
Storage temperature range	T _{stg}	-55 ~ 150	٥C

Electrical Characteristics

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Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Output cut-off current	I _{O(OFF)}	$V_0 = 50 V, V_1 = 0$	-	-	500	nA
DC current gain	Gı	V _O = 5V, I _O = 10m A	80	200	-	-
Output voltage	V _{O(ON)}	$I_0 = 10 \text{ mA}, I_1 = 0.5 \text{ mA}$	-	0.1	0.3	V
Input voltage (ON)	V _{I(ON)}	$V_0 = 0.2V, I_0 = 5mA$	-	-	1.1	V
Input voltage (OFF)	V _{I(OFF)}	$V_0 = 5V, I_0 = 0.1 \text{ mA}$	0.5	-	-	V
Transition frequency	f _T *	V_0 = 10V, I_0 = 5mA, f= 1MHz	-	200	-	MHz
Input current	I ₁	V ₁ =5V, I ₀ =0	-	-	3.6	mА
Input resistor (Input to base)	R ₁	-	1.54	2.2	2.86	KΩ
Input resistor (Base to common)	R ₂	-	33	47	61	KΩ

* : Characteristic of transistor only

(Ta=25°C)

Electrical Characteristic Curves

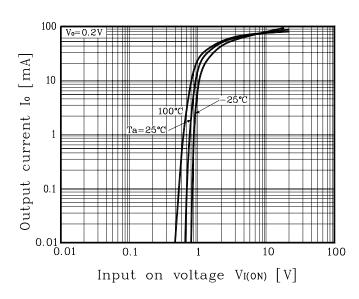


Fig. 1 $I_O - V_{I(ON)}$

Fig. 3 $G_I - I_O$

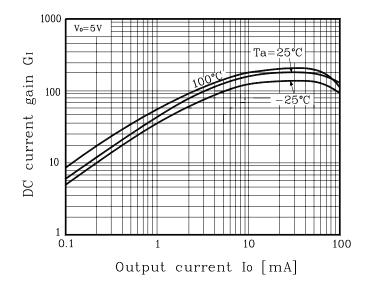
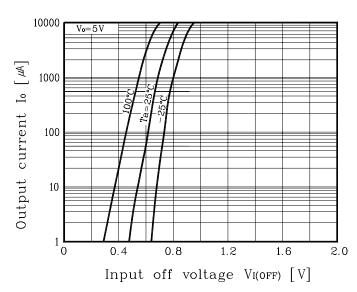
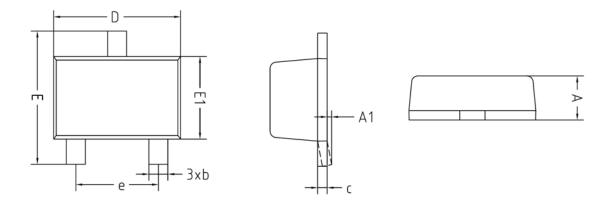


Fig. 2 Io - VI(OFF)

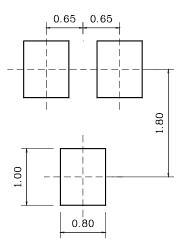


Outline Dimension



SYMBOL	MILLIMETERS			NOTE
STRIBUL	MINIMUM	NOMINAL	MAXIMUM	NUTE
A	0.60	-	0.80	
A1	0.00	-	0.10	
Ь	0.30	-	0.40	
С	0.08	-	0.16	
D	1.90	2.00	2.10	
E	1.95	2.10	2.25	
E1	1.20	1.30	1.40	
e	1.30BSC			

*Recommend PCB solder land [Unit: mm]



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