

NPN Silicon Transistor

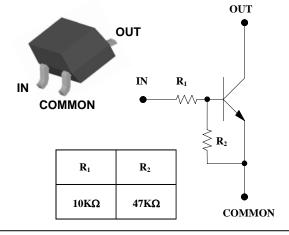
Descriptions

- Switching application
- Interface circuit and driver circuit application

Features

- With built-in bias resistors
- Simplify circuit design
- Reduce a quantity of parts and manufacturing process
- High packing density

PIN Connection



Ordering Information

Type NO.	Marking	Package Code
SRC1207S	<u>RC7</u> □ ① ②	SOT-23
	Device Code 2Vear&Week Co	

Device Code @Year&Week Code

Absolute Maximum Ratings

Absolute Maximum Ratings			(Ta=25°C)
Characteristic	Symbol	Rating	Unit
Out voltage	Vo	50	V
Input voltage	VI	30,-6	V
Out current	Ι _ο	100	m A
Power dissipation	PD	200	mW
Junction temperature	TJ	150	°C
Storage temperature	T _{stg}	-55 ~ 150	°C

Electrical Characteristics

Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Output cut-off current	I _{O(OFF)}	$V_0 = 50 V, V_1 = 0$	-	-	500	nA
DC current gain	Gı	V _O = 5V, I _O = 10m A	80	150	-	-
Output voltage	V _{O(ON)}	$I_0 = 10 \text{ mA}, I_1 = 0.5 \text{ mA}$	-	0.1	0.3	V
Input voltage (ON)	V _{I(ON)}	$V_0 = 0.2V, I_0 = 5mA$	-	-	1.8	V
Input voltage (OFF)	V _{I(OFF)}	$V_0 = 5V, I_0 = 0.1 \text{ mA}$	0.5	-	-	V
Transition frequency	f _T *	V_0 = 10V, I_0 = 5mA, f= 1MHz	-	200	-	MHz
Input current	I ₁	V ₁ =5V, I ₀ =0	-	-	0.88	mA
Input resistor (Input to base)	R ₁	-	7	10	13	KΩ
Input resistor (Base to common)	R ₂	-	33	47	61	KΩ

* : Characteristic of transistor only

(Ta=25°C)

Electrical Characteristic Curves

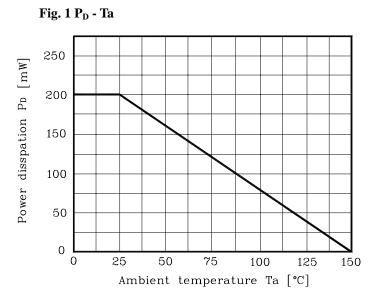


Fig. 3 I_O - V_{I(OFF)}

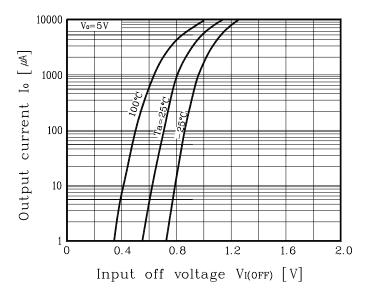


Fig. 2 I_O - V_{I(ON)}

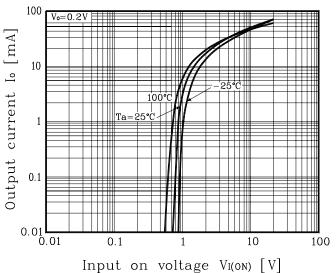
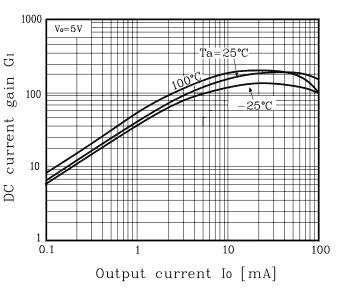
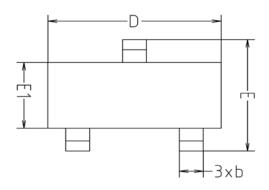
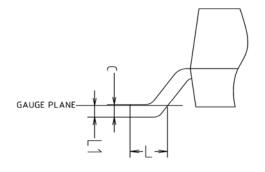


Fig. 4 G_I - I_O

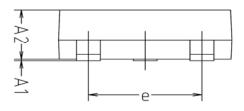


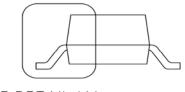
Outline Dimension





DETAIL 'A'

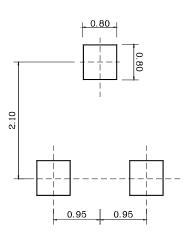




SEE DETAIL 'A'

SYMBOL	MILLIMETERS			NOTE
	MINIMUM	NOMINAL	MAXIMUM	NOTE
A1	0.00	-	0.10	
A2	0.82	-	1.02	
Ь	0.39	0.42	0.45	
с	0.09	0.12	0.15	
D	2.80	2.90	3.00	
E	2.20	2.40	2.60	
E1	1.20	1.30	1.40	
е	1.90BSC			
L	0.20	-	-	
L1	0.12BSC			

*Recommend PCB solder land [Unit: mm]



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