

STC4250F

NPN Silicon Transistor

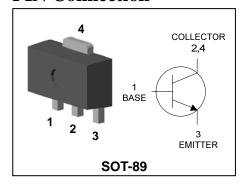
Applications

- Power amplifier application
- High current switching application

Features

- Low saturation voltage: $V_{CE(sat)}$ =0.15V Typ. @ I_C =1A, I_B =50mA
- Large collector current capacity: $I_C=2A$ Small and compact SMD type package
- Complementary pair with STA3250F

PIN Connection



Ordering Information

| Type NO. | Marking | Package Code |
|----------|------------|--------------|
| STC4250F | HW2 YWW | SOT-89 |

HW2: DEVICE CODE, YWW(Y: Year code, WW: Weekly code)

Absolute Maximum Ratings

[Ta=25°C]

| Characteristic | Symbol | Rating | Unit |
|-----------------------------|------------------|---------|------|
| Collector-base voltage | V_{CBO} | 50 | V |
| Collector-emitter voltage | V_{CEO} | 50 | V |
| Emitter-base voltage | V_{EBO} | 5 | V |
| Collector current | I_{C} | 2 | А |
| Base current | I_{B} | 0.4 | А |
| Callantan Dawan diasinatian | P _C | 0.5 | W |
| Collector Power dissipation | P _C * | 1 | W |
| Junction temperature | T ₃ | 150 | °C |
| Storage temperature range | T_{stg} | -55~150 | °C |

[▼] Device mounted on ceramic substrate (250mm² x 0.8t)

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STC4250F

Electrical Characteristics

[Ta=25℃]

| Charac | eteristic | Symbol | Test Condition | Min. | Тур. | Max. | Unit |
|--------------------------------------|-------------------|----------------------|---|------|------|------|------|
| Collector-emitter b | oreakdown voltage | BV _{CEO} | $I_C=10$ mA, $I_B=0$ | 50 | - | - | ٧ |
| Collector cut-off current | | I_{CBO} | V _{CB} =50V, I _E =0 | - | - | 0.1 | μΑ |
| Emitter cut-off current | | I_{EBO} | V_{EB} =5V, I_C =0 | ı | ı | 0.1 | μΑ |
| DC current gain | | h _{FE} | V _{CE} =2V, I _C =0.5A* | 120 | - | 240 | |
| | | h _{FE} | V _{CE} =2V, I _C =1.5A* | 40 | - | - | |
| Collector-emitter saturation voltage | | $V_{\text{CE(sat)}}$ | I _C =1A, I _B =0.05A* | 1 | - | 0.35 | ٧ |
| Base-emitter saturation voltage | | $V_{BE(sat)}$ | I _C =1A, I _B =0.05A* | 1 | - | 1.2 | > |
| Transition frequency | | f_{T} | V _{CE} =2V, I _C =50mA | 1 | 240 | - | MHz |
| Collector output capacitance | | C_ob | V _{CB} =10V, I _E =0, f=1MHz | - | 15 | - | pF |
| Switching Time | Turn-on Time | t _{on} | INPUT Ist OUTPUT | - | 100 | - | |
| | Storage Time | t _{stg} | | - | 300 | - | nS |
| | Fall Time | t _f | lsi=-lex=50mA 30V DUTY CYCLE ⊴% | - | 50 | - | |

^{*:} Pulse test: $t_P \le 300 \mu s$, Duty cycle $\le 2\%$

Electrical Characteristic Curves

Fig. 1 $P_{\rm C}\,$ - T_a

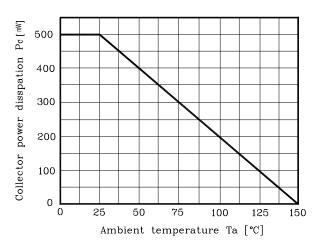


Fig. 3 I_C - V_{CE}

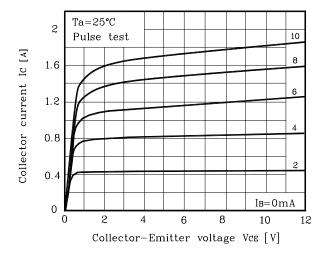


Fig. 5 $V_{\text{CE}(\text{sat})}$ - I_{C}

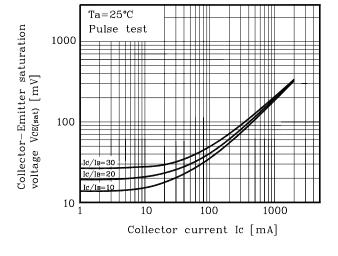


Fig. 2 $I_{C}\;$ - V_{BE}

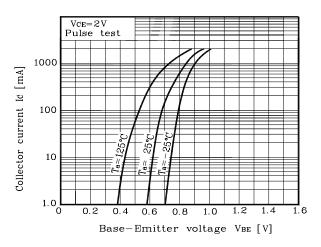


Fig. 4 h_{FE} - I_C

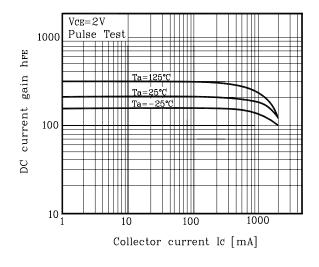
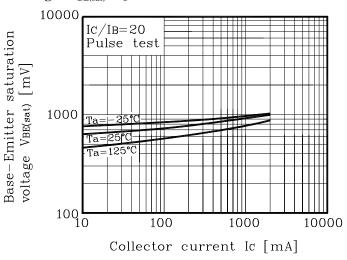


Fig. 6 $V_{BE(sat)}$ - I_{C}



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Electrical Characteristic Curves

Fig. 7 C_{Ob} - V_{CB}

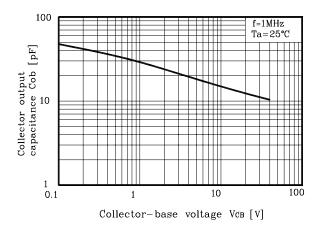
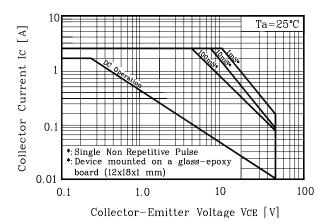
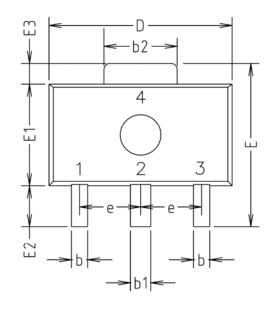
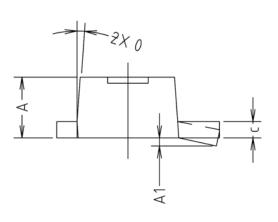


Fig. 8 Safe Operating Area



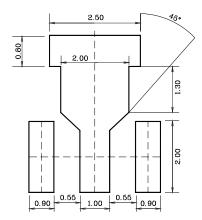
Outline Dimension(mm)





| | MILLIMETERS | | | NOTE |
|--------|-------------|-----------|---------|-------|
| SYMBOL | MINIMUM | NOMINAL | MAXIMUM | INOTE |
| Α | 1.40 | 1.50 | 1.60 | |
| A1 | 0.00 | _ | 0.10 | |
| b | 0.38 | 0.42 | 0.48 | |
| b1 | 0.48 | 0.52 | 0.58 | |
| b2 | 1.79 | 1.82 | 1.87 | |
| С | 0.40 | 0.42 | 0.46 | |
| D | 4.40 | 4.50 | 4.70 | |
| Ε | 3.70 | 4.00 | 4.30 | |
| E1 | 2.40 | 2.50 | 2.70 | |
| E2 | 0.80 | 1.00 | 1.20 | |
| E3 | 0.40 | 0.50 | 0.60 | |
| е | | 1.50 TYP. | | |
| 0 | | 4° TYP. | | |

*Recommend PCB solder land [Unit: mm]



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