

STD1664

NPN Silicon Transistor

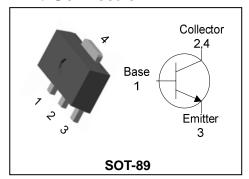
Description

• Medium power amplifier application

Features

- P_C(Collector power dissipation) = 1W (Ceramic substate of 250 mm × 0.8t used)
- Low collector saturation voltage : $V_{CE(sat)} = 0.15V(Typ.)$
- Complementary pair with STB1132

PIN Connection



Ordering Information

Type NO.	Marking	Package Code
STD1664	A2 □YWW	SOT-89
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A2: DEVICE CODE, \square : h_{FE} rank, YWW(Y: Year code, WW: Weekly code)

Absolute maximum ratings

(Ta=25°C)

Characteristic	Symbol	Ratings	Unit	
Collector-Base voltage	V_{CBO}	40	V	
Collector-Emitter voltage	$V_{\sf CEO}$	32	V	
Emitter-Base voltage	V_{EBO}	5	V	
Collector current	I _C	1	A(DC)	
Collector current	I _{CP} *	2	A(Pulse)	
Callector newer dissination	Pc	0.5	W	
Collector power dissipation	P _C [*]	1	VV	
Junction temperature	TJ	150	°C	
Storage temperature	T_{stg}	-55~150	°C	

^{*:} Single pulse, tp= 300 μ s

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^{**:} When mounted on ceramic substrate(250 mm²×0.8t)

Electrical Characteristics

 $(Ta=25^{\circ}C)$

Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Collector-Base breakdown voltage	BV _{CBO}	Ι _C = 50 μΑ, Ι _Ε = 0	40	-	ı	V
Collector-Emitter breakdown voltage	BV _{CEO}	I _C = 1 MA, I _B = 0	32	1	ı	V
Emitter-Base breakdown voltage	BV _{EBO}	I _E = 50 μA, I _C = 0	5	1	ı	V
Collector cut-off current	I _{CBO}	V _{CB} = 20V, I _E = 0	1	1	0.5	μA
Emitter cut-off current	I _{EBO}	V _{EB} = 4V, I _C = 0	-	1	0.5	μA
DC current gain	h _{FE} *	V _{CE} = 3V, I _C = 0.1A	100	1	320	ı
Collector-Emitter saturation voltage	V _{CE(sat)}	$I_C = 500 \text{ MA}, I_B = 50 \text{ MA}$	-	0.15	0.4	V
Transition frequency	f _T	V_{CE} = 5V, I_{C} = 50 mA	-	150	-	MHz
Collector output capacitance	C _{ob}	V_{CB} = 10 V, I_E = 0, f = 1 MHz	-	15	-	pF

^{* :} h_{FE} rank / O : 100 ~ 200, Y : 160 ~ 320

Electrical Characteristic Curves

Fig. $1 P_C - T_a$

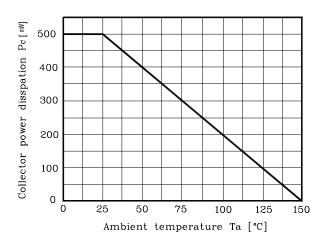


Fig. 2 $I_{\rm C}~$ - V_{BE}

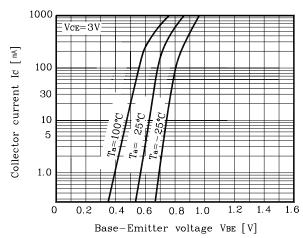


Fig. 3 $I_{C}\;$ - V_{CE}

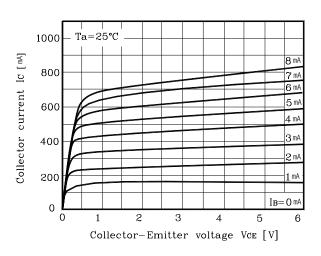


Fig. 4 $V_{CE(sat)}\,$ - $\,I_{C}\,$

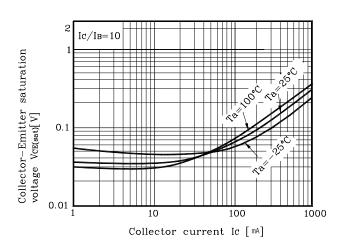
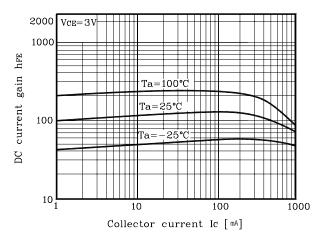
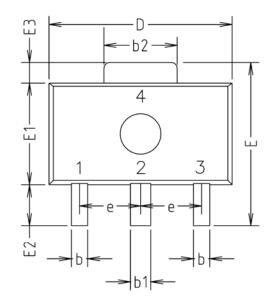


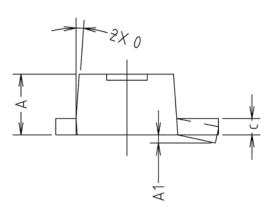
Fig. 5 $h_{FE}\,\,$ - $\,\,$ I_{C}



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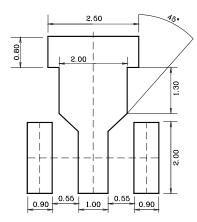
Outline Dimension(mm)





	MILLIMETERS			NOTE
SYMBOL	MINIMUM	NOMINAL	MAXIMUM	INOTE
Α	1.40	1.50	1.60	
A1	0.00	_	0.10	
b	0.38	0.42	0.48	
b1	0.48	0.52	0.58	
b2	1.79	1.82	1.87	
С	0.40	0.42	0.46	
D	4.40	4.50	4.70	
E	3.70	4.00	4.30	
E1	2.40	2.50	2.70	
E2	0.80	1.00	1.20	
E3	0.40	0.50	0.60	
е		1.50 TYP.		
0		4° TYP.		

*Recommend PCB solder land [Unit: mm]



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