

STK7002

Advanced N-Channel MOSFET

HIGH SPEED SWITCHING APPLICATIONS

Features

• Low Gate Threshold Voltage

• Low C_{rss} : $C_{rss}=2.0pF(Typ.)$

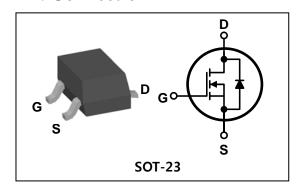
• Voltage controlled small signal switch

• Low $R_{DS(on)}$: $R_{DS(on)} = 5\Omega(Max.)$

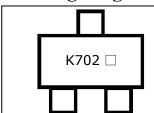
Ordering Information

Type No.	Marking	Package Code
STK7002	<u>K702</u> <u>□</u> ① ②	SOT-23

PIN Connection



Marking Diagram



K702: Specific Device Code

☐ : year & week Code Marking

Absolute maximum ratings (T_A=25°C unless otherwise noted)

Characteristic	Symbol	Rating	Unit	
Drain-source voltage	V_{DSS}	60	V	
Gate-source voltage	V_{GSS}	±20	V	
Drain current (DC) *	I_{D}	115	mA	
Drain current (Pulsed) *	${ m I}_{\sf DM}$	800	mA	
Junction temperature	$T_{\mathtt{J}}$	150	°C	
Storage temperature range	T_{stg}	-55~150	٠	

^{*} Limited by maximum junction temperature

Thermal Characteristics

Characteristic	Symbol	Rating	Unit
Power dissipation	P_{D}	350	mW
Thermal resistance, Junction-Ambient *	$R_{th(J-A)}$	357	°C/W

^{*} Device mounted on FR-4 PCB, 99.5% Alumina 10 x 8 x 0.6mm. Minimum land pad size

KSD-T5C041-005

STK7002

$\underline{\pmb{Electrical\ Characteristics}\ (T_A=25^{\circ}C\ unless\ otherwise\ noted)}$

Characteristic	Symbol	Symbol Test Condition			Max.	Unit	
Characteristic Symbol Test Condition Min. Typ. Max. Unit Off Characteristics (Note1)							
Drain-source breakdown voltage	n-source breakdown voltage BV_{DSS} $I_D=10uA$, $V_{GS}=0$		60	-	-	V	
Drain-source cut-off current	I_{DSS}	V _{DS} =60V, V _{GS} =0	-	-	1.0	uA	
		V _{DS} =60V, V _{GS} =0, @T _C =125°C	-	-	200		
Gate leakage current	I_{GSS}	I_{GSS} V_{DS} =0V, V_{GS} =±20V		-	±100	nA	
On Characteristics (Note1)	On Characteristics (Note1)						
Gate threshold voltage	threshold voltage $V_{GS(th)}$ $I_D=250uA, V_{DS}=V_{GS}$		1.0	2.0	2.5	V	
Drain-source on-resistance	R _{DS(ON)}	V _{GS} =10V, I _D =500mA	-	2.4	5.0		
		V_{GS} =5V, I_D =50mA	-	3.2	5.0	Ω	
Forward transfer conductance	g _{fs}	V _{DS} =10V, I _D =100mA	80	-	-	mS	
Dynamic Characteristics							
Input capacitance	Ciss		-	22	-		
Output capacitance	Coss	V_{GS} =0V, V_{DS} =25V, f=1MHz	-	11	-	pF	
Reverse transfer capacitance	Crss		-	2	-		
Switching Characteristics							
Turn-on delay time	t _{D(ON)}	V _{DD} =30V, I _D =100mA	-	7	-	no	
Turn-off delay time	t _{D(OFF)}	V_{GS} =10V, R_{G} =25 Ω	-	11	-	ns	

Note1: Short duration test pulse used to minimize self-heating effect.

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Electrical Characteristic Curves

Fig. 1 I_D - V_{DS}

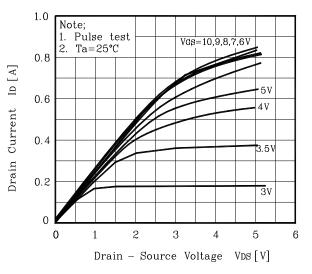


Fig. 3 $R_{DS(on)}$ - I_D

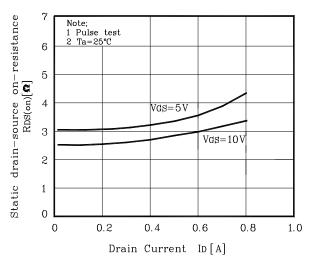


Fig. 5 Capacitance - V_{DS}

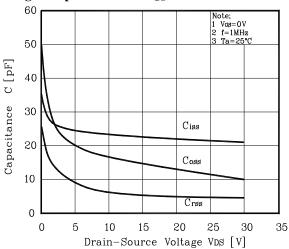


Fig. 2 I_D - V_{GS}

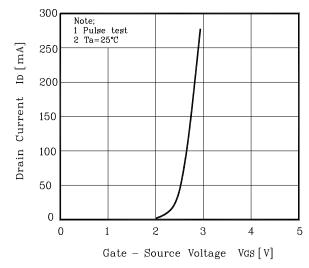


Fig. 4 $R_{DS(on)}$ - V_{GS}

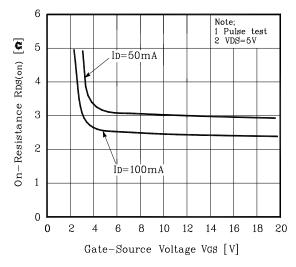
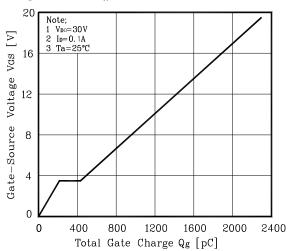


Fig. 6 V_{GS} - Q_g



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Electrical Characteristic Curves

Fig. 7 $I_{S}\,$ - $\,V_{SD}\,$

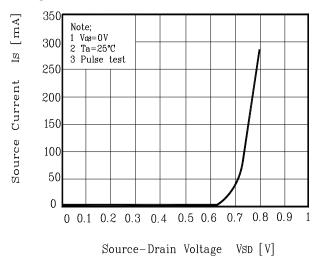
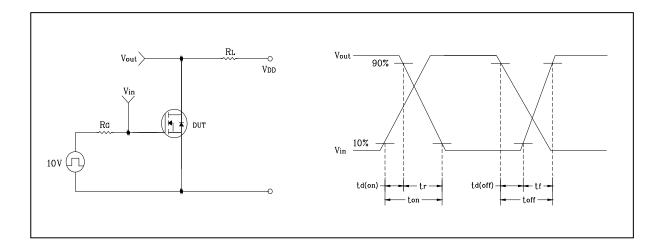
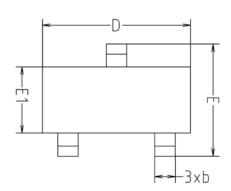
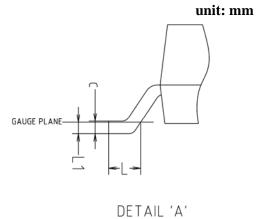


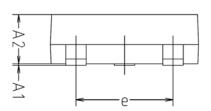
Fig. 8 Resistive Switching Test Circuit & Waveform

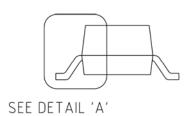


Outline Dimension



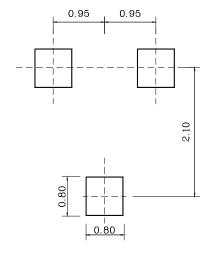






SYMBOL		MILLIMETERS			NOTE
		MINIMUM	NOMINAL	MAXIMUM	NOTE
A1		0.00	-	0.10	
A2		0.82	-	1.02	
Ь		0.39	0.42	0.45	
С		0.09	0.12	0.15	
D		2.80	2.90	3.00	
Е		2.20	2.40	2.60	
E1		1.20	1.30	1.40	
е		1.90BSC			
L		0.20	-	-	
L1		0.12BSC			

**** Recommended Land Pattern** [unit: mm]



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