

SUT562EF

Epitaxial planar NPN silicon transistor

Description

• Complex type bipolar transistor

Feature

- Very small package save PCB area
- Reduce quantity of parts and mounting cost
- Two 2SC5343 chips in SOT-563F package

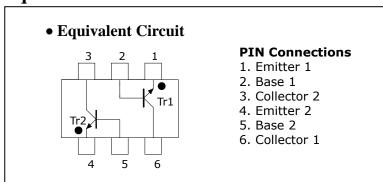
Package : SOT-563F

Ordering Information

Type NO.	Marking	Package Code
SUT562EF	XX□	SOT-563F

□ : Year & Week Code

Equivalent circuit & PIN Connections



Absolute Maximum Ratings [Tr1, Tr2]

 $(Ta=25^{\circ}C)$

Characteristic	Symbol	Rating	Unit
Collector-base voltage	V_{CBO}	60	V
Collector-emitter voltage	V_{CEO}	50	V
Emitter-base voltage	V_{EBO}	5	V
Collector current	I_{C}	150	mA
Collector power dissipation	P _C **	150	mW
Junction temperature	T _J	150	°C
Storage temperature range	T _{stg}	-55~150	°C

*: Total rating

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SUT562EF

Electrical Characteristics [Tr1, Tr2]

(Ta=25°C)

Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Collector-emitter breakdown voltage	BV _{CEO}	I _C =1mA, I _B =0	50	-	-	V
Collector cut-off current	I_{CBO}	V _{CB} =60V, I _E =0	-	-	0.1	μА
Emitter cut-off current	I _{EBO}	V _{EB} =5V, I _C =0	-	-	0.1	μА
DC current gain	h _{FE}	V _{CE} =6V, I _C =2mA	120	-	400	-
Collector-emitter saturation voltage	$V_{\text{CE(sat)}}$	I _C =100mA, I _B =10mA	-	-	0.25	V
Base-emitter voltage	V_{BE}	V _{CE} =6V, I _C =2mA	-	0.65	-	V
Transition frequency	f⊤	V _{CE} =10V, I _C =10mA	-	200	-	MHz
Collector output capacitance	C _{ob}	V _{CB} =10V, I _E =0, f=1MHz		2	-	pF

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Electrical Characteristic Curves

[Tr1, Tr2]

Fig. 1 I_C - V_{BE}

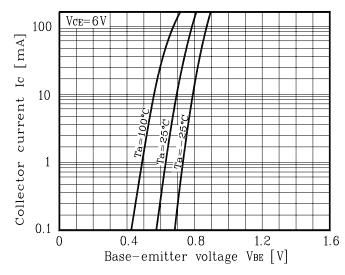


Fig. 2 I_C - V_{CE}

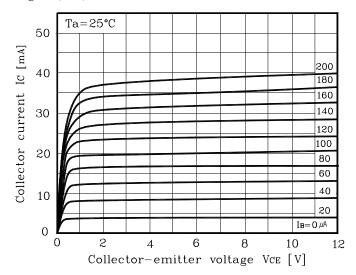


Fig. 3 h_{FE} - I_{C}

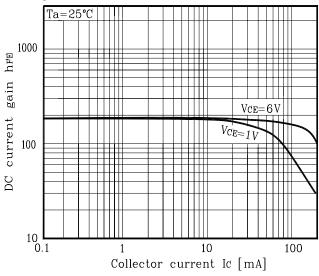


Fig. 4 $V_{CE(sat)}$ - I_{C}

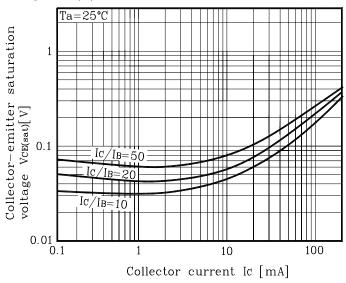
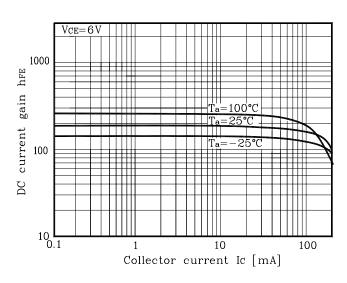
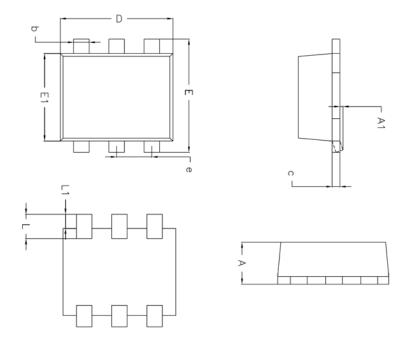


Fig. 6 h_{FE} - I_C

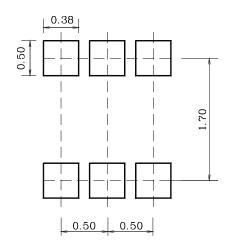


Outline Dimension



NOTE	
NULL	
INOIE	
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* Recommend PCB solder land [Unit: mm]



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