

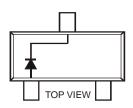
1A SURFACE MOUNT SCHOTTKY BARRIER RECTIFIER

Features

- Very Low Forward Voltage Drop
- High Conductance
- For Use in DC-DC Converter, PCMCIA, and Mobile Telecommunications Applications
- Lead, Halogen and Antimony Free, RoHS Compliant "Green" Device (Notes 1 and 2)
- Qualified to AEC-Q101 Standards for High Reliability

Mechanical Data

- Case: SOT-23
- Case Material: Molded Plastic. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Matte Tin Finish annealed over Alloy 42 leadframe (Lead Free Plating). Solderable per MIL-STD-202, Method 208
- Polarity: See Diagram
- Weight: 0.008 grams (approximate)



Top View

Schematic and Pin Configuration

Ordering Information (Note 3)

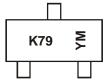
| Part Number | Case | Packaging |
|-------------|--------|------------------|
| BAT1000-7-F | SOT-23 | 3000/Tape & Reel |

Notes: 1. No purposefully added lead. Halogen and Antimony Free.

 Product manufactured with Data Code V9 (week 33, 2008) and newer are built with Green Molding Compound. Product manufactured prior to Date Code V9 are built with Non-Green Molding Compound and may contain Halogens or Sb₂O₃ Fire Retardants.

3. For packaging details, go to our website at http://www.diodes.com.

Marking Information



K79 = Product Type Marking Code YM = Date Code Marking Y = Year (ex: N = 2002) M = Month (ex: 9 = September)

| Date Code Key | |
|---------------|--|
|---------------|--|

| Year | 2002 | 2003 | 2004 | 200 | 5 20 | 06 2 | 007 | 2008 | 8 | 2009 | 2010 | 2011 | 2012 |
|-------|------|------|------|-----|------|------|-----|------|-----|------|------|------|------|
| Code | N | Р | R | S | Т | | U | V | | W | Х | Y | Z |
| Month | Jan | Feb | Mar | Apr | Мау | Jun | Jı | ıl | Aug | Sep | Oct | Nov | Dec |
| Code | 1 | 2 | 3 | 4 | 5 | 6 | 7 | , | 8 | 9 | 0 | Ν | D |



Maximum Ratings $@T_A = 25$ °C unless otherwise specified

| Characteristic | Symbol | Value | Unit |
|---|--|-------|------|
| Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage | V _{RRM} V _{RWM} V _R | 40 | V |
| RMS Reverse Voltage | V _{R(RMS)} | 28 | V |
| Average Rectified Current | lo | 1.0 | A |
| Non-Repetitive Peak Forward Surge Current 8.3ms Single half sine-wave superimposed on rated load | I _{FSM} | 5.5 | A |

Thermal Characteristics

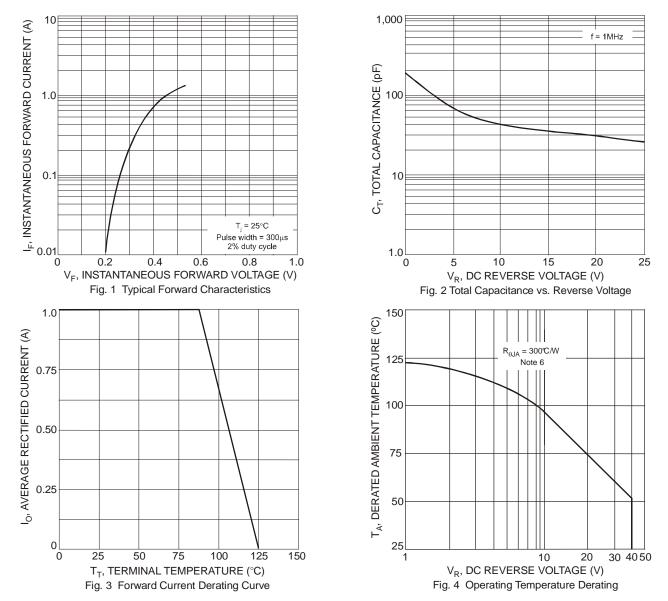
| Characteristic | Symbol | Value | Unit |
|---|-------------------|-------------|------|
| Power Dissipation (Note 4) | PD | 500 | mW |
| Typical Thermal Resistance, Junction to Ambient Air (Note 4) | R ₀ JA | 200 | °C/W |
| Operating Temperature Range | TJ | -40 to +125 | ۵° |
| Storage Temperature Range | T _{STG} | -40 to +150 | °C |

Electrical Characteristics @T_A = 25°C unless otherwise specified

| Characteristic | Symbol | Min | Тур | Max | Unit | Test Condition |
|------------------------------------|--------------------|-----|---|---|----------|---|
| Reverse Breakdown Voltage (Note 5) | V _{(BR)R} | 40 | _ | _ | V | I _R = 300uA |
| Forward Voltage | VF | | 225 235 290 340 390 420 475 | 270 290 340 400 450 500 600 | mV | IF = 50MA IF = 100MA IF = 250MA IF = 500MA IF = 750MA IF = 1000MA IF = 1500MA |
| Reverse Current (Note 5) | I _R | | _ | 100 | μΑ | V _R = 30V |
| Total Capacitance | CT | | 175 25 | | pF pF | $V_R = 0V, f = 1.0MHz$ $V_R = 25V, f = 1.0MHz$ |

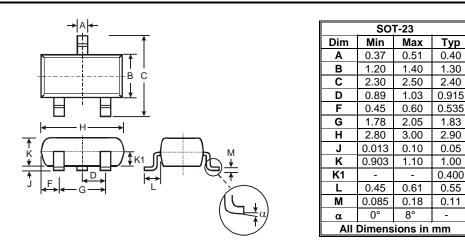
4. Part mounted on FR-4 board with recommended pad layout, which can be found on our website at http://www.diodes.com.5. Short duration pulse test used to minimize self-heating effect. Notes:







Package Outline Dimensions



Тур

0.40

1.30

2.40

1.83 2.90

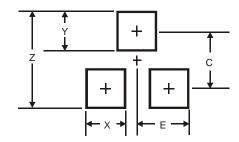
0.05

1.00

0.11



Suggested Pad Layout



| Dimensions | Value (in mm) |
|------------|---------------|
| Z | 2.9 |
| Х | 0.8 |
| Y | 0.9 |
| С | 2.0 |
| E | 1.35 |

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