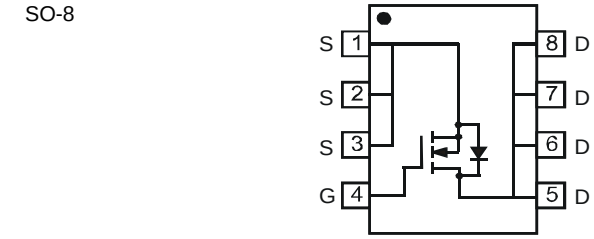
Features

- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- **Lead Free By Design/RoHS Compliant (Note 1)**
- **"Green" Device (Note 2)**
- **Qualified to AEC-Q101 Standards for High Reliability**

Mechanical Data

- Case: SO-8
- Case Material: Molded Plastic, “Green” Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram Below
- Weight: 0.072 grams (approximate)

SO-8



Top View

Top View
Internal Schematic

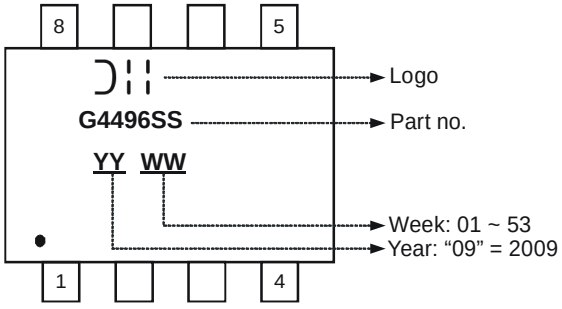
Ordering Information (Note 3)

Part Number	Qualification	Case	Packaging
DMG4496SSS-13	Commercial	SO-8	2500 / Tape & Reel
DMG4496SSSQ-13	Automotive	SO-8	2500 / Tape & Reel

Notes: 1. EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. No purposely added lead. Halogen and Antimony free.
2. Diodes Inc.'s "Green" policy can be found on our website at http://www.diodes.com/products/lead_free/index.php.
3. For packaging details, go to our website at http://www.diodes.com.

Marking Information

Top View



Maximum Ratings @T_A = 25℃ unless otherwise specified

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			V_{DSS}	30	V
Gate-Source Voltage			V_{GSS}	±25	V
Continuous Drain Current (Note 4)	Steady State	T_A = 25℃ T_A = 85℃	I_D	10 6	A
Pulsed Drain Current (Note 5)			I_{DM}	60	A
Avalanche Current (Notes 5 & 6)			I_{AR}	8	A
Repetitive Avalanche Energy (Notes 5 & 6) L = 0.1mH			E_{AR}	3.2	mJ

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 4)	P_D	1.42	W
Thermal Resistance, Junction to Ambient @T_A = 25℃ (Note 4)	$R_{\theta JA}$	88.49	℃/W
Operating and Storage Temperature Range	T_J, T_{STG}	-55 to +150	℃

Electrical Characteristics @T_A = 25℃ unless otherwise specified

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV_{DSS}	30	-	-	V	V_{GS} = 0V, I_D = 250μA
Zero Gate Voltage Drain Current	I_{DSS}	-	-	1	μA	V_{DS} = 30V, V_{GS} = 0V
Gate-Source Leakage	I_{GSS}	-	-	±100	nA	V_{GS} = ±25V, V_{DS} = 0V
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	$V_{GS(th)}$	0.8	1.2	2.0	V	V_{DS} = V_{GS}, I_D = 250μA
Static Drain-Source On-Resistance	$R_{DS\ (ON)}$	-	16 22	21.5 29	mΩ	V_{GS} = 10V, I_D = 10A V_{GS} = 4.5V, I_D = 7.5A
Forward Transfer Admittance	$\|Y_{fs}\|$	-	11.7	-	S	V_{DS} = 5V, I_D = 10A
Diode Forward Voltage	V_{SD}	-	0.70	1	V	V_{GS} = 0V, I_S = 1A
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	C_{iss}	-	493.5	-	pF	V_{DS} =15V, V_{GS} = 0V, f = 1.0MHz
Output Capacitance	C_{oss}	-	94.5	-	pF	
Reverse Transfer Capacitance	C_{rss}	-	50.4	-	pF	
Gate Resistance	R_g	-	2.86	-	Ω	V_{DS} =0V, V_{GS} = 0V, f = 1MHz
Total Gate Charge (V_{GS} = 4.5V)	Q_g	-	4.7	-	nC	V_{DS} = 15V, V_{GS} = 4.5V, ID =10A
Total Gate Charge (V_{GS} = 10V)	Q_g	-	10.2	-		V_{DS} = 15V, V_{GS} = 10V, ID =10A
Gate-Source Charge	Q_{gs}	-	1.4	-	nC	
Gate-Drain Charge	Q_{gd}	-	1.7	-	nC	
Turn-On Delay Time	$t_{D(on)}$	-	4.76	-	ns	V_{GS} = 10V, V_{Ds} = 15V, R_G = 6Ω, R_L = 15Ω,
Turn-On Rise Time	t_r	-	3.64	-	ns	
Turn-Off Delay Time	$t_{D(off)}$	-	19.5	-	ns	
Turn-Off Fall Time	t_f	-	4.9	-	ns	

Notes:
4. Device mounted on 1 in.^{2} FR-4 board with 2oz. Copper, in a still air environment @ T_A = 25℃. The value in any given application depends on the user's specific board design.
5. Repetitive rating, pulse width limited by junction temperature.
6. I_{AR} and E_{AR} rating are based on low frequency and duty cycles to keep T_J = 25℃
7. Short duration pulse test used to minimize self-heating effect.
8. Guaranteed by design. Not subject to production testing.

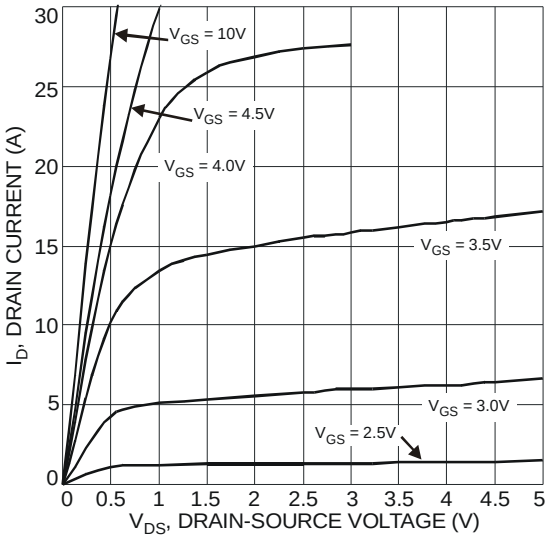


Fig. 1 Typical Output Characteristic

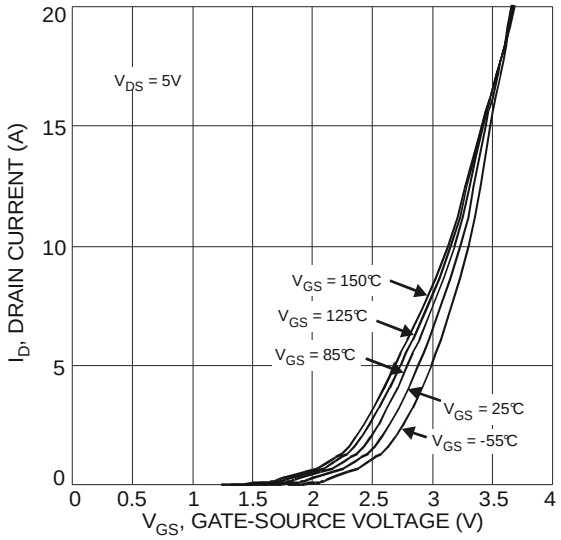


Fig. 2 Typical Transfer Characteristic

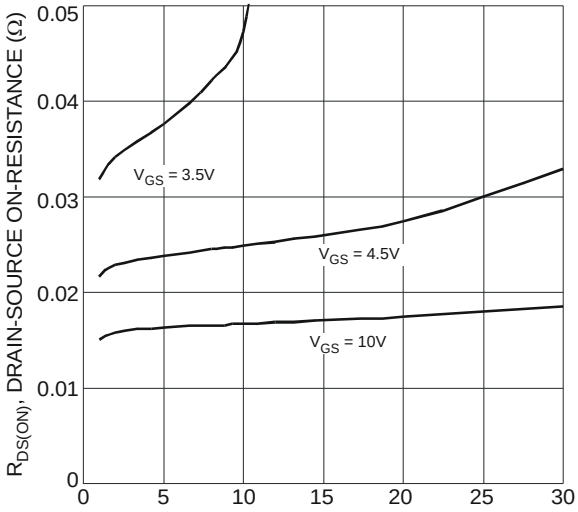


Fig. 3 Typical On-Resistance
vs. Drain Current and Gate Voltage

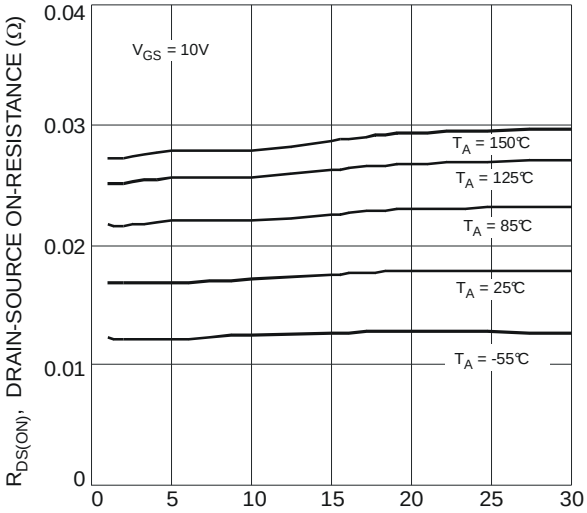


Fig. 4 Typical On-Resistance
vs. Drain Current and Temperature

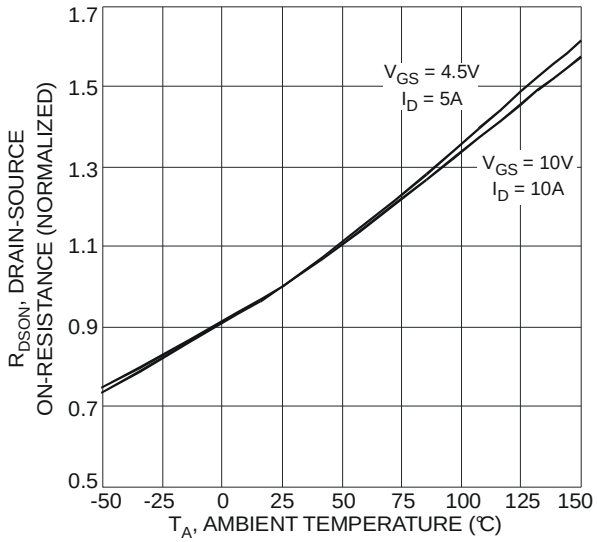


Fig. 5 On-Resistance Variation with Temperature

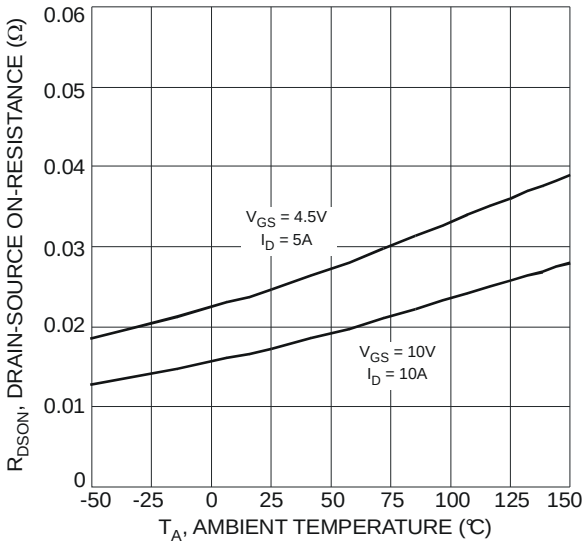


Fig. 6 On-Resistance Variation with Temperature

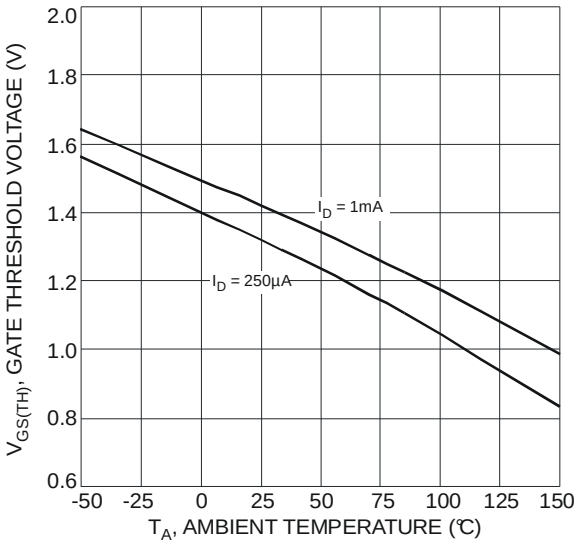


Fig. 7 Gate Threshold Variation vs. Ambient Temperature

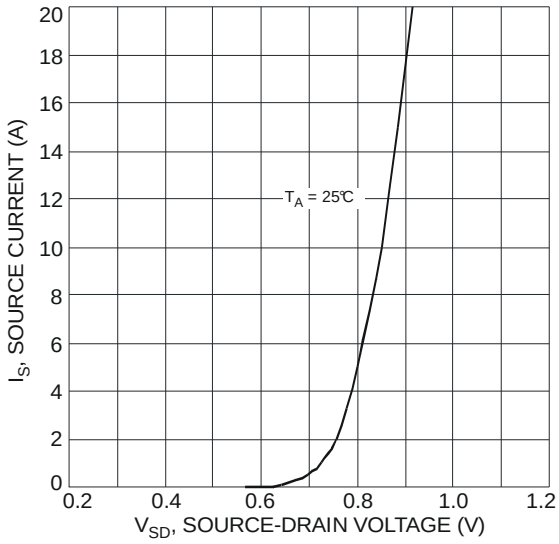


Fig. 8 Diode Forward Voltage vs. Current

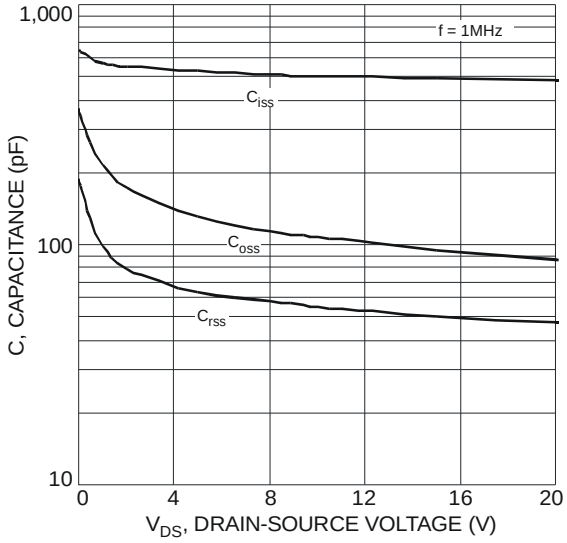


Fig. 9 Typical Total Capacitance

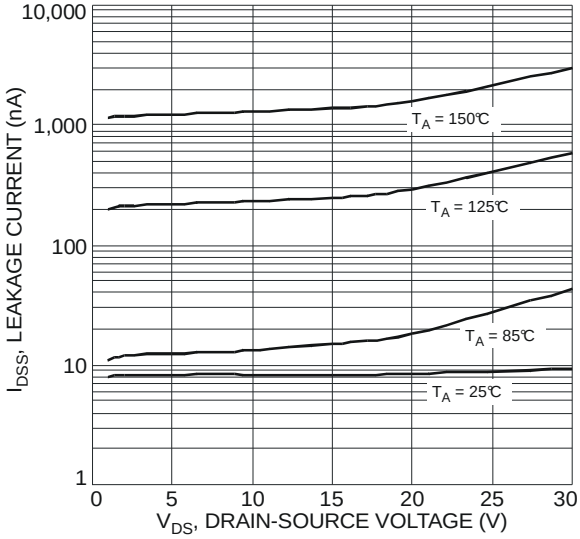


Fig. 10 Typical Leakage Current vs. Drain-Source Voltage

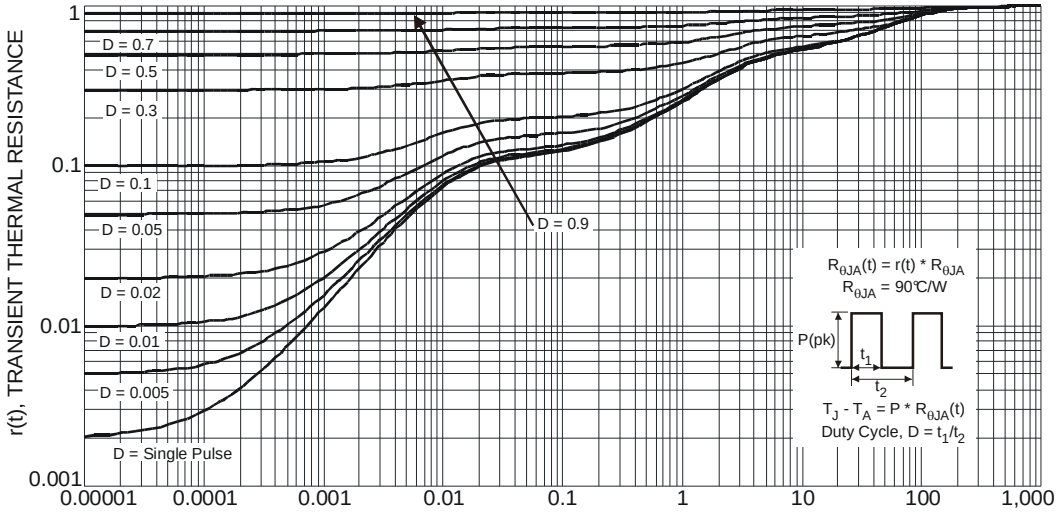
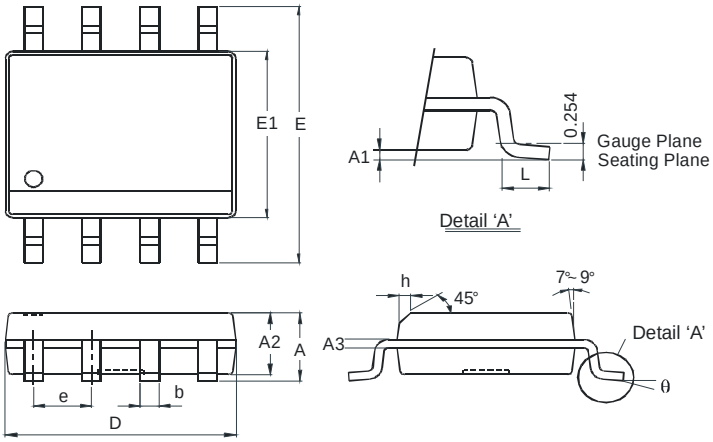


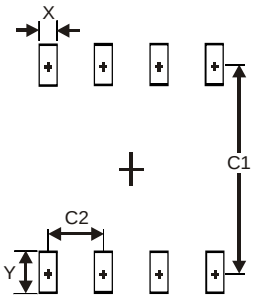
Fig. 11 Transient Thermal Response

Package Outline Dimensions



SO-8		
Dim	**Min**	**Max**
A	-	1.75
A1	0.10	0.20
A2	1.30	1.50
A3	0.15	0.25
b	0.3	0.5
D	4.85	4.95
E	5.90	6.10
E1	3.85	3.95
e	1.27 Typ	
h	-	0.35
L	0.62	0.82
θ	0°	8°
All Dimensions in mm		

Suggested Pad Layout



Dimensions	Value (in mm)
X	0.60
Y	1.55
C1	5.4
C2	1.27

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