

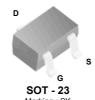
May 2010

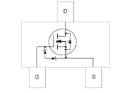
BSS138K

N-Channel Logic Level Enhancement Mode Field Effect Transistor

Features

- · Low On-Resistance
- · Low Gate Threshold Voltage
- · Low Input Capacitance
- · Fast Switching Speed
- · Low Input/Output Leakage
- · Ultra-Small Surface Mount Package
- · Pb Free/RoHS Compliant
- · Green Compound
- ESD HBM=2000V as per JEDEC A114A; ESD CDM = 2000V as per JEDEC C101C





Absolute Maximum Ratings * T_A = 25°C unless otherwise noted

Symbol	Parameter		Value	Units	
V _{DSS}	Drain-Source Voltage		50	V	
V _{GSS}	Gate-Source Voltage		±12	V	
I _D	Drain Current	Continuous Pulsed	0.22 0.88	А	
T _J	Operating Junction Temperature Range		-55 to +150	°C	
T _{STG}	Storage Temperature Range		-55 to +150	°C	

^{*} These ratings are limiting values above which the serviceability of any semiconductor device maybe impaired.

Thermal Characteristics

Symbol	Parameter	Value	Units	
P _D	Total Device Dissipation Derating above T _A = 25°C	350 2.8	mW mW/°C	
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient *	350	°C/W	

^{*} Device mounted on FR-4 PCB, 1 inch x 0.85 inch x 0.062 inch. Minimum land pad size

Electrical Characteristics $T_A = 25^{\circ}C$ unless otherwise noted

Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Units
Off Chara	cteristics			•		
BV _{DSS}	Drain-Source Breakdown Voltage	V_{GS} = 0V, I_D =10 μ A	50			V
BV _{DSS}	Breakdown Voltage Temperature Coefficient	I _D =250μA, Referenced to 25°C		0.11		V/°C
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 50V, V _{GS} = 0V			0.1	μА
I _{GSS}	Gate-Body Leakage	V_{GS} = ±12V, V_{DS} = 0V V_{GS} = ±10V, V_{DS} = 0V V_{GS} = ±5V, V_{DS} = 0V			±1 ±0.5 ±0.05	μА
On Chara	cteristics				•	
V _{GS(th)}	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	0.6		1.2	V
V _{GS(th)}	Gate Threshold Voltage Temperature Coefficient	I _D = 1mA, Referenced to 25°C		-1.4		mV/°C
R _{DS(ON)}	Static Drain-Source On-Resistance	$V_{GS} = 1.8V, I_D = 50mA,$ $V_{GS} = 2.5V, I_D = 50mA,$ $V_{GS} = 5V, I_D = 50mA,$			2.5 2.0 1.6	Ω
I _{D(ON)}	On-State Drain Current	V _{GS} = 10V, V _{DS} = 5V	0.2			Α
9 _{FS}	Forward Transconductance	V _{DS} = 10V, I _D = 200mA	200			mS
Dynamic	Characteristics				•	
C _{iss}	Input Capacitance			58		
C _{oss}	Output Capacitance	$V_{DS} = 25V, V_{GS} = 0V, f = 1.0MHz$		9.75		pF
C _{rss}	Reverse Transfer Capacitance			5.2		
R_{G}	Gate Resistance	V _{DS} = 5V, V _{GS} = 10mV		281		Ω
Switching	Characteristics					
t _{D(ON)}	Turn-On Delay Time				5	
t _r	Turn-On Rise Time	$V_{DD} = 30V, I_D = 0.29A,$			5	- ns
t _{D(OFF)}	Turn-Off Delay Time	V_{GS} = 10V, R_{GEN} = 6Ω			60	
t _f	Turn-Off Fall Time				35	
Qg	Total Gate Change				2.4	
Q _{gs}	Gate-Source Change	$V_{DS} = 25V, I_{D} = 0.2A,$ $V_{GS} = 10V, I_{G} = 0.1mA$			0.5	nC
Q _{gd}	Gate-Drain Change	1.03 .4.1, .0 4			0.5	
Drain-Sou	irce Diode Characteristics and	d Maximum Ratings				
V _{sd}	Drain-Source Diode Forward Voltage	V _{GS} = 0V, I _S = 115mA			1.2	V

Typical Performance Characteristics

Figure 1. On-Region Characteristics

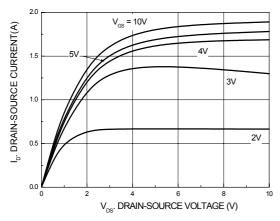


Figure 3. On-Resistance Variation with

Temperature

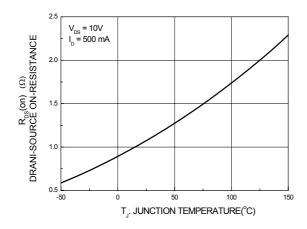


Figure 5. Transfer Characteristics

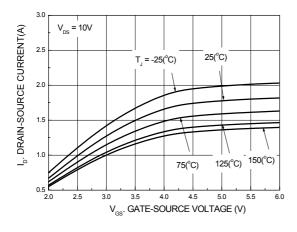


Figure 2. On-Resistance Variation with Gate Voltage and Drain Current

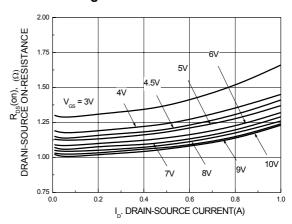


Figure 4. On-Resistance Variation with Gate-Source Voltage

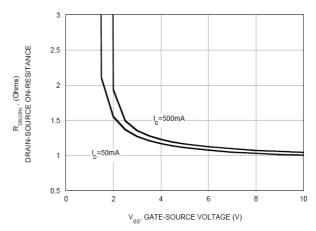
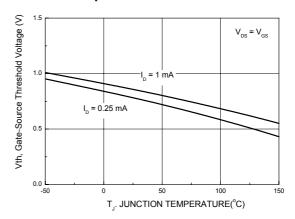
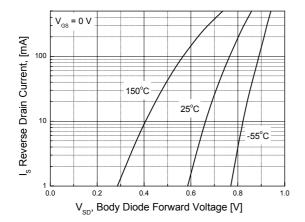


Figure 6. Gate Threshold Variation with Temperature



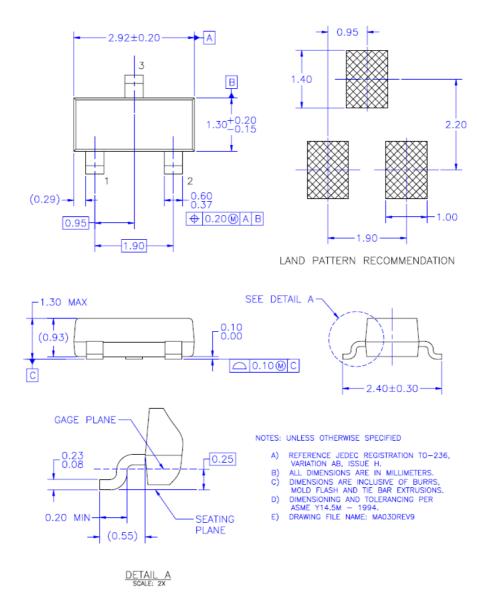
Typical Performance Characteristics (Continue)

Figure 7. Reverse Drain Current Variation with Diode Forward Voltage and Temperature



Physical Dimensions

SOT - 23







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Definition of Terms			
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