

June 2007

# BZX55C2V4 - BZX55C56 Zener Diodes

Tolerance = 5%



DO-35 Glass case
COLOR BAND DENOTES CATHODE

# Absolute Maximum Ratings \* Ta = 25°C unless otherwise noted

Symbol	Parameter	Value	Units
P <sub>D</sub>	Power Dissipation @ TL ≤ 75°C, Lead Length = 3/8"	500	mW
	Derate above 75°C	4.0	mW/°C
T <sub>J</sub> , T <sub>STG</sub>	Operating and Storage Temperature Range	-65 to +200	°C

 $<sup>^{\</sup>star}$  These ratings are limiting values above which the serviceability of the diode may be impaired.

# $\textbf{Electrical Characteristics} \quad \textbf{T}_{a} = 25 \text{ C unless otherwise noted}$

Device	V <sub>Z</sub> (V) @ I <sub>Z</sub> (Note 1)		Z <sub>Z</sub> @ I <sub>Z</sub> Test Current		I <sub>R</sub> (μA) @ V <sub>R</sub>			I <sub>ZM</sub> (mA)
Device	Min.	Max.	$(\Omega)$	I <sub>Z</sub> (mA)	T <sub>a</sub> = 25°C	T <sub>a</sub> = 125°C	<b>V</b> <sub>R</sub> (V)	(Note 2)
BZX55C2V4	2.28	2.56	85	5	50	100	1	155
BZX55C2V7	2.50	2.9	85	5	10	50	1	135
BZX55C3V0	2.8	3.2	85	5	4	40	1	125
BZX55C3V3	3.1	3.5	85	5	2	40	1	115
BZX55C3V6	3.4	3.8	85	5	2	40	1	105
BZX55C3V9	3.7	4.1	85	5	2	40	1	95
BZX55C4V3	4.0	4.6	75	5	1	40	1	90
BZX55C4V7	4.4	5.0	60	5	0.5	10	1	85
BZX55C5V1	4.8	5.4	35	5	0.1	2	1	80
BZX55C5V6	5.2	6.0	25	5	0.1	2	1	70
BZX55C6V2	5.8	6.6	10	5	0.1	2	2	64
BZX55C6V8	6.4	7.2	8	5	0.1	2	3	58
BZX55C7V5	7.0	7.9	7	5	0.1	2	5	53
BZX55C8V2	7.7	8.7	7	5	0.1	2	6	47
BZX55C9V1	8.5	9.6	10	5	0.1	2	7	43
BZX55C10	9.5	10.6	15	5	0.1	2	7.5	40
BZX55C11	10.4	11.6	20	5	0.1	2	8.5	36
BZX55C12	11.4	12.7	20	5	0.1	2	9	32
BZX55C13	12.4	14.1	26	5	0.1	2	10	29
BZX55C15	13.8	15.6	30	5	0.1	2	11	27

BZX55C16 BZX55C18 BZX55C20 BZX55C22 BZX55C24	15.3 16.8 18.8 20.8 22.8	17.1 19.1 21.1 23.3 25.6	40 50 55 55 80	5 5 5 5 5	0.1 0.1 0.1 0.1 0.1	2 2 2 2 2	12 14 15 17 18	24 21 20 18 16
BZX55C27 BZX55C30 BZX55C33 BZX55C36 BZX55C39	25.1 28.0 31.0 34.0 37.0	28.9 32.0 35.0 38.0 41.0	80 80 80 80 90	5 5 5 5 2.5	0.1 0.1 0.1 0.1 0.1	2 2 2 2 2 5	20 22 24 27 28	14 13 12 11 10
BZX55C43 BZX55C47 BZX55C51 BZX55C56	40 44 48 52	46 50 54 60	90 110 125 135	2.5 2.5 2.5 2.5	0.1 0.1 0.1 0.1	5 5 10 10	32 35 38 42	9.2 8.5 7.8 7.0
V. Famuard Valtage 4 2V May @ L. 400m A								

V<sub>F</sub> Forward Voltage = 1.3V Max. @ I<sub>F</sub> = 100mA

# **Top Mark Information**

Device	Line 1	Line 2	Line 3
BZX55C2V4	LOGO	5C	2V4
BZX55C2V7	LOGO	5C	2V7
BZX55C3V0	LOGO	5C	3V0
BZX55C3V3	LOGO	5C	3V3
BZX55C3V6	LOGO	5C	3V6
BZX55C3V9	LOGO	5C	3V9
BZX55C4V3	LOGO	5C	4V3
BZX55C4V7	LOGO	5C	4V7
BZX55C5V1	LOGO	5C	5V1
BZX55C5V6	LOGO	5C	5V6
BZX55C6V2	LOGO	5C	6V2
BZX55C6V8	LOGO	5C	6V8
BZX55C7V5	LOGO	5C	7V5
BZX55C8V2	LOGO	5C	8V2
BZX55C9V1	LOGO	5C	9V1
BZX55C10	LOGO	5C	10
BZX55C11	LOGO	5C	11
BZX55C12	LOGO	5C	12
BZX55C13	LOGO	5C	13
BZX55C15	LOGO	5C	15
BZX55C16	LOGO	5C	16
BZX55C18	LOGO	5C	18
BZX55C20	LOGO	5C	20
BZX55C22	LOGO	5C	22
BZX55C24	LOGO	5C	24
BZX55C27	LOGO	5C	27
BZX55C30	LOGO	5C	30
BZX55C33	LOGO	5C	33
BZX55C36	LOGO	5C	36
BZX55C39	LOGO	5C	39
BZX55C43	LOGO	5C	43
BZX55C47	LOGO	5C	47
BZX55C51	LOGO	5C	51
BZX55C56	LOGO	5C	56

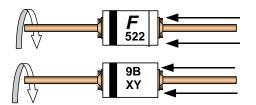
<sup>1.</sup> Zener Voltage ( $V_Z$ )

The zener voltage is measured with the device junction in the thermal equilibrium at the lead temperature ( $T_L$ ) at 30°C  $\pm$  1°C and 3/8" lead length.

<sup>2.</sup> Maximum Zener Current Ratings (I<sub>ZM</sub>)

The maximum current handling capability on a worst case basis is limited by the actual zener voltage at the operation point and the power derating curve.

# **Top Mark Information** (Continued)



1st line: F - Fairchild Logo

2<sup>nd</sup> line: Device Name - 4<sup>th</sup> to 5<sup>th</sup> characters of the device name. or 5<sup>th</sup> to 6<sup>th</sup> characters for BZXyy series

3<sup>rd</sup> line: Device Name - 6<sup>th</sup> to 7<sup>th</sup> characters of the device name. or Voltage rating for BZXyy series

## **General Requirements:**

1.0 Cathode Band

2.0 First Line: F - Fairchild Logo

3.0 Second Line: Device name - For 1Nxx series: 4<sup>th</sup> to 5<sup>th</sup> characters of the device name.

For BZxx series: 5<sup>th</sup> to 6<sup>th</sup> characters of the device name.

4.0 Third Line: Device name - For 1Nxx series: 6<sup>th</sup> to 7<sup>th</sup> characters of the device name.

For BZXyy series: Voltage rating

5.0 Devices shall be marked as required in the device specification (PID or FSC Test Spec).

6.0 Maximum no. of marking lines: 3

7.0 Maximum no. of digits per line: 2

8.0 FSC logo must be 20 % taller than the alphanumeric marking and should occupy the 2 characters of the specified line.

9.0 Marking Font: Arial (Except FSC Logo)

10.0 First character of each marking line must be aligned vertically.

11.0 All device markings must be based on Fairchild device specification.





### **TRADEMARKS**

The following are registered and unregistered trademarks Fairchild Semiconductor owns or is authorized to use and is not intended to be an exhaustive list of all such trademarks.

PowerSaver™ TinyBuck™ ACEx™ GTO™ TinyLogic<sup>®</sup>  $\mathsf{PowerTrench}^{\mathbb{R}}$ Across the board. Around the world.™ HiSeC™ ActiveArray<sup>™</sup> i-Lo™ Programmable Active Droop™ TINYOPTO™ QFET® TinyPower™ Bottomless™ ImpliedDisconnect™ TinyWire™  $QS^{TM}$ Build it Now™ IntelliMAX™ ISOPLANAR™ QT Optoelectronics™ TruTranslation™ CoolFET™ MICROCOUPLER™ Quiet Series™ μSerDes™ CROSSVOLT™  $\mathsf{UHC}^{\mathbb{B}}$ RapidConfigure™  $CTL^{TM}$ MicroPak™ UniFET™ Current Transfer Logic™ MICROWIRE™ RapidConnect™ **VCX**<sup>TM</sup> DOME™ ScalarPump™  $MSX^{TM}$ Wire™ E<sup>2</sup>CMOS™ SMART START™ MSXPro™

EcoSPARK®  $OCX^{TM}$ SPM™ EnSigna™  $OCXPro^{TM}$ SuperFET™ OPTOLOGIC<sup>®</sup> FACT Quiet Series™ SuperSOT™-3 FACT<sup>®</sup>  $\mathsf{OPTOPLANAR}^{\mathsf{TM}^{\textcircled{\tiny{\$}}}}$ SuperSOT™-6  $\mathsf{FAST}^{\mathbb{R}}$ PACMAN™ SuperSOT™-8 РОР™  $\mathsf{TCM}^\mathsf{TM}$ FASTr™

FPS™ Power220<sup>®</sup> The Power Franchise<sup>®</sup>

FRFET<sup>TM</sup> Power247<sup>®</sup> TinyBoost<sup>TM</sup>

GlobalOptoisolator™ PowerEdge™

### **DISCLAIMER**

FAIRCHILD SEMICONDUCTOR RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION OR DESIGN. FAIRCHILD DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS. THESE SPECIFICATIONS DO NOT EXPAND THE TERMS OF FAIRCHILD'S WORLDWIDE TERMS AND CONDITIONS, SPECIFICALLY THE WARRANTY THEREIN, WHICH COVERS THESE PRODUCTS.

## **LIFE SUPPORT POLICY**

FAIRCHILD'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF FAIRCHILD SEMICONDUCTOR CORPORATION.

As used herein:

1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, or (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in significant injury to the user.

2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

## PRODUCT STATUS DEFINITIONS

## **Definition of Terms**

Datasheet Identification	Product Status	Definition
Advance Information	Formative or In Design	This datasheet contains the design specifications for product development. Specifications may change in any manner without notice.
Preliminary	First Production	This datasheet contains preliminary data, and supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
No Identification Needed	Full Production	This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
Obsolete	Not In Production	This datasheet contains specifications on a product that has been discontinued by Fairchild semiconductor. The datasheet is printed for reference information only.

Rev. I23