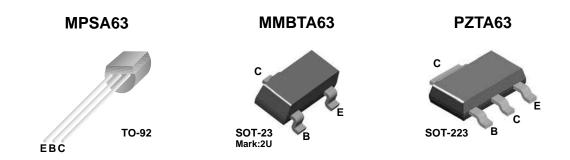


August 2010

# MPSA63 / MMBTA63 / PZTA63 PNP Darlington Transistor

#### **Features**

- This device is designed for applications requiring extremely high current gain at currents to 800 mA.
- Sourced from Process 61.



### **Absolute Maximum Ratings \*** T<sub>a</sub> = 25°C unless otherwise noted

Symbol	Parameter	Value	Units
V <sub>CES</sub>	Collector-Emitter Voltage	-30	V
V <sub>CBO</sub>	Collector-Base Voltage	-30	V
V <sub>EBO</sub>	Emitter-Base Voltage	-10	V
I <sub>C</sub>	Collector Current - Continuous	-1.2	Α
T <sub>J,</sub> T <sub>stg</sub>	Operating and Storage Junction Temperature Range	- 55 to +150	°C

<sup>\*</sup> These ratings are limiting values above which the serviceability of any semiconductor device may be impaired.

#### **Thermal Characteristics** T<sub>a</sub> = 25°C unless otherwise noted

Symbol	Parameter	Max.			Units
		MPSA63	*MMBTA63	**PZTA63	Oille
P <sub>D</sub>	Total Device Dissipation Derate above 25°C	625 5.0	350 2.8	1,000 8.0	mW mW/°C
$R_{\theta JC}$	Thermal Resistance, Junction to Case	83.3			°C/W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	200	357	125	°C/W

<sup>\*</sup> Device mounted on FR-4 PCB 1.6"  $\times$  1.6"  $\times$  0.06".

<sup>1)</sup> These ratings are based on a maximum junction temperature of 150 degrees C.

<sup>2)</sup> These are steady state limits. The factory should be consulted on applications involving pulsed or low duty cycle operations.

<sup>\*\*</sup> Device mounted on FR-4 PCB 36mm × 18mm × 1.5mm; mounting pad for the collector lead min. 6cm<sup>2</sup>.

## **Electrical Characteristics** $T_a = 25\%$ unless otherwise noted

Symbol	Parameter	Test Condition	Min.	Max.	Units
Off Character	istics				•
BV <sub>(BR)CES</sub>	Collector-Emitter Breakdown Voltage	$I_C = -100 \mu A, I_B = 0$	-30		V
I <sub>CBO</sub>	Collector-Cutoff Current	$V_{CB} = -30V, I_{E} = 0$		-100	nA
I <sub>EBO</sub>	Emitter-Cutoff Current	$V_{EB} = -10V, I_{C} = 0$		-100	nA
On Character	istics *				
h <sub>FE</sub>	DC Current Gain	I <sub>C</sub> = -10mA, V <sub>CE</sub> = -5.0V I <sub>C</sub> = -100mA, V <sub>CE</sub> = -5.0V	5,000 10,000		
V <sub>CE(sat)</sub>	Collector-Emitter Saturation Voltage	$I_C = -100 \text{mA}, I_B = -0.1 \text{mA}$		-1.5	V
V <sub>BE(on)</sub>	Base-Emitter On Voltage	$I_C = -100 \text{mA}, V_{CE} = -5.0 \text{V}$		-2.0	V
Small Signal	Characteristics		•	•	•
f <sub>T</sub>	Current Gain - Bandwidth Product	I <sub>C</sub> = -10mA, V <sub>CE</sub> = -5.0V, f = 100MHz	125		MHz

<sup>\*</sup> Pulse Test: Pulse Width ≤ 300μs, Duty Cycle ≤ 2.0%

#### **Typical Performance Characteristics**

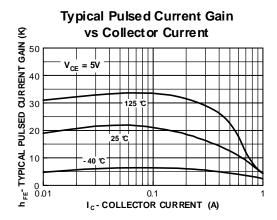


Figure 1. Typical Pulsed Current Gain vs Collector Current

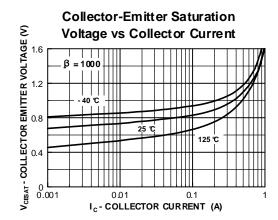


Figure 2. Collector-Emitter Saturation Voltage vs Collector Current

#### **Typical Performance Characteristics** (continued)

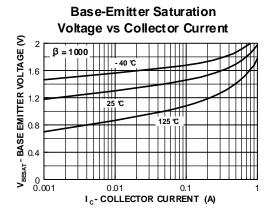


Figure 3. Base-Emitter Saturation Voltage vs Collector Current

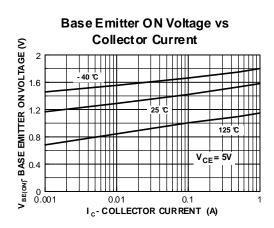


Figure 4. Base-Emitter On Voltage vs Collector Current



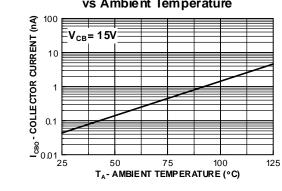


Figure 5. Collector Cutoff Current vs Ambient Temperature

## Input and Output Capacitance vs Reverse Bias Voltage

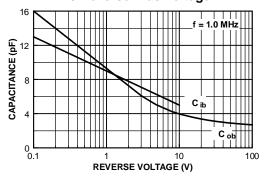


Figure 6. Input and Output Capacitance vs Reverse Bias Voltage

#### Power Dissipation vs Ambient Temperature

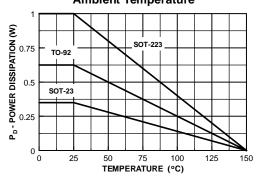


Figure 7. Power Dissipation vs Ambient Temperature





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