

July 2008

# TIP41/TIP41A/TIP41B/TIP41C NPN Epitaxial Silicon Transistor

### **Features**

• Complementary to TIP42/TIP42A/TIP42B/TIP42C



1. Base 2. Collector 3. Emitter

## Absolute Maximum Ratings T<sub>C</sub>=25°C unless otherwise noted

Symbol	Parameter	Value	Units
V <sub>CBO</sub>	Collector-Emitter Voltage: TIP41 : TIP41A : TIP41B : TIP41C	40 60 80 100	V V V
V <sub>CEO</sub>	Collector-Emitter Voltage: TIP41 : TIP41A : TIP41B : TIP41C	40 60 80 100	V V V
V <sub>EBO</sub>	Emitter-Base Voltage	5	V
Ic	Collector Current (DC)	6	А
I <sub>CP</sub>	Collector Current (Pulse)	10	А
I <sub>B</sub>	Base Current	2	А
P <sub>C</sub>	Collector Dissipation (T <sub>C</sub> =25°C)	65	W
	Collector Dissipation (T <sub>a</sub> =25°C)	2	W
T <sub>J</sub>	Junction Temperature	150	°C
T <sub>STG</sub>	Storage Temperature	- 65 ~ 150	°C

## Electrical Characteristics $T_C=25$ °C unless otherwise noted

Symbol	Parameter	Test Condition	Min.	Max.	Units
V <sub>CEO</sub> (sus)	* Collector-Emitter Sustaining Voltage : TIP41 : TIP41A : TIP41B : TIP41C	I <sub>C</sub> = 30mA, I <sub>B</sub> = 0	40 60 80 100		> > >
I <sub>CEO</sub>	Collector Cut-off Current : TIP41/41A : TIP41B/41C	V <sub>CE</sub> = 30V, I <sub>B</sub> = 0 V <sub>CE</sub> = 60V, I <sub>B</sub> = 0		0.7 0.7	mA mA
I <sub>CES</sub>	Collector Cut-off Current : TIP41 : TIP41A : TIP41B : TIP41C	V <sub>CE</sub> = 40V, V <sub>EB</sub> = 0 V <sub>CE</sub> = 60V, V <sub>EB</sub> = 0 V <sub>CE</sub> = 80V, V <sub>EB</sub> = 0 V <sub>CE</sub> = 100V, V <sub>EB</sub> = 0		400 400 400 400	μΑ μΑ μΑ μΑ
I <sub>EBO</sub>	Emitter Cut-off Current	V <sub>EB</sub> = 5V, I <sub>C</sub> = 0		1	mA
h <sub>FE</sub>	* DC Current Gain	$V_{CE} = 4V_{IC} = 0.3A$ $V_{CE} = 4V_{IC} = 3A$	30 15	75	
V <sub>CE</sub> (sat)	* Collector-Emitter Saturation Voltage	I <sub>C</sub> = 6A, I <sub>B</sub> = 600mA		1.5	V
V <sub>BE</sub> (sat)	* Base-Emitter Saturation Voltage	V <sub>CE</sub> = 4V, I <sub>C</sub> = 6A		2.0	V
f <sub>T</sub>	Current Gain Bandwidth Product	V <sub>CE</sub> = 10V, I <sub>C</sub> = 500mA, f = 1MHz	3.0		MHz

<sup>\*</sup> Pulse Test: PW≤300ms, Duty Cycle≤2%

# **Typical Characteristics**

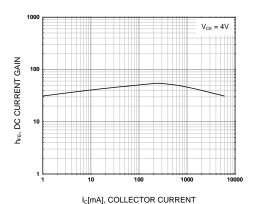


Figure 1. DC current Gain

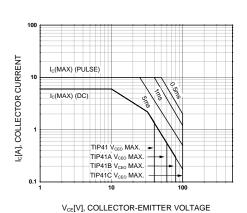


Figure 3. Safe Operating Area

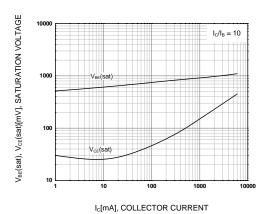


Figure 2. Base-Emitter Saturation Voltage Collector-Emitter Saturation Voltage

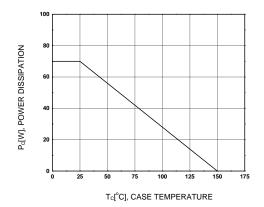
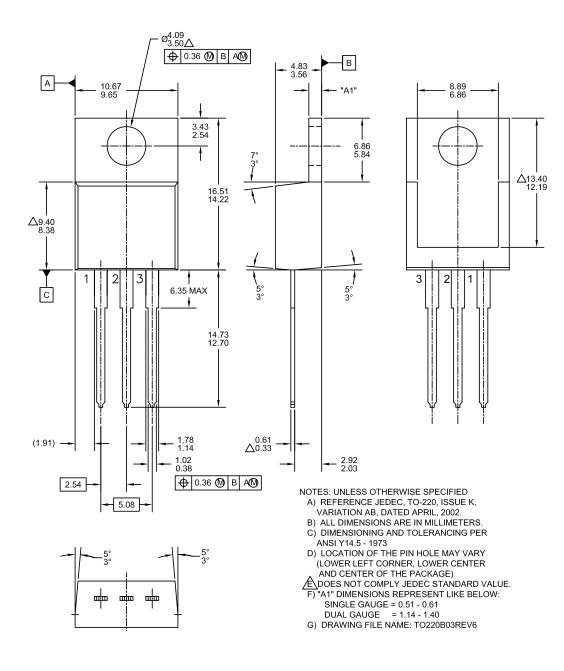


Figure 4. Power Derating

## **Mechanical Dimensions**

## **TO220**







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