

**Dual Output** 

# HIGH RELIABILITY DC-DC CONVERTER

16-40V Continuous Input 16-50V Transient Input ±5V

4A Total Output 83% @ 2A / 85% @ 4A

Efficiency

Full Power Operation: -55°C to +125°C

Output

The MilQor® series of high-reliability DC-DC converters brings SynQor's field proven high-efficiency synchronous rectifier technology to the Military/Aerospace industry. SynQor's innovative QorSeal™ packaging approach ensures survivability in the most hostile environments. Compatible with the industry standard format, these converters operate at a fixed frequency, have no opto-isolators, and follow conservative component derating guidelines. They are designed and manufactured to comply with a wide range of military standards.

## **Design Process**

MOBL series converters are:

- Designed for reliability per NAVSO-P3641-A guidelines
- Designed with components derated per:
  - MIL-HDBK-1547A
  - NAVSO P-3641A

#### **Qualification Process**

MQBL series converters are qualified to:

- MIL-STD-810F
  - consistent with RTCA/D0-160E
- SynQor's First Article Qualification
  - consistent with MIL-STD-883F
- SynQor's Long-Term Storage Survivability Qualification
- SynQor's on-going life test

#### **In-Line Manufacturing Process**

- AS9100 and ISO 9001:2008 certified facility
- Full component traceability
- Temperature cycling
- Constant acceleration
- •24, 96, 160 hour burn-in
- Three level temperature screening





Designed & Manufactured in the USA Featuring QorSeal\*\* HI-Rel Assembly

## **Features**

- Fixed switching frequency
- No opto-isolators
- Output over-voltage shutdown
- Remote sense
- Clock synchronization
- Primary referenced enable
- Continuous short circuit and overload protection
- Input under-voltage and over-voltage shutdown

## **Specification Compliance**

MQBL series converters (with MQHE filter) are designed to meet:

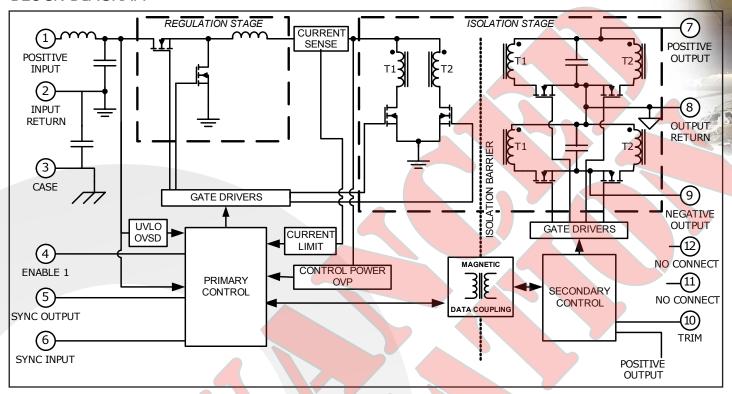
- MIL-HDBK-704-8 (A through F)
- RTCA/DO-160 Section 16
- MIL-STD-1275 for  $V_{IN} > 16V$
- DEF-STAN 61-5 (part 6)/5 for V<sub>™</sub> > 16V
- MIL-STD-461 (C, D, E)
- RTCA/DO-160 Section 22



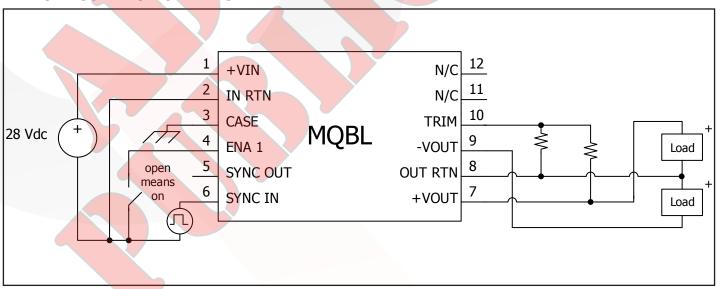
Output: ±5V

**Current: 4A Total** 

## **BLOCK DIAGRAM**



## TYPICAL CONNECTION DIAGRAM





Output: ±5V

**Current: 4A Total** 

Vin = 289 of £5%, +lout = 1out = 2A, CL = 0µf, Sut free numing (see Nuce 9) unless otherwise specified   (see 125%)   (see Nuclear 1) unless otherwise specified   (see 125%)   (see Nuclear 1) unless otherwise specified   (see 125%)   (see Nuclear 1) unless otherwise specified   (see Nucle	MQBL-28-05D ELECTRIC Parameter					Notes & Conditions	Group A
ABSOLUTE MAXIMUM RATINGS   Imput Votage   Non-Operating   Coperating			.,,	l luxi		Vin = 28V dc $\pm$ 5%, +Iout = -Iout = 2A, CL = 0 $\mu$ F,	Subgroup
Input Voltage   Non-Operating   60	ARSOLUTE MAYIMUM PATINGS					free running (see Note 9) unless otherwise specified	(see Note 11)
Non-Operating							
Continuous	_ '			60	V		
Reverse Bias (Tase = -59C)						See Note 1	
				-0.8	V		
Continuous	Reverse Bias (Tcase = -55°C)			-1.2	V		-5-6
Transient (\$100µs)							
Operating Case Temperature							63
Storage Case Temperature	Transient (≤100µs)						1 500 00
Lead Temperature (20s)   Voltage a EINA1   1.2   50   V   Voltage at EINA1   1.2   50   V   Voltage at EINA1   1.2   50   V   Voltage Temperature (20s)   V   Voltage Threshold   14.75   15.50   V   V   V   V   V   V   V   V   V	Operating Case Temperature				_	See Note 2	etta
Voltage   TeNA1		-65			_		
INPUT CHARACTERISTICS   16		1 2			_		
Operating Input Voltage Range		-1.2		30	V		
16		16	28	40	V	Continuous	1, 2, 3
Input Under-Voltage Shutdown   14.75   15.50   16.00   V	"						4, 5, 6
Turn-On Voltage Threshold 14,75 15,50 14,75 15,50 V Shutdown Voltage Hysteresis 0,55 0,85 1,05 V See Note 3	Input Under-Voltage Shutdown	10					., 5, 6
Turn-Off Voltage Threshold   14,00   14,75   15,50   V     Shutdown Voltage Hysteresis   0,55   0,85   1,05   V     See Note 3   Turn-Off Voltage Threshold   52,0   55,0   54,0   56,5   V     See Note 3   Turn-Off Voltage Threshold   50,5   54,0   56,5   V   See Note 3   Turn-Off Voltage Threshold   50,5   54,0   56,5   V   See Note 3   Turn-Off Voltage Threshold   50,5   54,0   56,5   V   See Note 3   Turn-Off Voltage Threshold   50,5   54,0   56,5   V   See Note 3   Turn-Off Voltage Threshold   50,5   54,0   56,5   V   See Note 3   Turn-Off Voltage Threshold   50,5   54,0   56,5   V   See Note 3   Turn-Off Voltage Threshold   50,5   54,0   56,5   V   See Note 14   Vin = 16V; +Iout = -Iout = 2A   Turn-Off Voltage Form of the properties   Turn-Off Voltage Form of the properties   Turn-Off Voltage Part of the properties   Turn-Off Voltage Part of the properties   Turn-Off Voltage Part of the Positive Output Voltage Line Regulation   15,5   0,5   0,5   0   V   See Note 14   See Not		14.75	15.50	16.00	V		1, 2, 3
Shutdown Voltage Hysteresis   0.65   0.85   1.05   V   See Note 3   1   1   1   1   1   1   1   1   1	Turn-Off Voltage Threshold						1, 2, 3
Input Over-Voltage Shutdown	Shutdown Voltage Hysteresis				V		1, 2, 3
Turn-On Voltage Phreshold   Shutdown Voltage Step Point (Tease = 25°C)   A.95						See Note 3	
Shutdown Voltage Hysteresis							1, 2, 3
Maximum Input Current (operating)							1, 2, 3
No Load Input Current (operating)   75   100   mA   10   15   mA   10		1.0	2.0				1, 2, 3
Disabled Input Current   Input Terminal Current Ripple (pk-pk)   25   40   mA   Mandwidth = 100kHz - 10MHz; see Figure 14   10   15   mA   Mandwidth = 100kHz - 10MHz; see Figure 14   10   15   mA   Mandwidth = 100kHz - 10MHz; see Figure 14   10   15   mA   Mandwidth = 100kHz - 10MHz; see Figure 14   10   15   mA   Mandwidth = 100kHz - 10MHz; see Figure 14   10   15   mA   Mandwidth = 100kHz - 10MHz; see Figure 14   10   15   mA   Mandwidth = 100kHz - 10MHz; see Figure 14   10   15   mA   Mandwidth = 100kHz - 10MHz; see Figure 14   10   15   mA   Mandwidth = 100kHz - 10MHz; see Figure 14   10   15   mA   Mandwidth = 100kHz - 10MHz; see Figure 14   10   15   mA   Mandwidth = 100kHz - 10MHz; see Figure 14   10   10   15   mA   Mandwidth = 100kHz - 10MHz; see Figure 14   10   10   10   10   10   10   10			75			Vin = 16V; +lout = -lout = 2A	1, 2, 3
Input Terminal Current Ripple (pk-pk)   25   40   mA   Bandwidth = 100kHz - 10MHz; see Figure 14   10   10   10   10   10   10   10						Vin 10V 20V FOW FNA	1, 2, 3
Output Voltage Set Point (Tcase = 25°C) Positive Output Negative Output Voltage Line Regulation Negative Output Voltage Notage Range Negative Output Voltage Range Notage Cross Regulation Notage						Pandwidth = 100kHz 10MHz; coo Figure 14	1, 2, 3
Output Voltage Set Point (Tcase = 25°C)			23	40	IIIA	Ballawiatii = 100ki iz = 10Mi iz, see Figure 14	1, 2, 3
Positive Output					V	See Note 14	
Negative Output		4.95	5.00	5.05		See Note 11	1
Output Voltage Set Point Over Temperature							ī
Positive Output			0.00		V	See Note 14	_
Positive Output Voltage Line Regulation   -20   0   20   mV   See Note 14; Vin = 16V, 28V, 40V   15		4.90	5.00	5.10	V		2, 3
Positive Output Voltage Load Regulation Total Positive Output Voltage Range 4.90 5.00 Cutput Voltage Cross Regulation 200 450 750 MV See Note 14; +Vout @(+Iout=-Iout=0A) - +Vout @(+Iout=-Iout=2A) 1 Output Voltage Cross Regulation 200 450 750 MV See Note 14; +Vout @(+Iout=-Iout=0A) - +Vout @(+Iout=-Iout=0A) 1 Output Voltage Shutdown 5.6 6.1 7.1 V See Note 13; and 14; +Vout @(+Iout=-Iout=0A) - +Vout @(+Iout=-Iout=0A) 1 Output Voltage Shutdown 5.6 6.1 7.1 V See Note 14; +Vout @(+Iout=-Iout=0A) - +Vout @(+Iout=-Iout=0A) 1 Output Voltage Shutdown 5.6 6.1 7.1 V See Note 14; +Vout @(+Iout=-Iout=0A) - +Vout @(+Iout=-Iout=0A) 1 Output Voltage Shutdown 5.6 6.1 7.1 V See Note 14; +Vout @(+Iout=-Iout=0A) - +Vout @(+Iout=-Iout=0A) 1 Output Voltage Range 0 4 A (+Iout) + (-Iout) 1 Output Por Current Range 0 3.2 A Maximum Hout or -Iout 1 Output DC Current Limit Inception 4.10 4.80 4.0 A Raximum Output Capacitance 0 0 A See Note 14; +Vout @(+Iout=-Iout=0A) - +Vout@(+Iout=-Iout=0A) 1 Output Por Current Limit Newleta			-5.00				2, 3
Total Positive Output Voltage Range Output Voltage Cross Regulation Output Over-Voltage Shutdown Output Over-Voltage Shutdown Output Voltage Ripple and Noise Peak to Peak Operating Output Current Range Operating Output Current Range Operating Output Power Range Operating Output Power Range Output DC Current-Limit Inception Back-Drive Current Limit while Enabled Back-Drive Current Limit while Disabled Maximum Output Capacitance DYNAMIC CHARACTERISTICS Output Voltage Deviation Load Transient For a Pos. Step Change in Load Current For a Neg. Step Change in Load Current For a Neg. Step Change in Load Current Output Voltage Deviation Line Transient For a Neg. Step Change in Line Voltage Turn-On Transient Output Voltage Overshoot Turn-On Delay, Rising ENA  4.90 5.00 450 750 mV See Note 14 See Note (14) See Note (15) See Note (14) See Note (15)			_				1, 2, 3
Output Voltage Cross Regulation Output Over-Voltage Shutdown Output Voltage Ripple and Noise Peak to Peak Operating Output Current Range Operating Output Current Range Operating Output Power Range Operating Output Power Range Output DC Current-Limit Inception Back-Drive Current Limit while Enabled Back-Drive Current Limit while Disabled Maximum Output Capacitance DYNAMIC CHARAGTERISTICS Output Voltage Deviation Load Current For a Pos. Step Change in Load Current For a Neg. Step Change in Load Current For a Neg. Step Change in Line Voltage Turn-On Transient Output Voltage Rise Time Output Voltage Rise Time Output Voltage North Range O	Positive Output Voltage Load Regulation						1, 2, 3
Output Over-Voltage Shutdown Output Voltage Ripple and Noise Peak to Peak Operating Output Current Range Operating Output Operating Current Range Operating Output Power Power Range Operating Output Power Power Range Operating Output Power Powe							1, 2, 3
Output Voltage Ripple and Noise Peak to Peak Operating Output Current Range O Single Output Operating Current Range O Operating Output Power Range O Output DC Current-Limit Inception Back-Drive Current Limit while Enabled Back-Drive Current Limit while Disabled Maximum Output Capacitance DYNAMIC CHARACTERISTICS Output Voltage Deviation Load Transient For a Pos. Step Change in Load Current For a Pos. Step Change in Line Voltage Output Voltage Deviation Line Transient For a Reg. Step Change in Line Voltage For a Neg. Step Change in Line Voltage Turn-On Transient Output Voltage Overshoot Turn-On Delay, Rising Vin Turn-On Delay, Rising ENA  Description  A Maximum Hout or -Iout A Maximum Hout or Iout B C+ Iout Hout + -Iout; Hout + -Iout; Hout = -Iout B C+ Iout Hout Hout or -Iout A Nout Hout Hout A A  (+Iout Hout Hout or -Iout A Nout Hout Hout A Nout Hout Hout Hou	Output Voltage Cross Regulation					See Notes 13 and 14; -Vout@(+Iout=-Iout=0.8A)Vout@(+Iout=3.2A, -Iout=0.8A)	1, 2, 3
Operating Output Current Range O Single Output Operating Current Range O Operating Output Power Range O Operating Output Power Range O Output DC Current-Limit Inception Back-Drive Current Limit while Enabled Back-Drive Current Limit while Disabled Maximum Output Capacitance Output Voltage Deviation Load Transient For a Pos. Step Change in Load Current For a Neg. Step Change in Line Voltage Turn-On Transient Output Voltage Overshoot Turn-On Delay, Rising Vin Turn-On Delay, Rising ENA  A (+Iout) + (-Iout) A (Hout) + (-Iout) A (Hout) + (-Iout) A (A (A) (A (+Iout) + (-Iout) A (A (A) (A (		5.6				Pandwidth 10MI by Cl. 11. For hoth cutmute	See Note 5
Single Output Operating Current Range Operating Output Power Range Output Doutput Power Range Output Doutput Power Range Output Doutput Power Range Output Doutput Inception And See Note 4; +Iout + -Iout; +Iout = -Iout  See Note 4; +Iout + -Iout; +Iout = -Iout  See Note 4; +Iout + -Iout; +Iout = -Iout  And See Note 4; +Iout + -Iout; +Iout = -Iout  Inception Inception And See Note 4; +Iout + -Iout; +Iout = -Iout  Inception Inception And See Note 4; +Iout + -Iout; +Iout = -Iout Inception Inception Inception And See Note 4; +Iout + -Iout; +Iout = -Iout Inception Inception Inception Inception Inception And See Note 4; +Iout + -Iout; +Iout = -Iout Inception Incept		0	20				1, 2, 3
Operating Output Power Range Output DC Current-Limit Inception Back-Drive Current Limit while Enabled Back-Drive Current Limit while Disabled DYNAMIC CHARACTERISTICS Output Voltage Deviation Load Transient For a Pos. Step Change in Load Current Output Voltage Deviation Line Transient For a Neg. Step Change in Line Voltage For a Neg. Step Change in Line Voltage Turn-On Transient Output Voltage Overshoot Output Voltage Overshoot Turn-On Delay, Rising Vin Turn-On Delay, Rising ENA  20  W Total on both outputs See Note 4; +Iout + -Iout; +Iout = -Iout  A See Note 4; +Iout + -Iout; +Iout = -Iout  1  See Note 4; +Iout + -Iout; +Iout = -Iout  1  See Note 4; +Iout + -Iout; +Iout = -Iout  1  See Note 4; +Iout + -Iout; +Iout = -Iout  1  See Note 4; +Iout + -Iout; +Iout = -Iout  1  See Note 4; +Iout + -Iout; +Iout = -Iout  1  Total on both outputs  See Note 6  Total on both outputs  See Note 6  Total Iout step = 2A to 4A, 0.4A to 2A; CL=11µF on both outputs  W Vin step = 16V to 50V; CL=11µF on both outputs; see Note 7  A  For a Neg. Step Change in Line Voltage  Turn-On Transient  Output Voltage Overshoot  Output Voltage Overshoot  Turn-On Delay, Rising Vin  Turn-On Delay, Rising Vin  Turn-On Delay, Rising ENA  3.0  See Note 4; +Iout + -Iout; +Iout = -Iout  A  See Note 4; +Iout + -Iout; +Iout = -Iout  A  See Note 4; +Iout + -Iout; +Iout = -Iout  In A  See Note 4; +Iout + -Iout; +Iout = -Iout  A  See Note 4; +Iout + -Iout; +Iout = -Iout  In A  See Note 6  Total on both outputs  See Note 6  Total Iout step = 2A to 4A, 0.4A to 2A; CL=11µF on both outputs  W Vin step = 16V to 50V; CL=11µF on both outputs; see Note 7  A  For a Neg. Step Change in Line Voltage  Total on both outputs  See Note 6  Total Iout step = 2A to 4A, 0.4A to 2A; CL=11µF on both outputs  W Vin step = 16V to 50V; CL=11µF on both outputs  A  For a Neg. Step Change		_					1, 2, 3 1, 2, 3
Output DC Current-Limit Inception  Back-Drive Current Limit while Enabled  Back-Drive Current Limit while Disabled  Back-Drive Disable Disabled  Back-Drive Disabled  Back-Drive Disable Disabled  Back-Drive Disable Disable Disabl							1, 2, 3
Back-Drive Current Limit while Enabled Back-Drive Current Limit while Disabled Maximum Output Capacitance DYNAMIC CHARACTERISTICS Output Voltage Deviation Load Transient For a Pos. Step Change in Load Current Output Voltage Deviation Line Transient For a Pos. Step Change in Line Voltage For a Neg. Step Change in Line Voltage Turn-On Transient Output Voltage Rise Time Output Voltage Overshoot Turn-On Delay, Rising Vin Turn-On Delay, Rising ENA  3.00 A MA  Total on both outputs See Note 6 Total Iout step = 2A to 4A, 0.4A to 2A; CL=11μF on both outputs  W Total Iout step = 16V to 50V; CL=11μF on both outputs; see Note 7  Total on both outputs  Total on both outputs  A Total on both outputs  See Note 6  Total Iout step = 16V to 50V; CL=11μF on both outputs; see Note 7  W Total Iout step = 16V to 50V; CL=11μF on both outputs; see Note 7  A Vin step = 16V to 50V; CL=11μF on both outputs; see Note 7  A A Total on both outputs  See Note 10  A Total on both outputs  A Total on both outputs  See Note 10  A Total on both outputs  See Note 10  A Total on both outputs  A Tot			4.80				1, 2, 3
Back-Drive Current Limit while Disabled Maximum Output Capacitance DYNAMIC CHARACTERISTICS Output Voltage Deviation Load Transient For a Pos. Step Change in Load Current Output Voltage Deviation Line Transient For a Pos. Step Change in Line Voltage For a Neg. Step Change in Line Voltage For a Neg. Step Change in Line Voltage Turn-On Transient Output Voltage Rise Time Output Voltage Overshoot Output Voltage Overshoot Turn-On Delay, Rising Vin Turn-On Delay, Rising ENA  Total on both outputs See Note 6 Total Iout step = 2A to 4A, 0.4A to 2A; CL=11µF on both outputs  W Total Iout step = 2A to 4A, 0.4A to 2A; CL=11µF on both outputs  W Total Iout step = 16V to 50V; CL=11µF on both outputs; see Note 7  Total Iout step = 16V to 50V; CL=11µF on both outputs  W Resistive load For a Neg. Step Change in Line Voltage  -125 Turn-On Delay, Rising Vin Turn-On Delay, Rising ENA  See Note 10  Total Iout step = 2A to 4A, 0.4A to 2A; CL=11µF on both outputs  W Resistive load For a Neg. Step Change in Line Voltage  -125 Turn-On Delay, Rising Vin See Note 10	Back-Drive Current Limit while Enabled	1.10		5.00		See Note 1, 11out 1 1out, 11out 2 1out	1, 2, 3
Maximum Output Capacitance2,000μFTotal on both outputsDYNAMIC CHARACTERISTICSOutput Voltage Deviation Load TransientSee Note 6For a Pos. Step Change in Load Current-300Total Iout step = 2A to 4A, 0.4A to 2A; CL=11μF on both outputsFor a Neg. Step Change in Load Current200300mVOutput Voltage Deviation Line TransientVin step = 16V to 50V; CL=11μF on both outputs; see Note 7For a Pos. Step Change in Line Voltage-125125mVFor a Neg. Step Change in Line Voltage-125125mVTurn-On Transient610ms+Vout = 0.5V to 4.5V; Full Resistive LoadOutput Voltage Rise Time610ms+Vout = 0.5V to 4.5V; Full Resistive LoadOutput Voltage Overshoot02%Resistive loadTurn-On Delay, Rising Vin5.58.0msENA = 5V; see Notes 8 & 10Turn-On Delay, Rising ENA3.06.0msSee Note 10				50			1, 2, 3
DYNAMIC CHARACTERISTICSOutput Voltage Deviation Load TransientSee Note 6For a Pos. Step Change in Load Current-300Total Iout step = 2A to 4A, 0.4A to 2A; CL=11μF on both outputsFor a Neg. Step Change in Load Current200300mVOutput Voltage Deviation Line TransientVin step = 16V to 50V; CL=11μF on both outputs; see Note 7For a Pos. Step Change in Line Voltage-125125mVFor a Neg. Step Change in Line Voltage-125125mVTurn-On Transient610ms+Vout = 0.5V to 4.5V; Full Resistive LoadOutput Voltage Rise Time610ms+Vout = 0.5V to 4.5V; Full Resistive LoadOutput Voltage Overshoot02%Resistive loadTurn-On Delay, Rising Vin5.58.0msENA = 5V; see Notes 8 & 10Turn-On Delay, Rising ENA3.06.0msSee Note 10						Total on both outputs	See Note 5
Output Voltage Deviation Load Transient For a Pos. Step Change in Load Current For a Neg. Step Change in Load Current Output Voltage Deviation Line Transient For a Pos. Step Change in Line Voltage For a Neg. Step Change in Line Voltage For a Neg. Step Change in Line Voltage For a Neg. Step Change in Line Voltage Turn-On Transient Output Voltage Rise Time Output Voltage Overshoot Turn-On Delay, Rising Vin Turn-On Delay, Rising ENA  See Note 6  Total Iout step = 2A to 4A, 0.4A to 2A; CL=11µF on both outputs  Win step = 16V to 50V; CL=11µF on both outputs; see Note 7  Total Iout step = 2A to 4A, 0.4A to 2A; CL=11µF on both outputs  Win step = 16V to 50V; CL=11µF on both outputs; see Note 7  For a Neg. Step Change in Line Voltage  Turn-On Transient Output Voltage Rise Time Output Voltage Overshoot See Turn-On Delay, Rising Vin Turn-On Delay, Rising ENA  See Note 6  Total Iout step = 2A to 4A, 0.4A to 2A; CL=11µF on both outputs  Win step = 16V to 50V; CL=11µF on both outputs; see Note 7  For a Neg. Step Change in Line Voltage  Turn-On Transient Output Voltage Rise Time Output Voltage Overshoot See Note 10							
For a Neg. Step Change in Load Current Output Voltage Deviation Line Transient For a Pos. Step Change in Line Voltage For a Neg. Step Change in Line Voltage Turn-On Transient Output Voltage Rise Time Output Voltage Overshoot Turn-On Delay, Rising Vin Turn-On Delay, Rising ENA  Two Vin step = 16V to 50V; CL=11µF on both outputs; see Note 7  Vin step = 16V to 50V; CL=11µF on both outputs; see Note 7  Vin step = 16V to 50V; CL=11µF on both outputs; see Note 7  Vin step = 16V to 50V; CL=11µF on both outputs; see Note 7  Vin step = 16V to 50V; CL=11µF on both outputs; see Note 7  Vin step = 16V to 50V; CL=11µF on both outputs; see Note 7  Vin step = 16V to 50V; CL=11µF on both outputs; see Note 7  Vin step = 16V to 50V; CL=11µF on both outputs; see Note 7  Vin step = 16V to 50V; CL=11µF on both outputs; see Note 7  Vin step = 16V to 50V; CL=11µF on both outputs; see Note 7  Vin step = 16V to 50V; CL=11µF on both outputs; see Note 7  Vin step = 16V to 50V; CL=11µF on both outputs; see Note 7  Vin step = 16V to 50V; CL=11µF on both outputs; see Note 7  Vin step = 16V to 50V; CL=11µF on both outputs; see Note 7  Vin step = 16V to 50V; CL=11µF on both outputs; see Note 7  Vin step = 16V to 50V; CL=11µF on both outputs; see Note 7  Vin step = 16V to 50V; CL=11µF on both outputs; see Note 7  Vin step = 16V to 50V; CL=11µF on both outputs; see Note 7							
Output Voltage Deviation Line Transient For a Pos. Step Change in Line Voltage For a Neg. Step Change in Line Voltage Turn-On Transient Output Voltage Rise Time Output Voltage Overshoot Turn-On Delay, Rising Vin Turn-On Delay, Rising ENA  Vin step = 16V to 50V; CL=11µF on both outputs; see Note 7  Vin step = 16V to 50V; CL=11µF on both outputs; see Note 7  Vin step = 16V to 50V; CL=11µF on both outputs; see Note 7  Vin step = 16V to 50V; CL=11µF on both outputs; see Note 7  Vin step = 16V to 50V; CL=11µF on both outputs; see Note 7  Vin step = 16V to 50V; CL=11µF on both outputs; see Note 7  Vin step = 16V to 50V; CL=11µF on both outputs; see Note 7  Vin step = 16V to 50V; CL=11µF on both outputs; see Note 7  Vin step = 16V to 50V; CL=11µF on both outputs; see Note 7  Vin step = 16V to 50V; CL=11µF on both outputs; see Note 7  Vin step = 16V to 50V; CL=11µF on both outputs; see Note 7  Vin step = 16V to 50V; CL=11µF on both outputs; see Note 7  Vin step = 16V to 50V; CL=11µF on both outputs; see Note 7  Vin step = 16V to 50V; CL=11µF on both outputs; see Note 7  Vin step = 16V to 50V; CL=11µF on both outputs; see Note 7  Vin step = 16V to 50V; CL=11µF on both outputs; see Note 7  Vin step = 16V to 50V; CL=11µF on both outputs; see Note 7	For a Pos. Step Change in Load Current	-300				Total Iout step = 2A to 4A, 0.4A to 2A; CL=11µF on both outputs	4, 5, 6
For a Pos. Step Change in Line Voltage For a Neg. Step Change in Line Voltage Turn-On Transient Output Voltage Rise Time Output Voltage Overshoot Turn-On Delay, Rising Vin Turn-On Delay, Rising ENA			200	300	mV	"	4, 5, 6
For a Neg. Step Change in Line Voltage  Turn-On Transient  Output Voltage Rise Time  Output Voltage Overshoot  Turn-On Delay, Rising Vin  Turn-On Delay, Rising ENA  Output Voltage Overshoot  See Notes 8 & 10  Turn-On Delay, Rising ENA  Turn-On Delay, Rising ENA  125  mV  +Vout = 0.5V to 4.5V; Full Resistive Load  Resistive load  See Notes 8 & 10  A See Notes 8 & 10						Vin step = 16V to 50V; CL=11µF on both outputs; see Note 7	
Turn-On Transient Output Voltage Rise Time Output Voltage Overshoot Output Voltage Overshoot Output Voltage Overshoot Turn-On Delay, Rising Vin Turn-On Delay, Rising ENA							4, 5, 6
Output Voltage Rise Time Output Voltage Overshoot Output Voltage Rise Time Output Voltage Overshoot Output Voltage Oversh		-125		125	mV_		4, 5, 6
Output Voltage Overshoot  Turn-On Delay, Rising Vin  Turn-On Delay, Rising ENA  Turn-On Delay, Rising ENA  O 2 % Resistive load  ENA = 5V; see Notes 8 & 10  See Note 10				10		A FIVE FAIR DE COLLEGE	4.5.6
Turn-On Delay, Rising Vin Turn-On Delay, Rising ENA  5.5 8.0 ms ENA = 5V; see Notes 8 & 10  4 See Note 10	Output Voltage Rise Time						4, 5, 6
Turn-On Delay, Rising ENA 3.0   6.0   ms   See Note 10							See Note 5
							4, 5, 6
	Restart Inhibit Time					See Note 10	4, 5, 6
Restart Inhibit Time 100 150 ms See Note 10 Short Circuit Start Time 12 14 20 ms Duration of pulse width, see Figure 22		12			me	Duration of pulse width, see Figure 22	4, 5, 6

Product # MQBL-28-05D



Output: ±5V

**Current: 4A Total** 

# **MQBL-28-05D ELECTRICAL CHARACTERISTICS (Continued)**

Parameter Parameter	Min.	Тур.	Max.	Units	Notes & Conditions Vin = 28V dc ±5%, +Iout = -Iout = 2A, CL = 0μF, free running (see Note 9) unless otherwise specified	Group A Subgroup (see Note 11)
EFFICIENCY						
Iout = 4 A (16 Vin)	83	87		%		1, 2, 3
Iout = 2 A (16 Vin)	82	86		%		1, 2, 3
Iout = 4 A (28 Vin)	81	85		%		1, 2, 3
Iout = 2 A (28 Vin)	80	83		%		1, 2, 3
Iout = 4 A (40 Vin)	81	84		%		1, 2, 3
Iout = 2 A (40 Vin)	77	81		%		1, 2, 3
Iout = 4 A (50 Vin)	77	81		%		1, 2, 3
Load Fault Power Dissipation		1.3		W	Sustained short circuit on output	
ISOLATION CHARACTERISTICS						
Isolation Voltage					Dielectric strength	
Input RTN to Output RTN	500			V		1
Any Input Pin to Case	500			V		1
Any Output Pin to Case	500			V		1
Isolation Resistance (in rtn to out rtn)	100			MΩ		1
Isolation Resistance (any pin to case)	100			ΜΩ		1
Isolation Capacitance (in rtn to out rtn)		22		nF		1
FEATURE CHARACTERISTICS						
Switching Frequency (free running)	500	550	600	kHz		1, 2, 3
Synchronization Input						
Frequency Range	500	_	700	kHz		1, 2, 3
Logic Level High	2.0		5.5	V		1, 2, 3
Logic Level Low	-0.5		0.8	V		1, 2, 3
Duty Cycle	20		80	%		See Note 5
Synchronization Output						
Pull Down Current	20		>	mA	VSYNC OUT = 0.8V	See Note 5
Duty Cycle	40		60	%	Output connected to SYNC IN of other MQBL unit	See Note 5
Enable Control (ENA)						
Off-State Voltage			0.8	V		1, 2, 3
Module Off Pulldown Current	80			μA	Current drain required to ensure module is off	See Note 5
On-State Voltage	2			V		1, 2, 3
Module On Pin Leakage Current			20	μA	Imax draw from pin allowed with module still on	See Note 5
Pull-Up Voltage	3.2	4.0	4.8	V	See Figure A	1, 2, 3
Output Voltage Trim Range	-10		10	%	See Figure E	1, 2, 3
RELIABILITY CHARACTERISTICS					<u> </u>	
Calculated MTBF (MIL-STD-217F2)						
GB @ Tcase = 70°C		2.5		10 <sup>6</sup> Hrs.		
AIF @ Tcase = 70°C		192		10 <sup>3</sup> Hrs.		
WEIGHT CHARACTERISTICS						
Device Weight		45		g		

#### **Electrical Characteristics Notes**

- 1. Converter will undergo input over-voltage shutdown.
- 2. Derate output power for continuous operation per Figure 5.
- 3. High or low state of input voltage must persist for about 200µs to be acted on by the shutdown circuitry.
- 4. Current limit inception is defined as the point where the output voltage has dropped to 90% of its nominal value. See Current Limit discussion in Features Description section.
- 5. Parameter not tested but guaranteed to the limit specified.
- 6. Load current transition time ≥ 10µs.
- 7. Line voltage transition time  $\geq 100 \mu s$ .
- 8. Input voltage rise time ≤ 250µs.
- 9. Operating the converter at a synchronization frequency above the free running frequency will cause the converter's efficiency to be slightly reduced and it may also cause a slight reduction in the maximum output current/power available. For more information consult the factory.
- 10. After a disable or fault event, module is inhibited from restarting for 100ms. See Shut Down section of the Control Features description.
- 11. Only the ES and HB grade products are tested at three temperatures. The C grade products are tested at one temperature. Please refer to the Construction and Environmental Stress Screening Options table for details.
- 12. These derating curves apply for the ES- and HB- grade products. The C- grade product has a maximum case temperature of 100°C.
- 13. The regulation stage operates to control the positive output. The negative output displays the cross regulation.
- 14. All +Vout and -Vout voltage measurements are made with Kelvin probes on the output leads.

Product # MQBL-28-05D Phone 1-888-567-9596 www.syngor.com Doc.# 005-0006202 Rev. 1 11/07/11 Page

Output: ±5V

**Current: 4A Total** 

## **BASIC OPERATION AND FEATURES**

The MQBL DC/DC converter uses a two-stage power conversion topology. The first, or regulation, stage is a buck-converter that keeps the output voltage constant over variations in line, load, and temperature. The second, or isolation, stage uses transformers to provide the functions of input/output isolation and voltage transformation to achieve the output voltage required.

In the dual output converter there are two secondary windings in the transformer of the isolation stage, one for each output. There is only one regulation stage, however, and it is used to control the positive output. The negative output therefore displays "Cross-Regulation", meaning that its output voltage depends on how much current is drawn from each output.

Both the positive and the negative outputs share a common OUTPUT RETURN pin.

Both the regulation and the isolation stages switch at a fixed frequency for predictable EMI performance. The isolation stage switches at one half the frequency of the regulation stage, but due to the push-pull nature of this stage it creates a ripple at double its switching frequency. As a result, both the input and the output of the converter have a fundamental ripple frequency of about 550 kHz in the free-running mode.

Rectification of the isolation stage's output is accomplished with synchronous rectifiers. These devices, which are MOSFETs with a very low resistance, dissipate far less energy than would Schottky diodes. This is the primary reason why the MQBL converters have such high efficiency, particularly at low output voltages.

Besides improving efficiency, the synchronous rectifiers permit operation down to zero load current. There is no longer a need for a minimum load, as is typical for converters that use diodes for rectification. The synchronous rectifiers actually permit a negative load current to flow back into the converter's output terminals if the load is a source of short or long term energy. The MQBL converters employ a "backdrive current limit" to keep this negative output terminal current small.

There is a control circuit in the MQBL converter that determines the conduction state of the power switches. It communicates across the isolation barrier through a magnetically coupled device. No opto-isolators are used.

An input under-voltage shutdown feature with hysteresis is provided, as well as an input over-voltage shutdown and an

output over-voltage limit. There is also an output current limit that is nearly constant as the load impedance decreases (i.e., there is not fold-back or fold-forward characteristic to the output current under this condition). When a load fault is removed, the output voltage rises exponentially to its nominal value without an overshoot. If a load fault pulls the output voltage below about 60% of nominal, the converter will shut down to attempt to clear the load fault. After a short delay it will try to auto-restart.

The MQBL converter's control circuit does not implement an over-temperature shutdown.

The following sections describe the use and operation of additional control features provided by the MQBL converter.

## **CONTROL FEATURES**

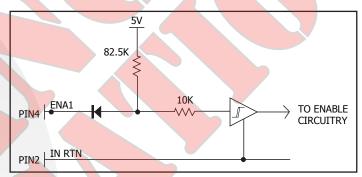


Figure A: Circuit diagram shown for reference only, actual circuit components may differ from values shown for equivalent circuit.

**ENABLE:** The MQBL converter has one enable pin, ENA1 (pin 4), which is referenced with respect to the converter's input return (pin 2). It must have a logic high level for the converter to be enabled; a logic low inhibits the converter.

The enable pin is internally pulled high so that an open connection will enable the converter. Figure A shows the equivalent circuit looking into the enable pin. It is TTL compatible and has hysteresis.

**SHUT DOWN:** The MQBL converter will shut down in response to only five conditions: ENA input low, VIN input below under-voltage shutdown threshold, VIN input above over-voltage shutdown threshold, output voltage below the output under-voltage threshold, and output voltage above the output over-voltage threshold. Following any shutdown event, there is a startup inhibit delay which will prevent the converter from restarting for approximately 100ms. After the 100ms delay elapses, if the enable inputs are high and the input voltage is within the operating range, the converter

Output: ±5V

**Current: 4A Total** 

will restart. If the VIN input is brought down to nearly 0V and back into the operating range, there is no startup inhibit, and the output voltage will rise according to the "Turn-On Delay, Rising Vin" specification.

**SYNCHRONIZATION:** The MQBL converter's switching frequency can be synchronized to an external frequency source that is in the 500 kHz to 700 kHz range. A pulse train at the desired frequency should be applied to the SYNC IN pin (pin 6) with respect to the INPUT RETURN (pin 2). This pulse train should have a duty cycle in the 20% to 80% range. Its low value should be below 0.8V to be guaranteed to be interpreted as a logic low, and its high value should be above 2.0V to be guaranteed to be interpreted as a logic high. The transition time between the two states should be less than 300ns.

If the MQBL converter is not to be synchronized, the SYNC IN pin should be left open circuit. The converter will then operate in its free-running mode at a frequency of approximately 550 kHz.

If, due to a fault, the SYNC IN pin is held in either a logic low or logic high state continuously, or the SYNC IN frequency is outside the 500-700 kHz range, the MQBL converter will revert to its free-running frequency.

The MQBL converter also has a SYNC OUT pin (pin 5). This output can be used to drive the SYNC IN pins of as many as ten (10) other MQBL converters. The pulse train coming out of SYNC OUT has a duty cycle of 50% and a frequency that matches the switching frequency of the converter with which it is associated. This frequency is either the free-running frequency if there is no valid synchronization signal at the SYNC IN pin, or the synchronization frequency if there is.

The synchronization feature is entirely compatible with that of SynQor's MQFL family of converters.

Figure B shows the equivalent circuit looking into the SYNC IN pin and Figure C shows the equivalent circuit looking into the SYNC OUT pin.

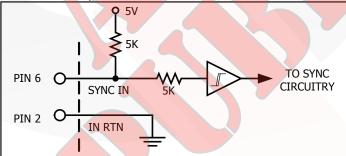


Figure B: Equivalent circuit looking into the SYNC IN pin with respect to the IN RTN (input return) pin.

**OUTPUT VOLTAGE TRIM:** If desired, it is possible to increase or decrease the MQBL dual converter's output voltage from its nominal value. To increase the output voltage a resistor, Rtrim up, should be connected between TRIM pin (pin 10) and the OUTPUT RETURN pin (pin 8), as shown in Figure D. The value of this resistor should be determined according to the following equation of from Figure E:

Rtrim up(
$$\Omega$$
) =  $\frac{6000\Omega*Vnom}{Vout - Vnom}$  -  $30000\Omega$ 

where:

Vnom = the converter's nominal output voltage, Vout = the desired output voltage (greater than Vnom), and Rtrim up is in Ohms.

As the output voltage is trimmed up, it produces a greater voltage stress on the converter's internal components and may cause the converter to fail to deliver the desired output voltage at the low end of the input voltage range at the higher end of the load current and temperature range. Please consult the factory for details. To trim the output voltage below its nominal value, connect an external resistor (Rtrim down) between the TRIM pin and the POSITIVE OUTPUT pin (pin 7), and another resistor (Rtrim sense) connected between the TRIM pin and the OUTPUT RETURN pin as shown in Figure D. The values of these trim down resistors should be chosen according to the following equation or from Figure E:

Rtrim down(
$$\Omega$$
) = 
$$\frac{15100\Omega*Vout - 6000\Omega*Vnom}{Vnom - Vout} - 30000\Omega$$

where:

Rtrim down and Rtrim sense are in Ohms.

Factory trimmed converters are available by request.

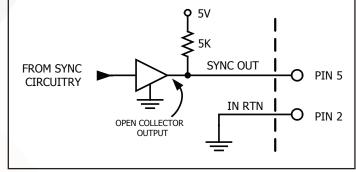


Figure C: Equivalent circuit looking into SYNC OUT pin with respect to the IN RTN (input return) pin.



Output: ±5V
Current: 4A Total

**INPUT UNDER-VOLTAGE SHUTDOWN:** The MQBL converter has an under-voltage shutdown feature that ensures the converter will be off if the input voltage is too low. The input voltage turn-on threshold is higher than the turn-off threshold. In addition, the MQBL converter will not respond to a state of the input voltage unless it has remained in that state for more than about 200µs. This hysteresis and the delay ensure proper operation when the source impedance is high or in a noisy environment.

**INPUT OVER-VOLTAGE SHUTDOWN:** The MQBL converter also has an over-voltage feature that ensures the converter will be off if the input voltage is too high. It also has a hysteresis and time delay to ensure proper operation.

**OUTPUT OVER-VOLTAGE SHUTDOWN:** The MQBL converter will shut down if the voltage at its power output pins ever exceeds about 130% of the nominal value. The shutdown threshold does not change with output trim or sense drops; excessive trim-up or output wiring drops may cause an output over-voltage shutdown event. After a startup inhibit delay, the converter will attempt to restart.

**OUTPUT UNDER-VOLTAGE SHUTDOWN:** The MQBL converter will also shut down if the voltage at its power output pins ever dips below 60% of the nominal value for more than a few milliseconds. Output voltage reduction due to output current overload (current limit) is the most common trigger for this shutdown. The shutdown threshold does not change with output trim but at only 10%, trimdown should not trigger this event. After a startup inhibit delay, the converter will attempt to restart. This shutdown is disabled during startup.

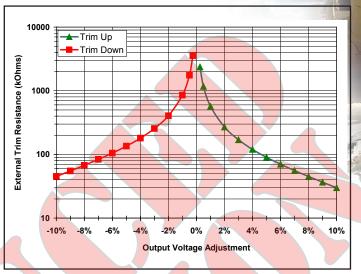


Figure E: Trim up and Trim down as a function of external trim resistance.

**BACK-DRIVE CURRENT LIMIT:** Converters that use MOSFETs as synchronous rectifiers are capable of drawing a negative current from the load if the load is a source of short- or long-term energy. This negative current is referred to as a "back-drive current".

Conditions where back-drive current might occur include paralleled converters that do not employ current sharing. It can also occur when converters having different output voltages are connected together through either explicit or parasitic diodes that, while normally off, become conductive during startup or shutdown. Finally, some loads, such as motors, can return energy to their power rail. Even a load capacitor is a source of back-drive energy for some period of time during a shutdown transient.

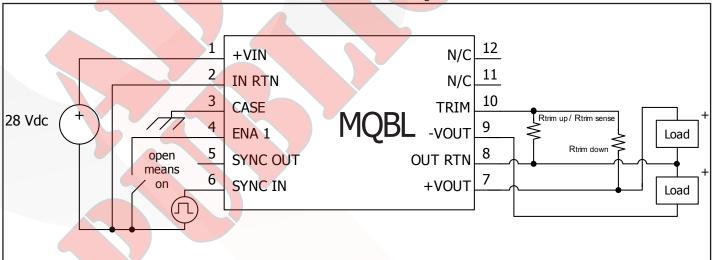


Figure D: Typical connection for output voltage trimming.

Output: ±5V

**Current: 4A Total** 

To avoid any problems that might arise due to back-drive current, the MQBL converters limit the negative current that the converter can draw from its output terminals. The threshold for this back-drive current limit is placed sufficiently below zero so that the converter may operate properly down to zero load, but its absolute value (see the Electrical Characteristics page) is small compared to the converter's rated output current.

**CURRENT LIMIT:** In the event of excess load, the MQBL converter will quickly reduce its output voltage to keep the load current within safe limits (see Figure 12). If the overload persists for more than 14 milliseconds, the converter will shut off, wait a restart delay, and then automatically attempt to re-start. The timeout is internally implemented with an integrator: counting up whenever current limit is active, and counting down at 1/5th the rate whenever current limit becomes inactive. In this way a series of short-duration overloads will not cause the converter to shut down, while it will shut down in response to sustained overloads.

**THERMAL CONSIDERTAIONS:** Figure 11 shows the suggested Power Derating Curves for this converter as a function of the case temperature and the maximum desired power MOSFET junction temperature. All other components within the converter are cooler than its hottest MOSFET, which at full power is no more than 20°C higher than the case temperature directly below this MOSFET.

The Mil-HDBK-1547A component derating guideline calls for a maximum component temperature of 105°C. Figure 11 therefore has one power derating curve that ensures this limit is maintained. It has been SynQor's extensive experience that reliable long-term converter operation can be achieved with a maximum component temperature of 125°C. In extreme cases, a maximum temperature of 145°C is permissible, but not recommended for long-term operation where high reliability is required. Derating curves for these higher temperature limits are also included in Figure 11. The maximum case temperature at which the converter should be operated is 135°C.

When the converter is mounted on a metal plate, the plate will help to make the converter's case bottom a uniform temperature. How well it does so depends on the thickness of the plate and on the thermal conductance of the interface layer (e.g. thermal grease, thermal pad, etc.) between the case and the plate. Unless this is done very well, it is important not to mistake the plate's temperature for the maximum case temperature. It is easy for them to be as much as 5-10°C different at full power and at high temperatures. It is suggested that a thermocouple be attached directly to the converter's case through a small hole in the plate when investigating how hot the converter is getting. Care must also be made to ensure that there is not a large thermal resistance between the thermocouple and the case due to whatever adhesive might be used to hold the thermocouple in place.

**INPUT SYSTEM INSTABILITY:** This condition can occur because any dc-dc converter appears incrementally as a negative resistance load. A detailed application note titled "Input System Instability" is available on the SynQor website which provides an understanding of why this instability arises, and shows the preferred solution for correcting it.

Output: ±5V

**Current: 4A Total** 

#### CONSTRUCTION AND ENVIRONMENTAL STRESS SCREENING OPTIONS

Screening	Consistent with MIL-STD-883F	C-Grade (-40 °C to +100 °C)	ES-Grade (-55 °C to +125 °C) (Element Evaluation)	HB-Grade (-55 °C to +125 °C) (Element Evaluation)					
Internal Visual	*	Yes	Yes	Yes					
Temperature Cycle	Method 1010	No	Condition B (-55 °C to +125 °C)	Condition C (-65 °C to +150 °C)					
Constant Acceleration	Method 2001 (Y1 Direction)	No	500g	Condition A (5000g)					
Burn-in	Method 1015	24 Hrs @ +125 °C	96 Hrs @ +125 °C	160 Hrs @ +125 °C					
Final Electrical Test	Method 5005 (Group A)	+25 °C	-45, +25, +100 °C	-55, +25, +125 °C					
Mechanical Seal, Thermal, and Coating Process		Full QorSeal	Full QorSeal	Full QorSeal					
External Visual	2009	*	Yes	Yes					
Construction Process		QorSeal	QorSeal	QorSeal					
	* Per IPC-A-610 Class 3								

MilQor converters and filters are offered in three variations of environmental stress screening options. All MilQor converters use SynQor's proprietary QorSeal™ Hi-Rel assembly process that includes a Parylene-C coating of the circuit, a high performance thermal compound filler, and a nickel barrier gold plated aluminum case. Each successively higher grade has more stringent mechanical and electrical testing, as well as a longer burn-in cycle. The ES- and HB-Grades are also constructed of components that have been procured through an element evaluation process that pre-qualifies each new batch of devices.

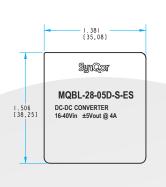
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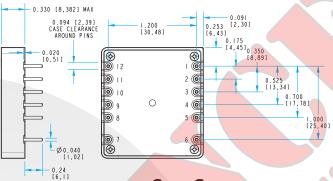


Output: ±5V

**Current: 4A Total** 



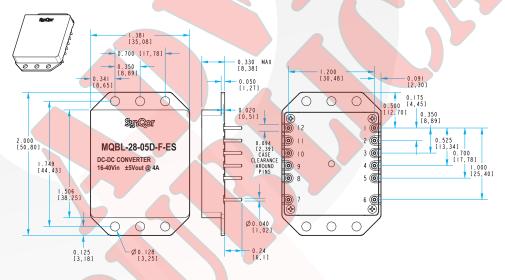




Case S

## PIN DESIGNATIONS

Pin #	Function
1	Positive input
2	Input return
3	Case
4	Enable 1
5	Sync output
6	Sync input
7	Positive output
8	Output return
9	Negative output
10	Trim
11	No connection
12	No connection



## Case F

#### **NOTES**

1)Pins: Diameter: 0.040" (1.02mm)

Material: Copper Alloy

Finish: Gold over Nickel plate

with Sn/Pb solder dip

2)Case: Material: Aluminum

Finish: Gold over Nickel plate
3)All dimensions are in inches (mm)
Tolerances: x.xx": +/-0.02"

nces: x.xx": +/-0.02" (x.xmm: +/-0.5mm) x.xxx": +/-0.010" (x.xxmm +/-

0.25mm)

4)Weight:

Standard: 1.17oz (33.3g) Flanged: 1.24oz (35.1g)

5)The flanged version can be mounted using the 2 center holes or the 4

outer holes

6)Workmanship: Meets or exceeds

IPC-A-610C Class III

Output: ±5V

**Current: 4A Total** 

## **MilQor Converter FAMILY MATRIX**

The tables below show the array of MilQor converters available. When ordering SynQor converters, please ensure that you use the complete part number according to the table in the last page. Contact the factory for other requirements.

		Single Output									
	1.5V	1.8V	2.5V	3.3V	5V	6V	7.5V	9V	12V	15V	28V
	(1R5S)	(1R8S)	(2R5S)	(3R3S)	(05S)	(06S)	(7R5S)	(09S)	(12S)	(15S)	(28S)
MQHL-28											
16-40Vin Cont.	20A	20A	20A	15A	10A	8A	6.6A	5.5A	4A	3.3A	1.8A
16-50Vin 1s Trans.*	20A	20A	20A	IDA	IUA	l <sup>oA</sup>	0.0A	5.5A	4A	3.3A	1.0A
Absolute Max Vin = 60V											
MQHL-28E											
16-70Vin Cont.	20A 20A	204	20A 20A	15A	10A	8A	6.6A	5.5A	4A	3.3A	1.8A
16-80Vin 1s Trans.*		20A   20A	20A	204	15/4	IUA	0A	0.0A 3.5A		3.3A	1.0A
Absolute Max Vin =100V											
MQHR-28											
16-40Vin Cont.	10A	10A	10A	7.5A	5A	4A	3.3A	2.75A	2A	1.65A	0.9A
16-50Vin 1s Trans.*	104	104	104	7.5A	JA	4//	J.JA	2.13A	ZA	1.03A	0.5A
Absolute Max Vin = 60V											
MQHR-28E											
16-70Vin Cont.	10A	10A	10A	7.5A	5A	4A	3.3A	2.75A	2A	1.65A	0.9A
16-80Vin 1s Trans.*	IUA	IUA	IUA	7.5A	3A	4/4	3.3A	2.13A	-ZA	1.05A	U.SA
Absolute Max Vin = 100V											

Dua	al Outp	ut †
5V	12V	15V
(05D)	(12D)	(15D)
10A	4A	3.3A
Total	Total	Total
10A	4A	3.3A
Total	Total	Total
5A	2A	1.65A
Total	Total	Total
5A	2A	1.65A
Total	Total	Total

Single Output											
	1.5V	1.8V	2.5V	3.3V	5V	6V	7.5V	9V	12V	15V	28V
	(1R5S)	(1R8S)	(2R5S)	(3R3S)	(05S)	(06S)	(7R5S)	(09S)	(12S)	(15S)	(28S)
MQBL-28			<u> </u>			5					
16-50Vin 1s Trans.*  Absolute Max Vin = 60V	8A	8A	8A	6A	4A	3.3A	2.6A	2.2A	1.6A	1.3A	0.7A
MQBL-28E 16-70Vin Cont. 16-80Vin 1s Trans.* Absolute Max Vin =100V	8A	8A	8A	6A	4A	3.3A	2.6A	2.2A	1.6A	1.3A	0.7A

Dual Output †										
5V	12V	15V								
(05D)	(12D)	(15D)								
4A Total	1.6A Total	1.3A Total								
4A Total	1.6A Total	1.3A Total								

Product # MQBL-28-05D Phone 1-888-567-9596 www.synqor.com Doc.# 005-0006202 Rev. 1 11/07/11

Check with factory for availability.
†80% of total output current available on any one output.
\*Converters may be operated at the highest transient input voltage, but some component electrical and thermal stresses would be beyond MIL-HDBK-1547A guidelines.

Output: ±5V

**Current: 4A Total** 

#### **PART NUMBERING SYSTEM**

The part numbering system for SynQor's MilQor DC-DC converters follows the format shown in the table below.

Not all combinations make valid part numbers, please contact SynQor for availability. See the Product Summary web page for more options.

**Example: MQBL-28-05D-F-ES** 

Model	Input	Output V	oltage(s)	Package Outline/	Screening
Name	Voltage Range	Single Output	Dual Output	Pin Configuration	Grade
MQBL	28 28E	1R5S 1R8S 2R5S 3R3S 05S 06S 7R5S 09S 12S 15S 28S	05D 12D 15D	SF	C ES HB

#### **APPLICATION NOTES**

A variety of application notes and technical white papers can be downloaded in pdf format from the SynQor website.

## **PATENTS**

SynQor holds the following U.S. patents, one or more of which apply to each product listed in this document. Additional patent applications may be pending or filed in the future.

5,999,417	6,222,742	6,545,890	6,577,109	6,594,159	6,731,520	6,894,468
6,896,526	6,927,987	7,050,309	7,072,190	7,085,146	7,119,524	7,269,034
7,272,021	7,272,023	7,558,083	7,564,702	7,765,687	7,787,261	8,023,290

## Contact SynQor for further information:

Phone: 978-849-0600 Toll Free: 1-888-567-9596 Fax: 978-849-0602

E-mail: mgnbofae@syngor.com

Web: www.synqor.com
Address: 155 Swanson Road

Boxborough, MA 01719

USA

<u>Warranty</u>

SynQor offers a two (2) year limited warranty. Complete warranty information is listed on our website or is available upon request from SynQor.

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