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April 1st, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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RENESAS BCR8CS-12LA

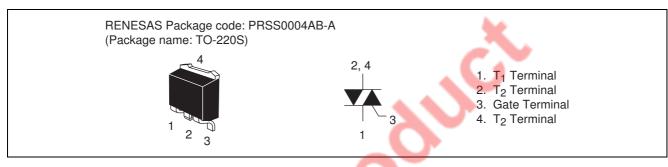
Triac Medium Power Use

> REJ03G0338-0300 Rev.3.00 Nov 30, 2007

Features

- $I_{T (RMS)}$: 8 A
- V_{DRM}: 600 V
- I_{FGTI} , I_{RGTI} , $I_{RGT III}$: 30 mA (20 mA)^{Note6}

Outline



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Non-Insulated Type

Planar Passivation Type

Applications

Solid state relay, hybrid IC

Maximum Ratings

Parameter	Symbol	Voltage class	Unit
Repetitive peak off-state voltage Note1	V _{DRM}	600	V
Non-repetitive peak off-state voltage ^{Note1}	V _{DSM}	720	V

BCR8CS-12LA

Parameter	Symbol	Ratings	Unit	Conditions
RMS on-state current	I _{T (RMS)}	8	A	Commercial frequency, sine full wave 360° conduction, Tc = $105^{\circ}C^{Note3}$
Surge on-state current	I _{TSM}	80	A	60Hz sinewave 1 full cycle, peak value, non-repetitive
I ² t for fusing	l ² t	26	A ² s	Value corresponding to 1 cycle of half wave 60Hz, surge on-state current
Peak gate power dissipation	P _{GM}	5	W	
Average gate power dissipation	P _{G (AV)}	0.5	W	
Peak gate voltage	V_{GM}	10	V	
Peak gate current	I _{GM}	2	А	
Junction temperature	Tj	- 40 to +125	°C	
Storage temperature	Tstg	- 40 to +125	°C	
Mass	_	1.2	g	Typical value

Notes: 1. Gate open.

Electrical Characteristics

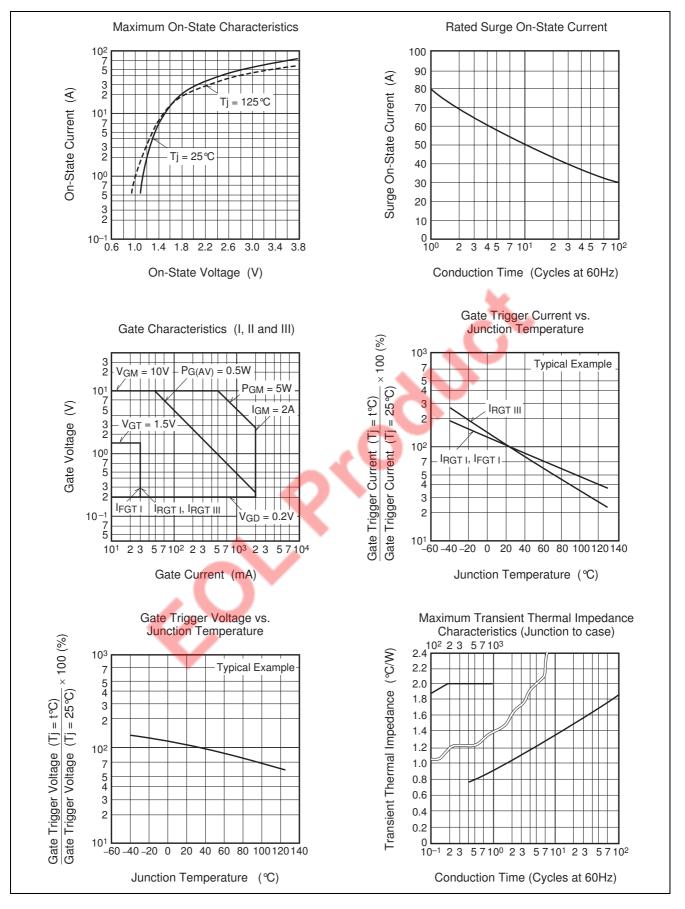
Parameter		Symbol	Min.	Тур.	Max.	Unit	Test conditions
Repetitive peak off-state current		I _{DRM}	_	—	2.0	mA	Tj = 125°C, V_{DRM} applied
On-state voltage		V _{TM}	_	—	1.5	V	$Tc = 25^{\circ}C$, $I_{TM} = 12 A$, Instantaneous measurement
Gate trigger voltage ^{Note2}	Ι	V_{FGTI}		—	1.5	V	$Tj=25^{\circ}C,V_{D}=6~V,R_{L}=6~\Omega,$
	II	V _{RGTI}	-	—	1.5	V	$R_G = 330 \Omega$
	III	V _{RGTIII}		—	1.5	V	
Gate trigger current ^{Note2}	Ι	I _{FGTI}	_	-	30 ^{Note6}	mA	$Tj = 25^{\circ}C, V_D = 6 V, R_L = 6 \Omega,$
	II	I _{RGTI}	_	-	30 ^{Note6}	mA	$R_G = 330 \Omega$
	III	I _{RGTIII}	_	4	30 ^{Note6}	mA	
Gate non-trigger voltage		V_{GD}	0.2		—	V	$Tj = 125^{\circ}C, V_{D} = 1/2 V_{DRM}$
Thermal resistance		R _{th (j-c)}		_	2.0	°C/W	Junction to case ^{Note3 Note4}
Critical-rate of rise of off-sta commutating voltage ^{Note5}	te	(dv/dt)c	10	-	—	V/µs	Tj = 125°C

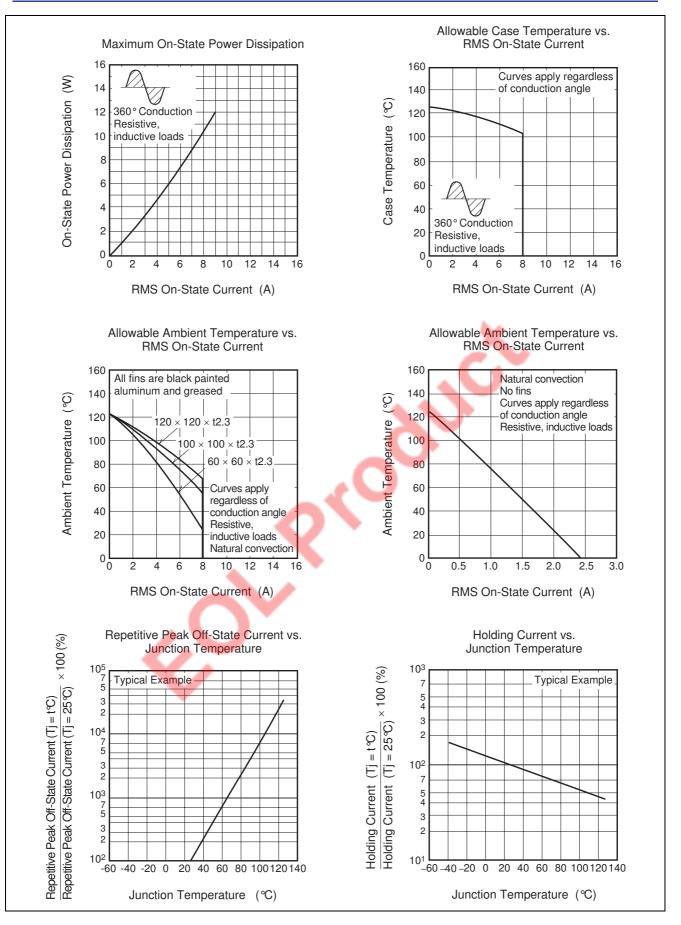
Notes: 2. Measurement using the gate trigger characteristics measurement circuit.

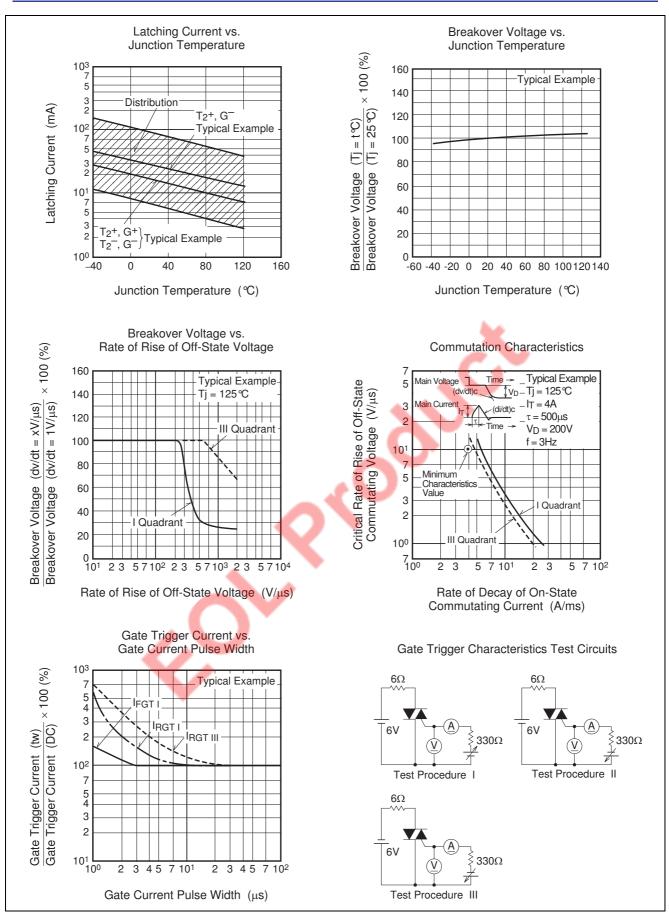
- 3. Case temperature is measured on the T_2 tab.
- 4. The contact thermal resistance $R_{th (c-f)}$ in case of greasing is 1.0 °C/W.
- 5. Test conditions of the critical-rate of rise of off-state commutating voltage is shown in the table below.
- 6. High sensitivity ($I_{GT} \le 20$ mA) is also available. (I_{GT} item: 1)

Test conditions	Commutating voltage and current waveforms (inductive load)
1. Junction temperature Tj = 125°C	Supply Voltage — → Time
 Rate of decay of on-state commutating current	Main Current
(di/dt)c = - 4.0 A/ms	→ Time
3. Peak off-state voltage	Main Voltage Time
V _D = 400 V	(dv/dt)c V _D

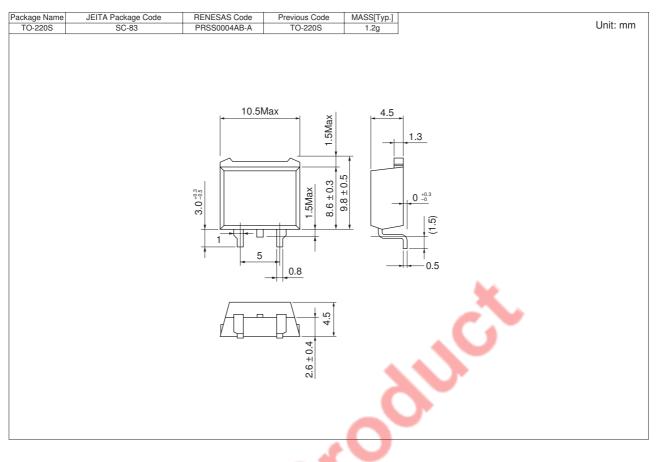
Performance Curves







Package Dimensions



Order Code

Lead form	Standard packing	Quantity	Standard order code	Standard order code example
Surface-mounted type	Taping 💦 🔷	1000	Type name – T +Direction (1 or 2) +1	BCR8CS-12LA-T11
Surface-mounted type	Plastic Magazine	50	Type name	BCR8CS-12LA
	(Tube)			

Note : Please confirm the specification about the shipping in detail.

RenesasTechnology Corp. sales Strategic Planning Div. Nippon Bldg., 2-6-2, Ohte-machi, Chiyoda-ku, Tokyo 100-0004, Japan

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Renesas Technology America, Inc 450 Holger Way, San Jose, CA 95134-1368, U.S.A Tel: <1> (408) 382-7500, Fax: <1> (408) 382-7501

Renesas Technology Europe Limited Dukes Meadow, Millboard Road, Bourne End, Buckinghamshire, SL8 5FH, U.K. Tel: <44> (1628) 585-100, Fax: <44> (1628) 585-900

Renesas Technology (Shanghai) Co., Ltd. Unit 204, 205, AZIACenter, No.1233 Lujiazui Ring Rd, Pudong District, Shanghai, China 200120 Tel: <86> (21) 5877-1818, Fax: <86> (21) 6887-7858/7898

Renesas Technology Hong Kong Ltd. 7th Floor, North Tower, World Finance Centre, Harbour City, Canton Road, Tsimshatsui, Kowloon, Hong Kong Tel: <852> 2265-6688, Fax: <852> 2377-3473

Renesas Technology Taiwan Co., Ltd. 10th Floor, No.99, Fushing North Road, Taipei, Taiwan Tel: <886> (2) 2715-2888, Fax: <886> (2) 3518-3399

Renesas Technology Singapore Pte. Ltd.

1 Harbour Front Avenue, #06-10, Keppel Bay Tower, Singapore 098632 Tel: <65> 6213-0200, Fax: <65> 6278-8001

Renesas Technology Korea Co., Ltd. Kukje Center Bldg. 18th Fl., 191, 2-ka, Hangang-ro, Yongsan-ku, Seoul 140-702, Korea Tel: <82> (2) 796-3115, Fax: <82> (2) 796-2145

Renesas Technology Malaysia Sdn. Bhd Unit 906, Block B, Menara Amcorp, Amcorp Trade Centre, No.18, Jln Persiaran Barat, 46050 Petaling Jaya, Selangor Darul Ehsan, Malaysia Tel: <603> 7955-9390, Fax: <603> 7955-9510