To our customers,

Old Company Name in Catalogs and Other Documents

On April 1st, 2010, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

Renesas Electronics website: http://www.renesas.com

April 1st, 2010
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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H5N2001LD, H5N2001LS, H5N2001LM

Silicon N Channel MOS FET
High Speed Power Switching

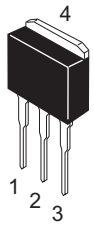
REJ03G1339-0600
Rev.6.00
Jul 14, 2006

Features

- Low on-resistance
- Low leakage current
- High speed switching

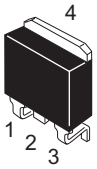
Outline

RENESAS Package code: PRSS0004AE-A
(Package name: LDPAK (L))



H5N2001LD

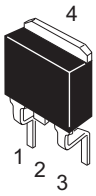
RENESAS Package code: PRSS0004AE-B
(Package name: LDPAK (S)-(1))



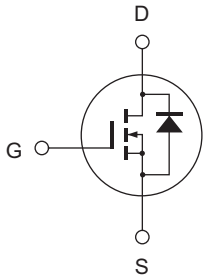
H5N2001LS

1. Gate
2. Drain
3. Source
4. Drain

RENESAS Package code: PRSS0004AE-C
(Package name: LDPAK (S)-(2))



H5N2001LM



Absolute Maximum Ratings

(Ta = 25°C)

Item	Symbol	Ratings	Unit
Drain to source voltage	V_{DSS}	200	V
Gate to source voltage	V_{GSS}	±30	V
Drain current	I_D	20	A
Drain peak current	$I_{D\ (pulse)}$ Note 1	80	A
Body to drain diode reverse drain current	I_{DR}	20	A
Body to drain diode reverse drain peak current	$I_{DR\ (pulse)}$ Note 1	80	A
Avalanche current	I_{AP} Note 3	20	A
Channel dissipation	Pch Note 2	75	W
Channel to case Thermal Impedance	θ ch-c	1.67	°C/W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	–55 to +150	°C

Notes: 1. PW ≤ 10 µs, duty cycle ≤ 1%
2. Value at Tc = 25°C
3. Tch ≤ 150°C

Electrical Characteristics

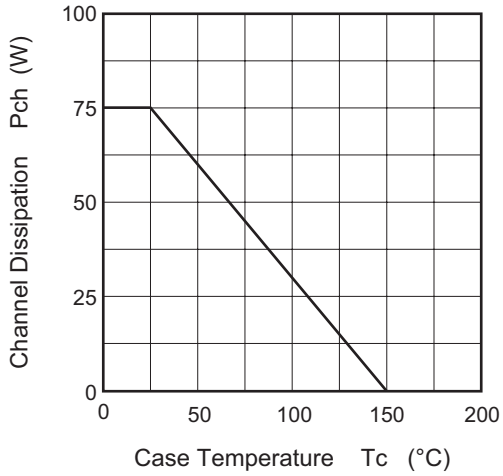
(Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test Conditions
Drain to source breakdown voltage	$V_{(BR)\ DSS}$	200	—	—	V	I_D = 10 mA, V_{GS} = 0
Gate to source leak current	I_{GSS}	—	—	±0.1	µA	V_{GS} = ±30 V, V_{DS} = 0
Zero gate voltage drain current	I_{DSS}	—	—	1	µA	V_{DS} = 200 V, V_{GS} = 0
Gate to source cutoff voltage	$V_{GS\ (off)}$	3.0	—	4.5	V	I_D = 1 mA, V_{DS} = 10 V
Static drain to source on state resistance	$R_{DS\ (on)}$	—	0.100	0.125	Ω	I_D = 10 A, V_{GS} = 10 V Note 4
Forward transfer admittance	$\|y_{fs}\|$	8	14	—	S	I_D = 10 A, V_{DS} = 10 V Note 4
Input capacitance	Ciss	—	1350	—	pF	V_{DS} = 25 V
Output capacitance	Coss	—	180	—	pF	V_{GS} = 0
Reverse transfer capacitance	Crss	—	55	—	pF	f = 1 MHz
Turn-on delay time	$t_{d\ (on)}$	—	35	—	ns	I_D = 10 A
Rise time	t_r	—	70	—	ns	R_L = 10 Ω
Turn-off delay time	$t_{d\ (off)}$	—	85	—	ns	V_{GS} = 10 V
Fall time	t_f	—	20	—	ns	Rg = 10 Ω
Total gate charge	Qg	—	44	—	nC	V_{DD} = 160 V
Gate to source charge	Qgs	—	8	—	nC	V_{GS} = 10 V
Gate to drain charge	Qgd	—	22	—	nC	I_D = 20 A
Body to drain diode forward voltage	V_{DF}	—	0.9	1.4	V	I_F = 20 A, V_{GS} = 0 Note4
Body to drain diode reverse recovery time	t_{rr}	—	140	—	ns	I_F = 20 A, V_{GS} = 0
Body to drain diode reverse recovery charge	Qrr	—	0.7	—	µC	di_F/dt = 100 A/µs

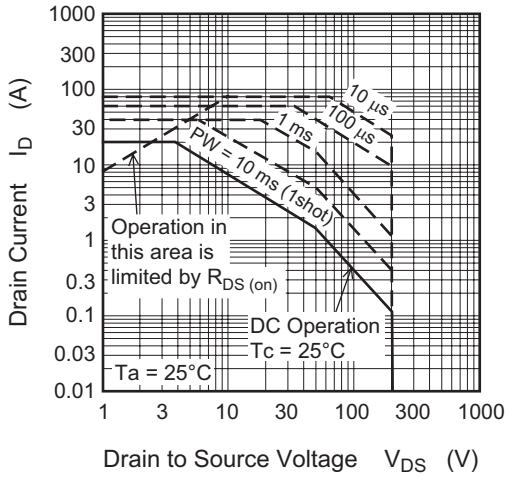
Note: 4. Pulse test

Main Characteristics

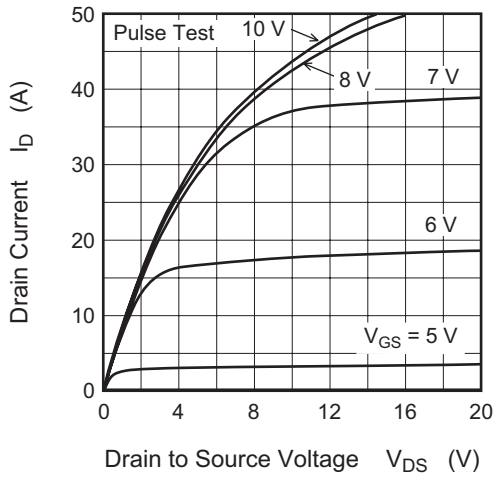
Power vs. Temperature Derating



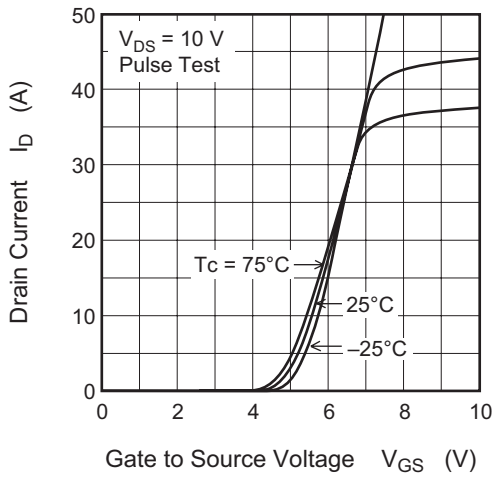
Maximum Safe Operation Area



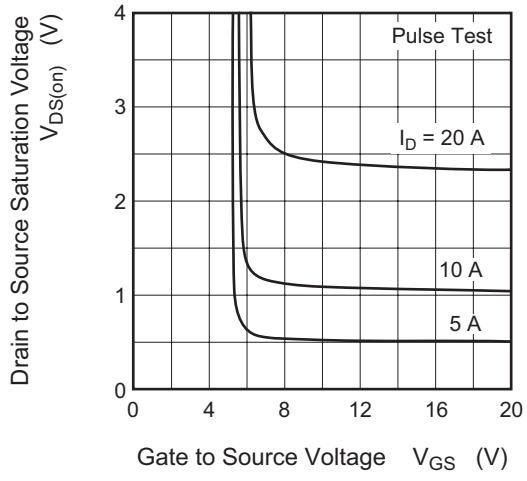
Typical Output Characteristics



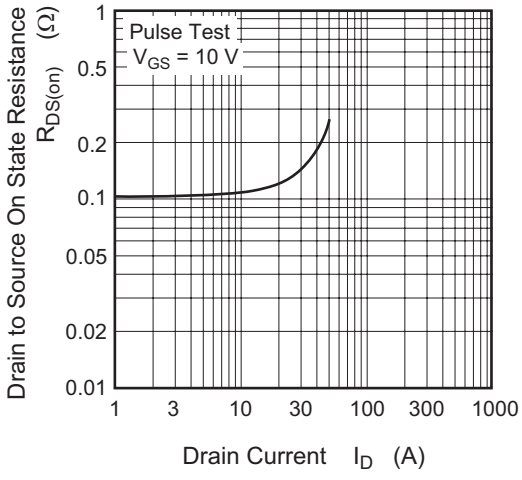
Typical Transfer Characteristics



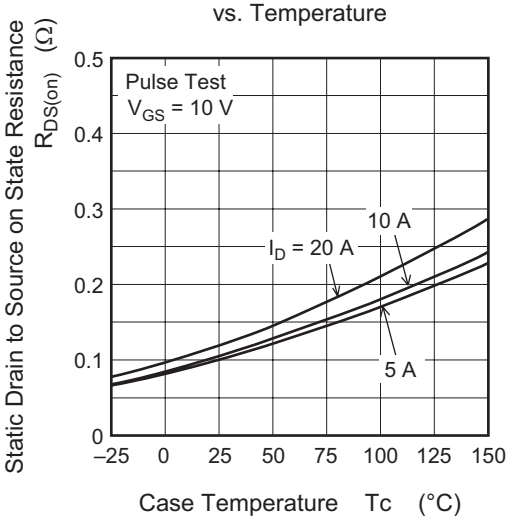
Drain to Source Saturation Voltage vs. Gate to Source Voltage



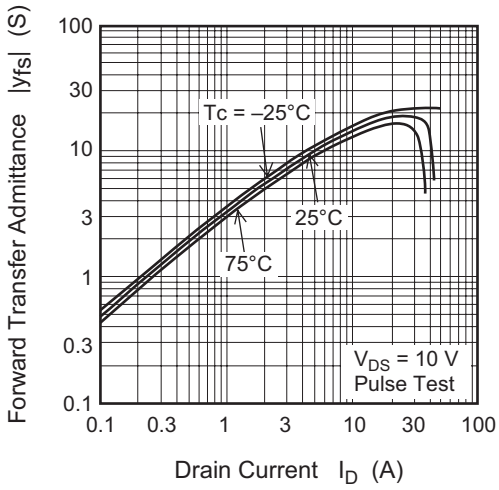
Static Drain to Source on State Resistance vs. Drain Current



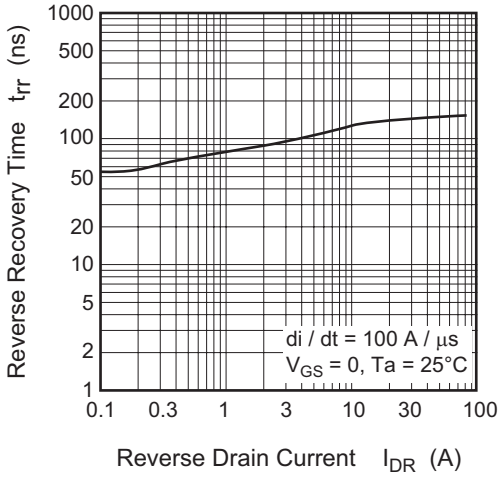
Static Drain to Source on State Resistance vs. Temperature



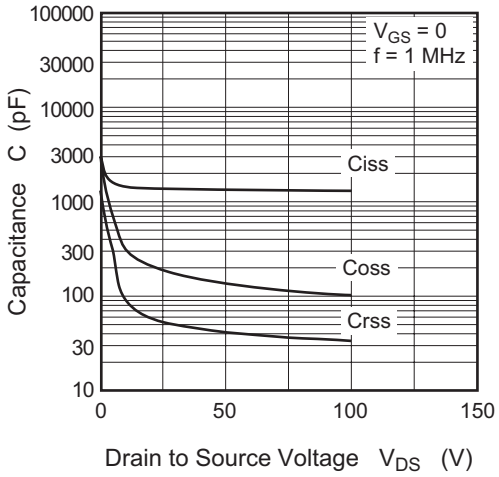
Forward Transfer Admittance vs. Drain Current



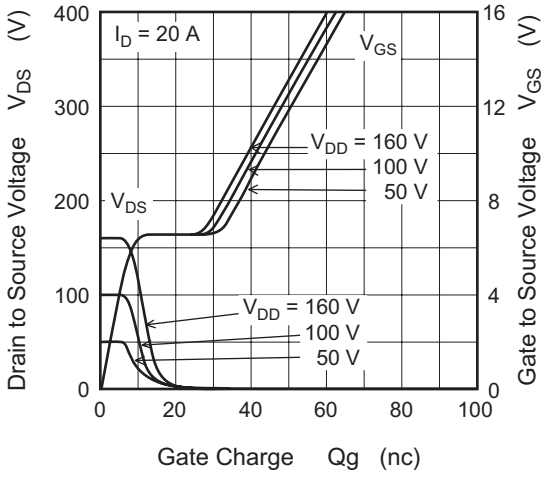
Body to Drain Diode Reverse Recovery Time



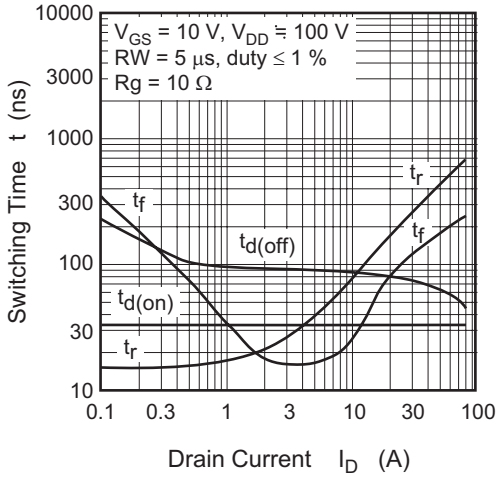
Typical Capacitance vs. Drain to Source Voltage



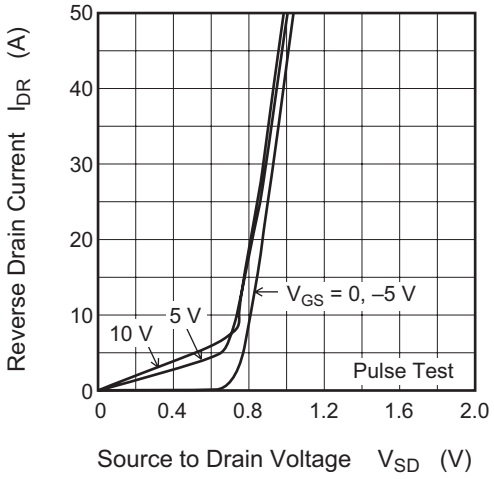
Dynamic Input Characteristics



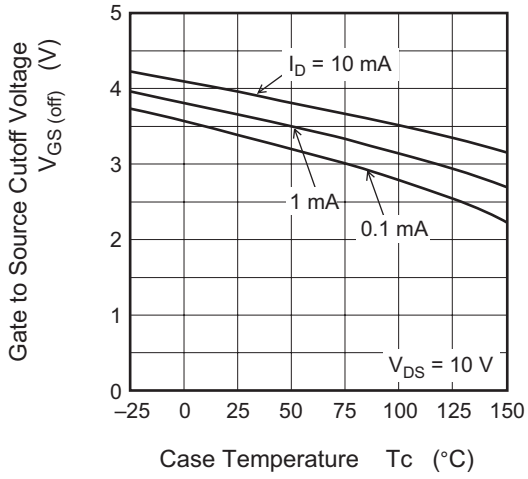
Switching Characteristics



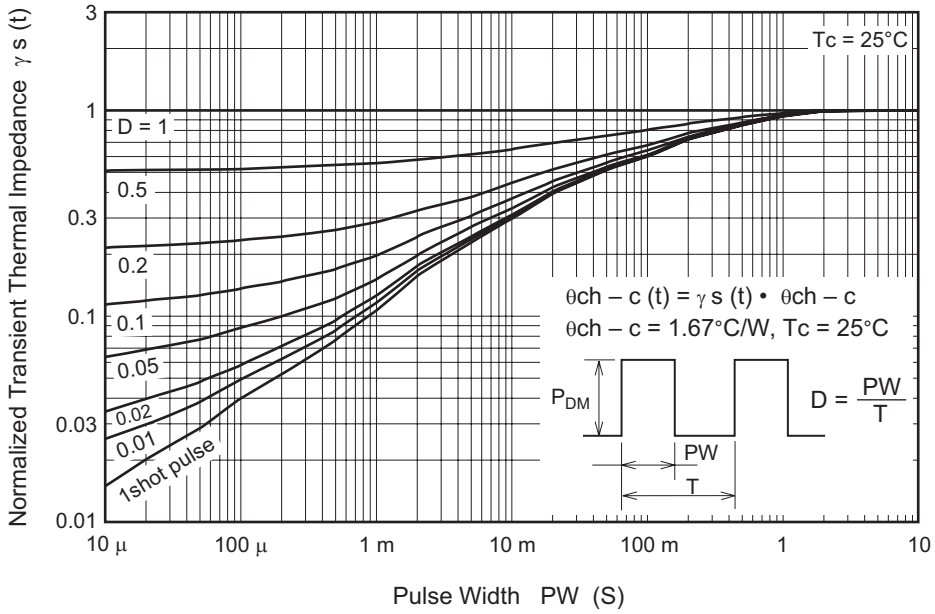
Reverse Drain Current vs.
Souece to Drain Voltage



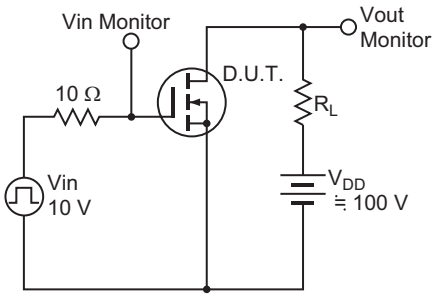
Gate to Source Cutoff Voltage vs.
Case Temperature



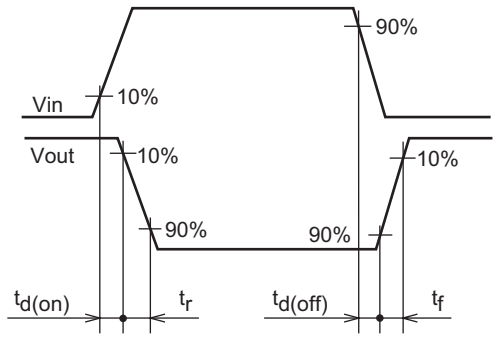
Normalized Transient Thermal Impedance vs. Pulse Width



Switching Time Test Circuit



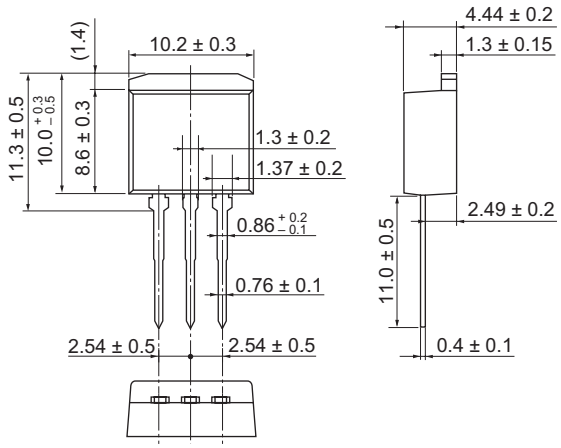
Switching Time Waveform



Package Dimensions

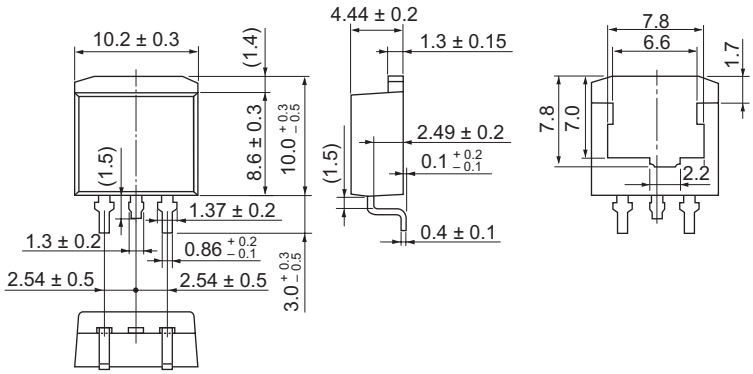
Package Name	JEITA Package Code	RENESAS Code	Previous Code	MASS[Typ.]
LDPAK(L)	—	PRSS0004AE-A	LDPAK(L) / LDPAK(L)V	1.40g

Unit: mm



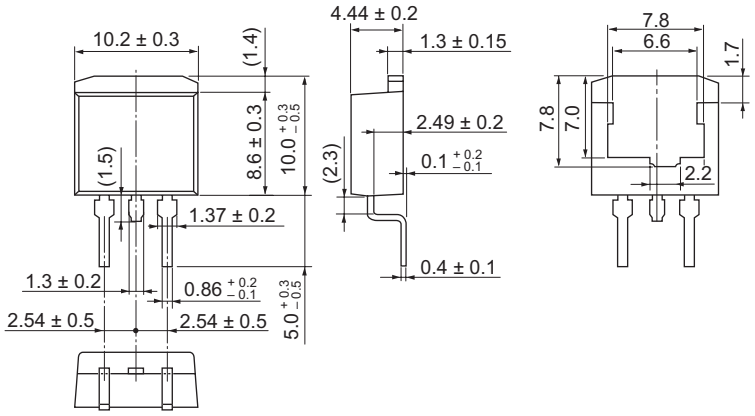
Package Name	JEITA Package Code	RENESAS Code	Previous Code	MASS[Typ.]
LDPAK(S)-(1)	SC-83	PRSS0004AE-B	LDPAK(S)-(1) / LDPAK(S)-(1)V	1.30g

Unit: mm



Package Name	JEITA Package Code	RENESAS Code	Previous Code	MASS[Typ.]
LDPAK(S)-(2)	—	PRSS0004AE-C	LDPAK(S)-(2) / LDPAK(S)-(2)V	1.35g

Unit: mm



Ordering Information

Part Name	Quantity	Shipping Container
H5N2001LD-E	500 pcs	Box (Conductive Sack)
H5N2001LSTL-E	1000 pcs	Taping
H5N2001LMTL-E	1000 pcs	Taping

Note: For some grades, production may be terminated. Please contact the Renesas sales office to check the state of production before ordering the product.

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