Old Company Name in Catalogs and Other Documents

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Renesas Electronics website: http://www.renesas.com

April 1st, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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H5N2505DL, H5N2505DS

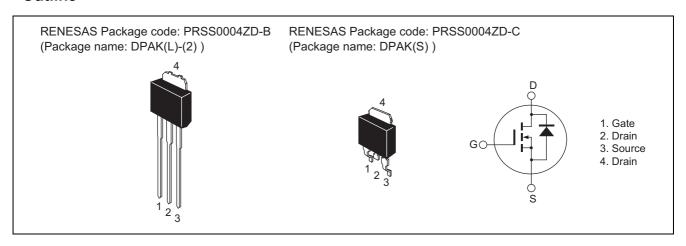
Silicon N Channel MOS FET High Speed Power Switching

REJ03G1107-0300 Rev.3.00 Oct 16, 2006

Features

- Low on-resistance
- Low drive current
- High speed switching
- Low gate change
- Avalanche ratings

Outline



Absolute Maximum Ratings

 $(Ta = 25^{\circ}C)$

Item	Symbol	Value	Unit
Drain to source voltage	V _{DSS}	250	V
Gate to source voltage	V _{GSS}	±30	V
Drain current	I _D	5	Α
Drain peak current	I _{D (pulse)} Note 1	20	Α
Body-drain diode reverse drain current	I _{DR}	5	Α
Body-drain diode reverse drain peak current	I _{DR (pulse)} Note 1	20	Α
Avalanche current	I _{AP} Note 3	5	A
Channel dissipation	Pch Note 2	25	W
Channel to case thermal Impedance	θ ch-c	5	°C/W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Notes: 1. PW \leq 10 μ s, duty cycle \leq 1%

2. Value at Tc = 25°C

3. STch = 25° C, Tch $\leq 150^{\circ}$ C

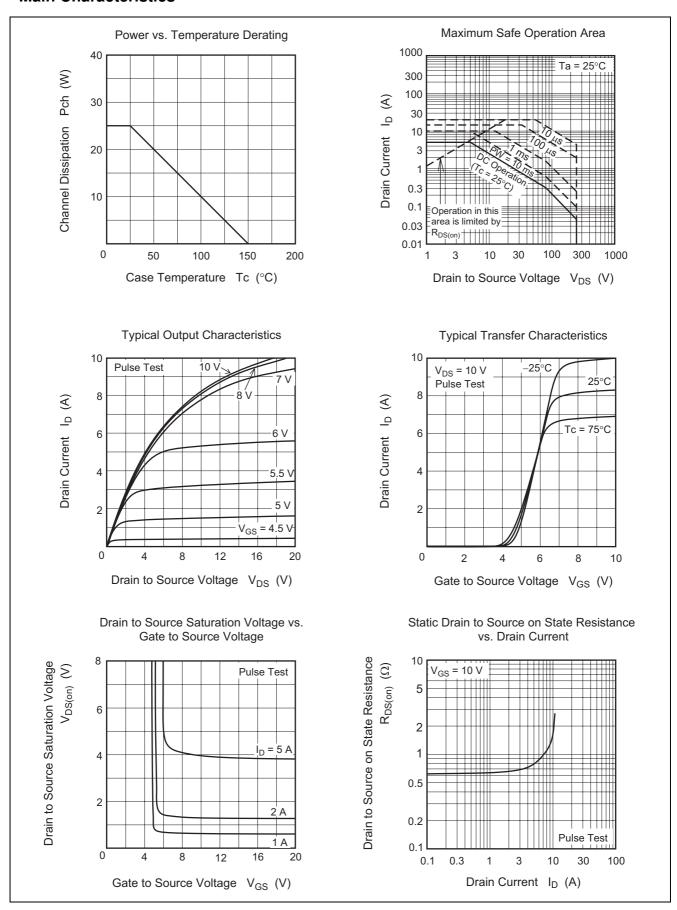
Electrical Characteristics

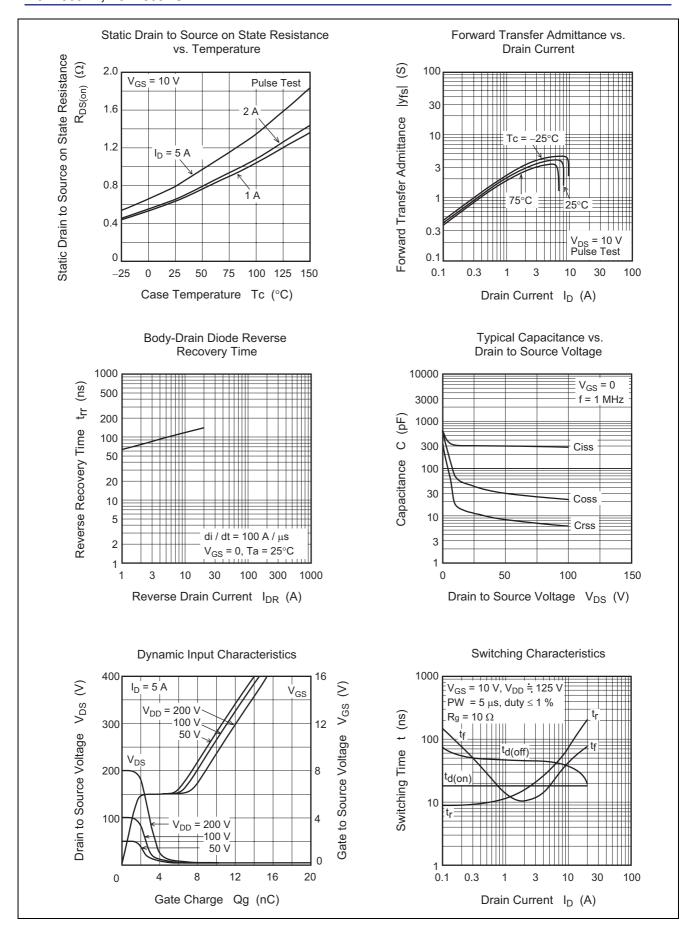
 $(Ta = 25^{\circ}C)$

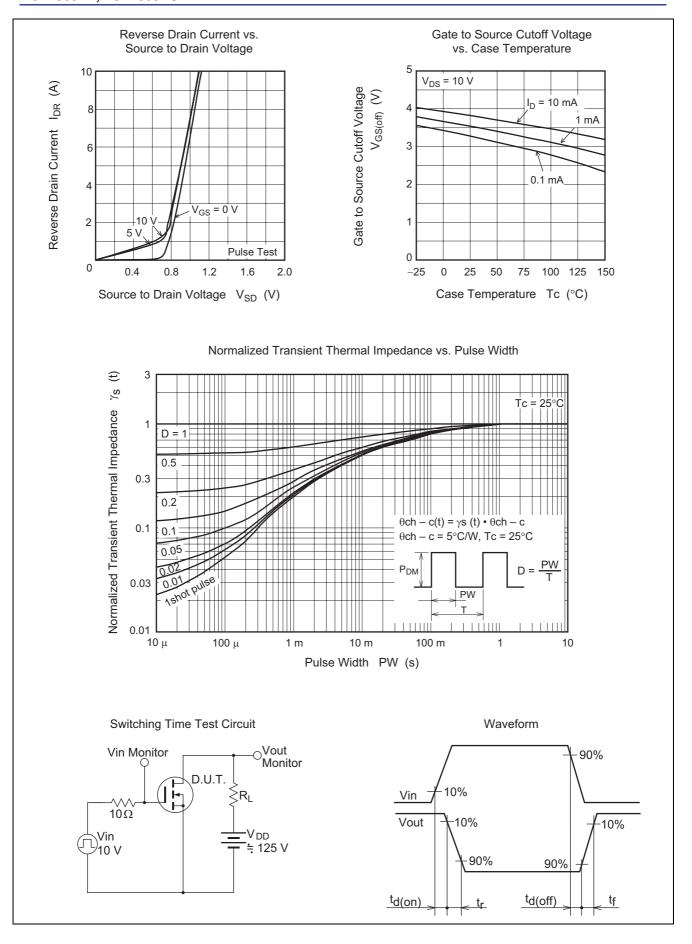
Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to source breakdown voltage	V _{(BR) DSS}	250		_	V	$I_D = 10 \text{ mA}, V_{GS} = 0$
Zero gate voltage drain current	I _{DSS}			1	μΑ	$V_{DS} = 250 \text{ V}, V_{GS} = 0$
Gate to source leak current	I _{GSS}			±0.1	μΑ	$V_{GS} = \pm 30 \text{ V}, V_{DS} = 0$
Gate to source cutoff voltage	V _{GS (off)}	3.0		4.5	V	$V_{DS} = 10 \text{ V}, I_{D} = 1 \text{ mA}$
Forward transfer admittance	y _{fs}	2.0	3.3	_	S	$I_D = 2.5 \text{ A}, V_{DS} = 10 \text{ V}^{\text{Note 4}}$
Static drain to source on state resistance	R _{DS (on)}		0.68	0.89	Ω	$I_D = 2.5 \text{ A}, V_{GS} = 10 \text{ V}^{\text{Note 4}}$
Input capacitance	Ciss	_	300	_	pF	$V_{DS} = 25 \text{ V}, V_{GS} = 0,$
Output capacitance	Coss	_	42	_	pF	f = 1 MHz
Reverse transfer capacitance	Crss	_	11	_	pF	
Total gate charge	Qg	_	11	_	nC	$V_{DD} = 200 \text{ V}, V_{GS} = 10 \text{ V},$
Gate to source charge	Qgs	_	2	_	nC	$I_D = 5 A$
Gate to drain charge	Qgd		5		nC	
Turn-on delay time	t _{d (on)}	_	18	_	ns	$V_{DD}\cong 125~V,~I_D=2.5~A,$
Rise time	t _r	_	18	_	ns	V _{GS} = 10 V
Turn-off delay time	t _{d (off)}	_	44	_	ns	$R_L = 50 \Omega$, $Rg = 10 \Omega$
Fall time	t _f	_	11	_	ns	
Body-drain diode forward voltage	V_{DF}	_	1.0	1.5	V	$I_F = 5 \text{ A}, V_{GS} = 0^{\text{Note 4}}$
Body-drain diode reverse recovery time	t _{rr}	_	100	_	ns	$I_F = 5 A, V_{GS} = 0$
Body-drain diode reverse recovery charge	Q_{rr}	_	0.32		μC	di _F /dt = 100 A/μs

Note: 4. Pulse test

Main Characteristics

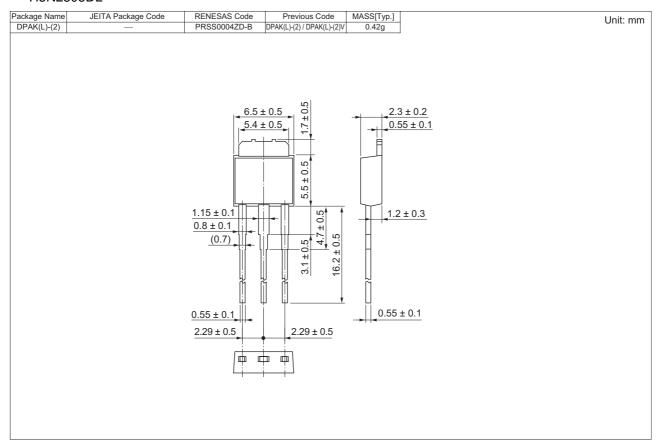




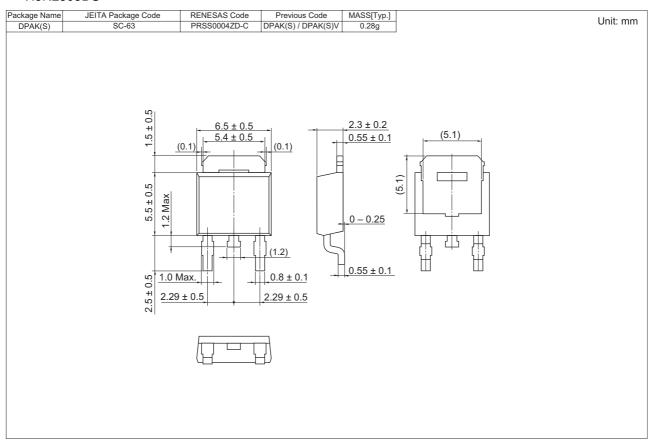


Package Dimensions

H5N2505DL



H5N2505DS



Ordering Information

Part Name	Quantity	Shipping Container
H5N2505DL-E	3200 pcs	Box (Sack)
H5N2505DSTL-E	3000 pcs	Taping

Note: For some grades, production may be terminated. Please contact the Renesas sales office to check the state of production before ordering the product.

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