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Renesas Electronics website: http://www.renesas.com

April 1st, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (<u>http://www.renesas.com</u>)

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RENESAS

H5N5006DL, H5N5006DS

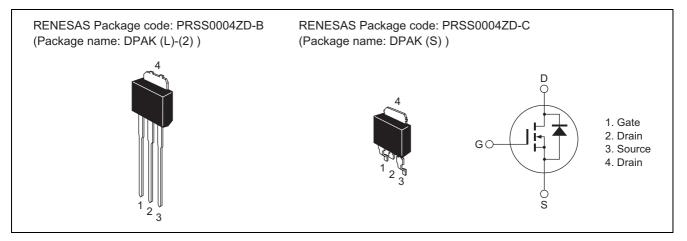
Silicon N Channel MOS FET High Speed Power Switching

> REJ03G0397-0100 Rev.1.00 May 30, 2006

Features

- Low on-resistance: $R_{DS(on)} = 2.5 \Omega$ typ.
- Low leakage current: $I_{DSS} = 1 \ \mu A \ max. \ (at \ V_{DS} = 500 \ V)$
- High speed switching: $t_f = 15$ ns typ. (at $V_{GS} = 10$ V, $V_{DD} \cong 250$ V, $I_D = 1.5$ A)
- Low gate charge: Qg = 14 nC typ. (at $V_{DD} = 400 \text{ V}$, $V_{GS} = 10 \text{ V}$, $I_D = 3 \text{ A}$)
- Avalanche ratings

Outline



Absolute Maximum Ratings

		$(Ta = 25^{\circ}C)$
Symbol	Ratings	Unit
V _{DSS}	500	V
V _{GSS}	±30	V
ID	3	А
I _{D (pulse)} Note1	12	А
I _{DR}	3	А
I _{DR (pulse)} Note1	12	А
I _{AP} ^{Note3}	3	А
Pch ^{Note2}	30	W
θch-c	4.17	°C/W
Tch	150	٥°
Tstg	-55 to +150	٥°
	V _{DSS} V _{GSS} I _D Note1 I _{DR} Note1 I _{DR} (pulse) I _{AP} ^{Note3} Pch ^{Note2} Øch-c Tch	$\begin{tabular}{ c c c c c c c c } \hline V_{DSS} & 500 \\ \hline V_{GSS} & \pm 30 \\ \hline I_D & 3 \\ \hline I_D & 3 \\ \hline I_D & 12 \\ \hline I_{DR} & 3 \\ \hline I_{DR} & 3 \\ \hline I_{DR} & 0000 \\ \hline I_{AP}^{Note1} & 12 \\ \hline I_{AP}^{Note3} & 3 \\ \hline Pch^{Note2} & 30 \\ \hline \thetach-c & 4.17 \\ \hline Tch & 150 \\ \hline \end{tabular}$

Notes: 1. $PW \le 10 \ \mu s$, duty cycle $\le 1\%$

2. Value at Tc = 25°C

3. STch = 25° C, Tch $\leq 150^{\circ}$ C



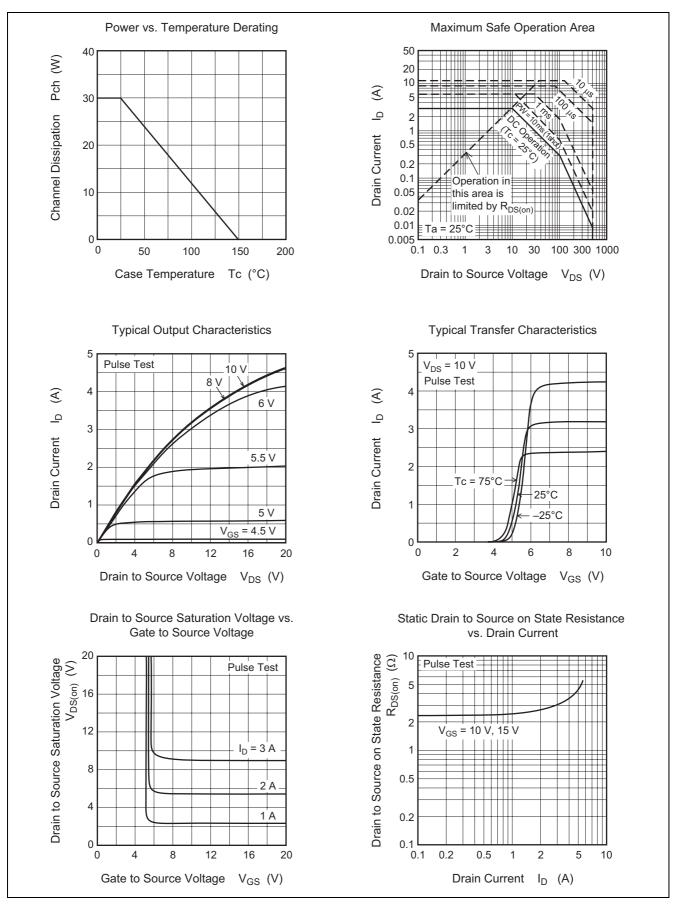
Electrical Characteristics

Item	Symbol	Min	Тур	Мах	Unit	Test conditions
Drain to source breakdown voltage	V _{(BR)DSS}	500	—	_	V	$I_{D} = 10 \text{ mA}, V_{GS} = 0$
Zero gate voltage drain current	I _{DSS}	_	—	1	μA	$V_{DS} = 500 \text{ V}, V_{GS} = 0$
Gate to source leak current	I _{GSS}	—	—	±0.1	μA	$V_{GS} = \pm 30 \text{ V}, V_{DS} = 0$
Gate to source cutoff voltage	$V_{GS(off)}$	3.0	_	4.5	V	$V_{\text{DS}} = 10 \text{ V}, \text{ I}_{\text{D}} = 1 \text{ mA}$
Forward transfer admittance	y _{fs}	1.5	2.5	—	S	$I_D = 1.5 \text{ A}, V_{DS} = 10 \text{ V}^{Note4}$
Static drain to source on state resistance	$R_{\text{DS(on)}}$	_	2.5	3.0	Ω	$I_D = 1.5 \text{ A}, V_{GS} = 10 \text{ V}^{Note4}$
Input capacitance	Ciss	_	365	—	pF	V _{DS} = 25 V
Output capacitance	Coss	_	35	—	pF	V _{GS} = 0 f = 1 MHz
Reverse transfer capacitance	Crss		8	—	pF	
Turn-on delay time	t _{d(on)}		20	—	ns	$V_{DD}\cong 250~V,~I_D=1.5~A$
Rise time	tr	_	12	—	ns	V _{GS} = 10 V
Turn-off delay time	t _{d(off)}	—	48	—	ns	$R_L = 167 \Omega$
Fall time	t _f	—	15	—	ns	Rg = 10 Ω
Total gate charge	Qg	—	14	—	nC	V _{DD} = 400 V
Gate to source charge	Qgs	—	2	—	nC	V _{GS} = 10 V
Gate to drain charge	Qgd	_	8	—	nC	$I_D = 3 A$
Body-drain diode forward voltage	V _{DF}	_	0.85	1.3	V	$I_F = 3 \text{ A}, V_{GS} = 0^{Note4}$
Body-drain diode reverse recovery time	t _{rr}	—	270	—	ns	$I_F = 3 \text{ A}, V_{GS} = 0$ di _F /dt = 100 A/µs
Body-drain diode reverse recovery charge	Q _{rr}	—	0.8	—	μC	

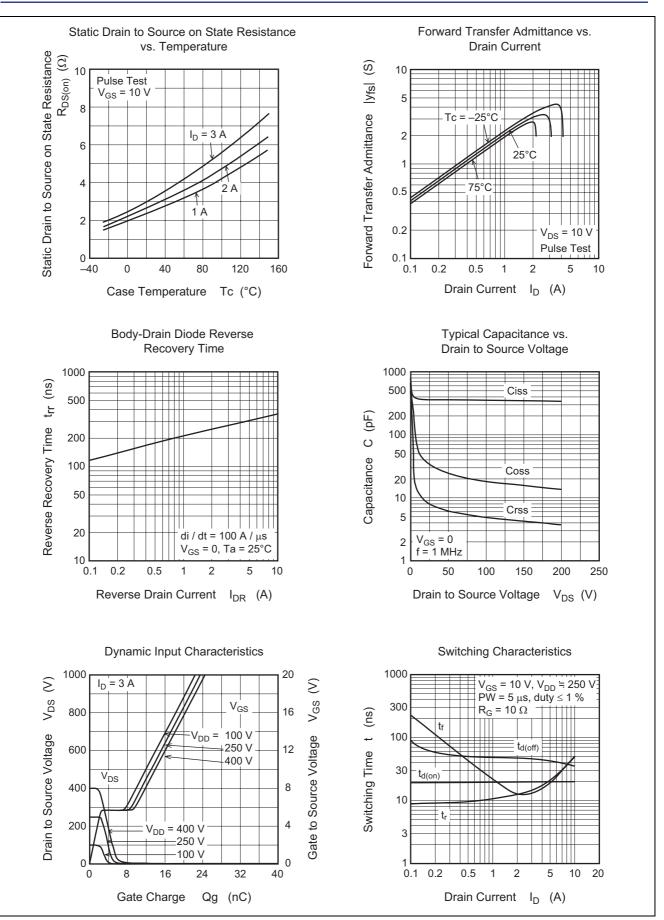
Notes: 4. Pulse test



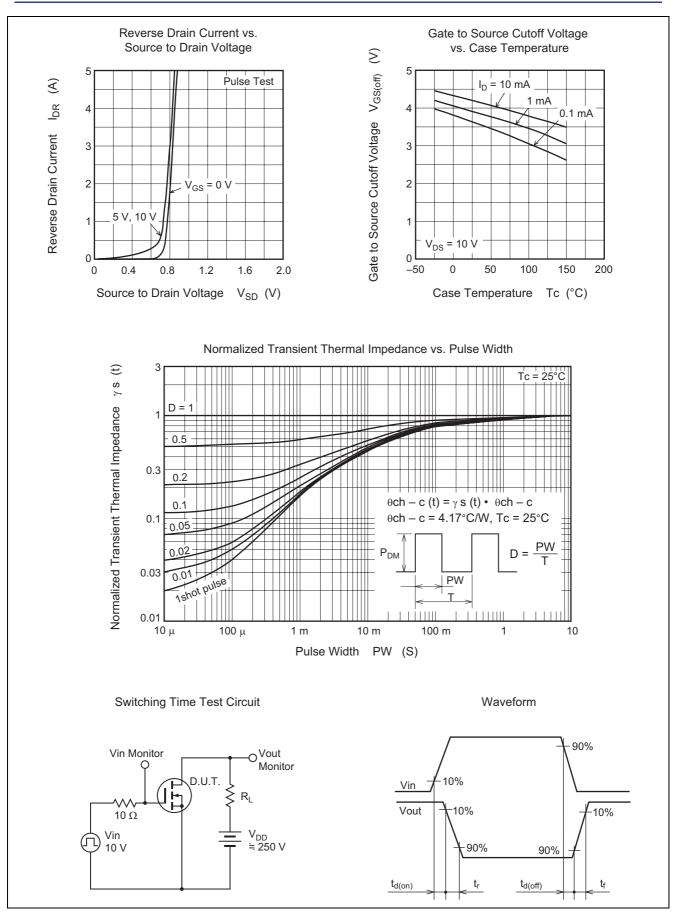
Main Characteristics







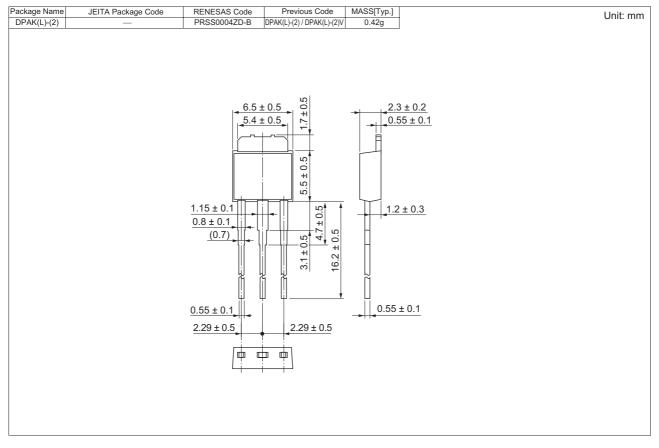




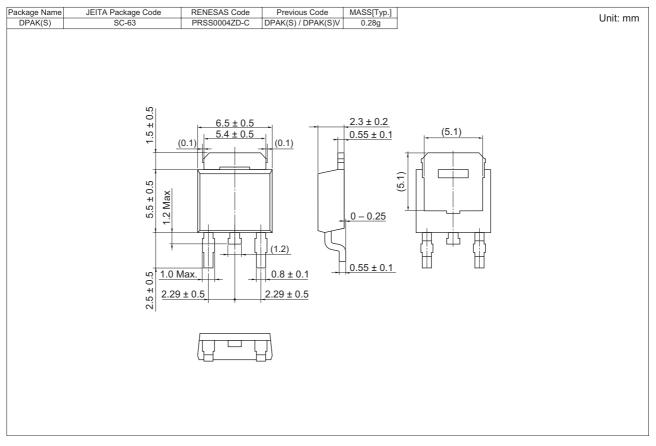


Package Dimensions

• H5N5006DL



• H5N5006DS





Ordering Information

Part Name	Quantity	Shipping Container		
H5N5006DL-E	3200 pcs	Box (Sack)		
H5N5006DSTL-E	3000 pcs	Taping		

Note: For some grades, production may be terminated. Please contact the Renesas sales office to check the state of production before ordering the product.



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